Large zero bias peaks and dips in a four-terminal thin InAs-Al nanowire device

Huading Song⁽⁰⁾,^{1,2,*} Zitong Zhang,^{1,*} Dong Pan⁽⁰⁾,^{3,*} Donghao Liu,¹ Zhaoyu Wang,¹ Zhan Cao⁽⁰⁾,² Lei Liu,³ Lianjun Wen,³ Dunyuan Liao,³ Ran Zhuo,³ Dong E. Liu,^{1,2,4} Runan Shang,^{2,†} Jianhua Zhao,^{3,‡} and Hao Zhang⁽⁰⁾,^{2,4,§}

Duryuan Elao, Kan Zhuo, Dong L. Elu, Kunan Shang, Stannia Zhao, and Hao Zhang

¹State Key Laboratory of Low Dimensional Quantum Physics, Department of Physics, Tsinghua University, Beijing 100084, China ²Beijing Academy of Quantum Information Sciences, Beijing 100193, China

³State Key Laboratory of Superlattices and Microstructures, Institute of Semiconductors, Chinese Academy of Sciences,

P. O. Box 912, Beijing 100083, China

⁴Frontier Science Center for Quantum Information, Beijing 100084, China

(Received 26 July 2021; revised 14 August 2022; accepted 6 September 2022; published 23 September 2022)

We report electron transport studies of a thin InAs-Al hybrid semiconductor-superconductor nanowire device using a four-terminal design. Compared to previous studies, thinner InAs nanowire (diameter less than 40 nm) is expected to reach fewer subband regime. The four-terminal device design excludes electrode contact resistance, an unknown value, which has inevitably affected previously reported device conductance. Using tunneling spectroscopy, we find large zero bias peaks (ZBPs) in differential conductance on the order of $2e^2/h$. At specific gate voltage settings, we find a magnetic-field-driven transition between a zero bias peak and a zero bias dip while the zero-bias conductance sticks close to $2e^2/h$. We discuss a topologically trivial interpretation involving disorder, smooth potential variation and quasi-Majorana zero modes.

DOI: 10.1103/PhysRevResearch.4.033235

I. INTRODUCTION

The decade-long hunting of Majorana zero modes (MZMs) [1,2] in hybrid semiconductor-superconductor nanowires is guided by a simple and elegant theory in 2010 [3,4]. This theory requires four basic ingredients: a one-dimensional electron system, strong spin-orbit interaction, s-wave superconducting pairing, and Zeeman energy. InAs and InSb semiconductor nanowires coupled to a superconductor are the most exhaustively studied material systems to engineer these four ingredients into a single device, aiming for the realization of MZMs [5,6]. Indeed, every single ingredient could be directly or indirectly probed by electron transport experiments and thus confirmed to be present in those devices. For example, (quasi-) one dimensionality could be revealed by the observation of quantum point contact (QPC)-like quantized conductance plateaus [7,8], a hallmark of ballistic one dimensional electron system; induced superconductivity could be probed by tunneling conductance, which resolves a hard induced superconducting gap [9-11]; Zeeman energy is related to various estimations of effective g factors [12,13]; spin-orbit coupling could be indirectly probed by anti-crossings of Andreev levels as well as the anisotropic closing of the superconducting gap for different magnetic field directions [13,14]. Majorana theory further predicts a quantized tunneling zero-bias conductance peak [15–18]. Initial experiments have measured various zero bias peaks (ZBPs) in hybrid nanowires but with a small peak height [19-23]. These first-generation experiments have suffered from finite subgap tunneling conductance, the soft gap problem. Theory later suggested that the soft gap is due to disorder at the superconductor-semiconductor interface, which leads to spatially nonuniform couplings [24]. In the following years, much experimental efforts have focused on the optimization of material growth and device control, trying to minimize the disorder level at those interfaces [25]. Indeed, epitaxial growth of superconductors on semiconductor nanowires show a hard induced superconducting gap and cleaner ZBPs [26]. Later on, large ZBPs with peak height reaching the order of $2e^2/h$ was also observed [27-29]. However, ZBPs with height robustly sticking to the quantized value by varying both magnetic field and gate voltages, as predicted by MZM theory, have not been demonstrated yet.

Meanwhile, new theory developments have introduced the concept of quasi-Majorana zero modes (quasi-MZM) [30,31], a type of zero-energy Andreev bound states (ABS) [32–36] with a topologically trivial origin. The key idea is that every ABS could be mathematically decomposed into two quasi-MZMs. In some regimes with disorder or smooth potential variation [32,37,38], the tunneling probe only couples to the first quasi-MZM. The second quasi-MZM, while partially overlapping with the first one in space (thus topologically trivial), has negligible coupling to the first one due to opposite spin. Therefore, with only one quasi-MZM contributing to tunneling, ZBPs can be quantized, mimicking topological MZMs.

On the device part, previous ZBP experiments used twoterminal device designs where the electrode contact resistance

^{*}These authors contributed equally to this work.

[†]shangrn@baqis.ac.cn

^{*}jhzhao@semi.ac.cn

[§]hzquantum@mail.tsinghua.edu.cn

Published by the American Physical Society under the terms of the Creative Commons Attribution 4.0 International license. Further distribution of this work must maintain attribution to the author(s) and the published article's title, journal citation, and DOI.

remains unknown. This unknown resistance introduces a systematic uncertainty, and if used as a fitting parameter, nonquantized ZBPs could be fitted to the quantized value [39]. Improving device fabrication for lower contact resistance could reduce this uncertainty to some extent. For example, one could estimate the upper bound of this resistance based on QPC plateaus in ballistic devices, or properties of the superconducting gap, which should remain as a constant by varying the tunnel barrier height [28]. If the contact resistance is under- or overestimated, the gap size, extracted from bias voltage, would vary after subtracting the bias drop shared by this inaccurate contact resistance. In addition, if the contact resistance is not an unknown constant but depends on bias, gate voltage or magnetic field, the ZBP height and shape will be affected in a more complex way. To solve these uncertainty problems, here we use a four-terminal device design for ZBP experiments aiming for quantized ZBPs.

Another improvement compared to previous papers is the InAs nanowire diameter, which is ~ 40 nm or below, much thinner than those commonly used in literature (typically \sim 100 nm). The motivation is twofold. First, the simplest MZM model assumes a one-dimensional electron system, i.e., single subband occupation. However, previous InAs/InSb devices likely have multiple subbands occupied in the nanowire region underneath the superconductor. The top subband, which hosts MZMs, usually has a much smaller coupling to the normal probe than the lower subbands due to smooth barrier potential [40]. This small tunnel coupling of MZM leads to a narrow ZBP whose quantized height (at zero temperature) could easily be destroyed by thermal averaging [41]. Therefore, fewer or ultimately single subband occupation is preferred for observing quantized ZBPs. To reach this regime, we reduce the wire diameter by growth. Though occupation number can not be directly probed, a lower number of occupied subbands is expected due to the small diameter, which is already comparable to InAs band bending size [42], and thus enhancing the subband energy spacing. The second motivation is material quality. Thick InAs nanowires (diameter larger than 50 nm) often exhibit randomly distributed twin defects and stacking faults [43-45], uncontrolled sources of disorder. Since disorder is currently the biggest obstacle in Majorana devices [46–48], thinner InAs nanowires with purephase crystal structure could suppress this type of disorder. We do note that other disorder sources, e.g., InAs/Al surface oxides and gate/dielectric imperfections, are still present, which remains as a future task.

II. EXPERIMENT AND DISCUSSION

Figure 1(a) shows a scanning electron micrograph (SEM) of the InAs-Al nanowire device. Growth details can be found in Ref. [49]. N1, N2 and S1, S2 label the four contact electrodes on the normal part of the nanowire and the super-conducting part, respectively. The upper side gate is labeled as TG (tunnel gate) for tunnel barrier tuning. The lower side gate and global back gate are labeled as SG and BG, respectively.

Figure 1(b) shows the device schematic with dimensions labeled together with a brief measurement circuit (see Fig. 10 in Appendix for details). A DC bias voltage and a small AC lock-in excitation are first summed and then applied together



FIG. 1. (a) False-color SEM of the device. Four normal electrodes (N1, N2, S1, and S2) and two side gates (TG and SG) are Ti/Au (5/70 nm). The InAs nanowire (gray) is partially covered by a thin Al shell (pink, thickness \sim 7 nm). Part of the Al shell was wet etched for contacts and tunnel barrier. (b) Device and measurement circuit schematic, with dimensions (not in scale) labeled. The substrate (brown) is p+ Si covered by 300-nm-thick SiO₂ (blue), acting as a global back gate (BG). (c) dI/dV vs bias voltage V and tunnel gate voltage V_{TG} at zero magnetic field. V_{BG} = -2.251 V. [(d),(e)] Vertical line-cut from (c) (black bar) in linear (d) and logarithmic scale (e).

to the N1 contact. The resulting current is drained from the S1 contact and measured through a preamplifier, as I and dI. The voltage drop between N2 and S2 is measured using a voltage meter and another lock-in (synchronized with the first one) to get V and dV. Therefore, the differential conductance dI/dV can be directly calculated without subtracting any series resistance (e.g., fridge filters or contacts) as was done before for two-terminal designs. In addition, the bias V can also be directly measured without subtracting the bias drop over series resistance as in the two-terminal case.

Figure 1(c) shows dI/dV as a function of V and V_{TG} , resolving a hard induced superconducting gap in the tunneling regime [Figs. 1(d) and 1(e)] where the outside-gap conductance is much smaller than $2e^2/h$. The gap size is ~0.27 meV.



FIG. 2. (a) dI/dV vs V and B at fixed V_{TG} and V_{BG} (see labeling). B is aligned with the nanowire axis for all measurements. (b) Extracted zero-bias (upper panel) and outside-gap conductance G_N (lower panel). Blue and red backgrounds in the lower panel mark the B ranges of zero bias dip (ZBD) and ZBP. Blue and red lines in the upper panel mark the B range where the conductance is close to $2e^2/h$. The full width at half maximums (FWHM) of ZBPs are shown as red crosses. (c) dI/dV line-cuts from 0.48 T to 1.06 T, resolving a transition from ZBD (blue) to ZBP (red) near $2e^2/h$. (d) ZBP line-cuts (red) at 0.9, 1, and 1.24 T, together with Lorentzian line shapes (gray) assuming a thermal broadening of 50 mK. Vertical-red-dashed lines mark the bias positions of "half maximum". (e) Several dI/dV line-cuts (B labeled).

Fridge base temperature is ~ 20 mK for all measurements. The gap remains hard at finite magnetic field *B* before its closing, see Fig. 11 in Appendix, for *B* dependence and more gate scans. The super gate (SG) is not well functional (see Fig. 12 in Appendix for details), thus fixed at 0 V for all measurements unless specified. The hard gap resolved by tunneling conductance in the gate-*B* parameter space (where large ZBPs are observed) is an important prerequisite for the search of quantized ZBPs: in soft gap devices [39], Majorana ZBPs are not expected to be quantized due to severe dissipation broadening [50]. Although no quantum dots are revealed in Fig. 1(c), dots or multiple dots can easily form at other gate voltage settings as will be shown later.

Next we apply *B* to search for possible MZM signatures at different gate voltages (see Fig. 13 in Appendix, unsuccessful searches). *B* direction is aligned with the nanowire axis throughout the measurement to minimize orbital effect of the Al film. Thinner nanowires are preferred for *B* rotation experiments [51]. Figure 2(a) shows a *B*-scan example at a particular gate voltage setting (labeled in the figure) with the zero-bias line-cut and G_N shown in Fig. 2(b). As *B* increases, two broad levels detach from the gap edges and merge at zero energy at ~0.5 T. They first form a zero bias dip (ZBD), which later on evolves into a ZBP. The zero-bias conductance, during this dip-to-peak transition (from 0.48 T to 1.06 T), sticks close to $2e^2/h$. The mean value of the zero-bias conductance within this *B* range of 0.58 T [blue and red lines in Fig. 2(b)], is 0.98 with a standard deviation of 0.02, both in unit of $2e^2/h$. All the dI/dV line-cuts within this *B* range are shown in Fig. 2(c) (blue for ZBDs and red for ZBPs). The smooth ZBD-ZBP crossover in *B* scan where the zero-bias conductance sticks close to $2e^2/h$ is a new observation of this paper.

For *B* higher than 1.06 T, the ZBP-height quickly decreases away from $2e^2/h$, and finally the peak splits (line-cuts shown in Fig. 14 in Appendix). The lower panel of Fig. 2(b) marks the full B range of ZBP with a red background, where its difference with the red line in the upper panel indicates the B range of ZBP whose height significantly decreases away from $2e^2/h$. Figure 2(d) shows three ZBP line-cuts with the full width at half maximum (FWHM) indicated by the reddashed lines. FWHM is extracted by the bias V where dI/dVis half of its zero-bias conductance. For some cases [e.g., 0.9 T in Fig. 2(d), the background conductance in the positive bias region is larger than the "half maximum". FWHM is then taken by doubling the |V| found in the negative bias region. If the background conductance is larger than the "half maximum" for the entire bias range, then no FWHM is extracted. The gray lines are calculated Lorentzian line shapes



FIG. 3. Numerical simulation. (a) Nanowire schematic (upper), potential landscape (middle) and two quasi-MZM wavefunctions at $V_z = 1.3$ meV (lower). (b) Energy spectrum. (c) dI/dV of the energy spectrum (middle), zero energy (bias) line-cut (lower), and V_z -dependent barrier height V_b (upper). (d) Line-cuts from (c), quantized ZBD-ZBP transition.

of $G_0/(1 + (eV/\Gamma)^2)$ after assuming a thermal broadening of 50 mK, showing a rough match with the ZBPs. G_0 is the zerobias conductance while $2\Gamma/e$ the extracted FWHM, shown as red crosses in Fig. 2(b).

The blue line in Fig. 2(b) indicates the *B* range of ZBD with zero-bias conductance sticking close to $2e^2/h$. We note that line-cuts at lower *B* (e.g., 0 T) also have line-shapes of "ZBD". This dip, a suppression by the superconducting gap, is however different from the ZBDs we quoted in Figs. 2(b) and 2(c), a result of two merging levels.

The outside-gap conductance G_N shows a decreasing trend in Fig. 2(b), suggesting a *B*-dependent barrier height since G_N is proportional to the barrier transmission. However, our G_N is extracted by averaging the conductance for the most positive and negative bias voltages available from the data. The available bias range [the color map edge in Fig. 2(a)] is not too far away from the gap edge, leading to an overestimation of G_N , especially at lower B. For a more accurate estimation, we use ZBP-width as the indicator of barrier transmission in the *B* range with no obvious peak splitting. In Figs. 2(b) and 2(c), the FWHM decreases as increasing *B* (from 0.86 T to 1.06 T), indicating that the barrier height (transmission) possibly increases (decreases). Within this B range, the ZBP-width varies by \sim 50%, much larger than the variation of ZBP-height: $\sim 5\%$ near $2e^2/h$. The relative variation of ZBP-height near $2e^2/h$ being significantly smaller than the relative variation of ZBP-width was used before to argue for a quantized ZBP [28]. For higher B, the FWHM starts to increase while the



FIG. 4. (a) dI/dV vs V and B at a different V_{TG} and V_{BG} (see labeling) with the zero-bias line-cut, extracted G_N and FWHM (red crosses) shown in the lower panels. The right y axis (red) is for FWHM in unit of mV. (b) Line-cuts of ZBD and ZBP near $2e^2/h$ with the B ranges labeled. [(c),(d)] and [(e),(f)] same with [(a),(b)] but different in gate voltage settings (labeled). [(g),(h),(i)] Three more B scans with maximum ZBP heights (g) close to, (h) above, and (i) below $2e^2/h$ (gate voltages labeled). Lower panels show zero-bias line-cuts. In (g), the ZBP first splits, then merges back at $B \sim 2$ T.



FIG. 5. (a) dI/dV vs V_{BG} and B (left panel) with V = 0, $V_{TG} = -3.544$ V. Right panel: Replot of the left panel using three colors: white, cyan, and red for conductance less than $0.9 \times 2e^2/h$, larger than $1.1 \times 2e^2/h$ and in between. (b) Upper (lower) panel, two horizontal (vertical) line-cuts from (a), indicated by the green (orange) dashed lines, see labeling for V_{BG} (B). (c) Upper panel, bias dependence of the black curve in the upper panel of (b). Middle panel, zero-bias line-cut. Lower panel, vertical line-cuts at 0, 0.05, 0.1, and 0.15 T, respectively. (d) dI/dV vs V_{TG} and B (left panel) with V = 0, $V_{BG} = -2.198$ V. Right panel, three-color replot. (e) Upper (lower) panel, two horizontal (vertical) line-cuts from (d), indicated by the green (orange) dashed lines, see labeling V_{TG} (B). (f) Horizontal line-cut from (d) at $V_{TG} = -3.588$ V. (g) dI/dV line-cuts [from Fig. 6(d), the leftmost part] near the V_{TG} setting of (f) at B = 1 T.

ZBP height shows a continuous decrease. Both are likely due to peak splitting, which becomes visible where the splitting is large enough to be resolved, e.g., see the 1.6 T line-cut in Fig. 2(e).

MZM theory predicts a quantized zero-bias conductance at zero temperature. Whether this resolves a ZBP or ZBD depends on the barrier height. For a high barrier (therefore low transmission, tunneling regime), the subgap conductance is suppressed and a quantized ZBP can be resolved. For a low barrier with high total transmission (e.g., more than one subband occupation in the barrier), the subgap conductance can be enhanced due to Andreev reflection and exceeds $2e^2/h$. Meanwhile, the zero-bias conductance, due to spin-filtering of MZMs, still stays at $2e^2/h$ and a quantized ZBD can be resolved [18,31,52]. Theory has proposed using the quantized ZBP-ZBD transition, enabled by lowering the barrier height, as an experimental tool to exclude a case of quasi-MZMs from topological MZMs [31,52]. In realistic situations, exact MZM quantization is not expected due to finite temperature and wire length. For a quantized ZBP with FWHM of 0.1 mV, thermal broadening of 50 mK can already decrease the peak height by $\sim 2\%$ of $2e^2/h$. This height will decrease more for narrower ZBPs. To minimize temperature effect, searching for quantized MZM peak requires a large peak width, thus large barrier transmission (high G_N). In fact, G_N of the ZBPs in Fig. 2(c) is close to $2e^2/h$, suggesting that the barrier is in open regime (instead of tunneling) with more than one spin-resolved subband occupied. In this open regime, the subgap conductance is finite due to Andreev reflection [53], resulting in a sizable background conductance for the ZBPs. This finite (Andreev) subgap conductance, superficially similar to a soft gap, does not "hurt" the quantized height of MZM peak [54], different from the true soft gap case, which destroys the quantization due to dissipation broadening [50]. To confirm its "hard gap" nature, we tune V_{TG} into tunneling regime where dI/dV indeed resolves a hard gap at zero B [Fig. 1(c)] and finite B (Fig. 11) for $V_{BG} = -2.251$ V, same with the V_{BG} value in Fig. 2. Overall, our ZBP is large and different from the ZBPs with small net height [39] because 1) the zero-bias conductance is close to $2e^2/h$, and 2) the ZBP's net height (above background) is also large and can exceed e^2/h . Another non-negligible effect in realistic devices is the finite wire length. In fact, our device has a relatively short length \sim 658 nm for the superconducting part [Fig. 1(b)]. As a result, MZMs could easily overlap, which can further degrade the quantization quality [55].

The discussion above serves as a background introduction and by no means suggests our observation as topological



FIG. 6. (a) dI/dV vs V_{BG} and V_{TG} at V = 0. B = 0 T. Lower panel, horizontal line-cut at $V_{BG} = -2.198$ V. (b) Same with (a) except that B = 1 T. (c) Bias dependence of the line-cut in (a). Lower panel, zero-bias line-cut. (d) Same with (b) except that B = 1 T. (e) Several vertical line-cuts from (c) (labeled with colored arrows) showing no obvious ZBPs. (f) Line-cuts from (d) with V_{TG} ranges labeled. For clarity, three panels and colors are used.

MZMs. In Fig. 3 we study an alternative explanation involving quasi-MZMs, solely for a qualitative illustration purpose rather than quantitative comparisons since many experimental parameters are unknown. Considering the nanowire tapering, we assume a tilted potential landscape shown in Fig. 3(a)(black curve). We note this is not the only possible landscape, e.g., Fig. 15 in Appendix shows another case (triangle), which gives similar results. Figure 3(b) shows the energy spectrum where the zero-energy state at finite Zeeman energy V_z is a pair of quasi-MZMs. Figure 3(a) plots the wavefunctions of these two quasi-MZMs (red γ_1 and blue γ_2), located where the chemical potential crosses the Zeeman split potential landscapes (red and blue lines). Although γ_1 and γ_2 are spatially separated, they are not at the wire ends, thus topologically trivial. Due to the separation, γ_1 has a much stronger coupling (Γ_1) to the probe than γ_2 (Γ_2 , almost negligible), leading to quantized conductance as shown in Figs. 3(c) and 3(d). For dI/dV calculation, we assume a very narrow and high barrier [vertical black line in Fig. 3(a)]. We further assume that the barrier height is V_z dependent [Fig. 3(c) upper]: slightly decreases first, then increases, trying to capture the B-dependent FWHM of ZBPs and the gap shape at 0 T in Fig. 2. The physics mechanism of this assumption is not fully clear and might possibly be related to *B*-induced suppression of Andreev reflection or shifting of dot levels, both affecting the barrier transmission. With this barrier assumption, we find the numerical simulation [Figs. 3(c) and 3(d)] qualitatively consistent with our observation of dip-to-peak transition near $2e^2/h$. The peak splitting at higher *B* is not revealed in the simulation possibly due to the difference of the critical field of the bulk gap between the experiment and the theory model.

We note the interpretation above is not exclusive, e.g., landscapes with various disorder, which have been extensively studied before [46] can not be ruled out at this stage.

Figures 4(a)–4(f) show similar *B* scans at three different gate voltage settings where the zero-bias conductance stays close to $2e^2/h$, persisting over sizable *B* ranges: 0.54 T, 0.55 T, and 0.56 T, respectively. The mean and standard deviation of zero-bias conductance within these *B* ranges are, 0.95 ± 0.02, 1.01 ± 0.04, and 1.02 ± 0.04, respectively (all in unit of $2e^2/h$). The blue and red line-cuts indicate the large ZBDs and ZBPs close to $2e^2/h$. The relative variation of ZBP-width for the red line-cuts in Figs. 4(d)–4(f) is significantly larger than the relative variation of ZBP-height near $2e^2/h$, similar to Fig. 2 [Fig. 4(b) has too few data points to conclude]. For higher *B*, the ZBP height decreases away from $2e^2/h$, accompanied by an increase of FWHM in roughly similar *B* ranges [see Figs. 4(c) and 4(e)], possibly due to peak splitting. See Fig. 14 for line-cuts.

In Fig. 2 and Figs. 4(a)–4(f), we have shown smooth ZBD-ZBP transitions near $2e^2/h$ with small fluctuations, forming "plateau-like" features. Based on measurements performed so far, we have not observed similar behavior at other values significantly different from $2e^2/h$. This possibility, however,

could not be completely excluded since we did not (also can not) exhaust the entire (V, B, V_{TG} , V_{BG}) multidimensional parameter space.

In Figs. 4(g), 4(h), and 4(i), we show three more B scans where the maximum ZBP heights are (g) close to, (h) above, and (i) below $2e^2/h$. We note that there is no clear boundary between Figs. 4(g)-4(i) and the plateau-like features. Instead, we expect a smooth crossover between these behaviors tuned by gate voltages. For example, if the zero-bias conductance in Fig. 4(i) [Fig. 4(c)] was higher and more (less) flat by tuning gate, it may evolve to Fig. 2 [Fig. 4(h)]. If the B range in Fig. 4(e) was narrower, it may evolve to Fig. 4(g). Although this smooth transition may be expected in quasi-MZMs: tuning gate voltages or B may affect Γ_2 and E_M (coupling between γ_1 and γ_2), which cause deviations from $2e^2/h$, below or above, both possible [29,31]. The ZBPs exceeding $2e^2/h$ at many different gate voltages also strongly suggest the presence of disorder [56] whose detailed simulation is beyond the scope of this paper. For a full overview, we show ten more B scans in Fig. 16 in Appendix.

Next we study B scans at V = 0, see Figs. 5(a) and 5(d), the zero-bias conductance map as a function of B and V_{BG} [Fig. 5(a)] or V_{TG} [Fig. 5(d)]. Right panels are replots using only three colors to highlight the conductance regions (red) close to $2e^2/h$ within $\pm 10\%$ variation. We note that this range of 10% is subjective: smaller variations surely lead to smaller areas of "red islands" in three-color replots. The red curves in Figs. 5(b) and 5(e) are horizontal and vertical line-cuts across the red islands, resolving plateau-like features. The B scan plateaus have gate voltage settings close to Figs. 4(a) and 4(e) whose zero-bias line-cuts show similar match. The V_{BG} scan plateau at 0.84 T (lower panel b) is resolved as large ZBPs in further bias scan at a lower B (0.8 T) and slightly different V_{TG} [Fig. 8(d)]. The V_{TG} scan at 0.98 T (lower panel e) shows sizable fluctuations near $2e^2/h$. Further bias dependence of this curve at similar B (1 T) also resolves large ZBPs [Fig. 6(d)]. For comparisons, the black curves in Figs. 5(b) and 5(e) are line-cuts not passing through the red islands. See Fig. 17 in Appendix for more line-cuts of Figs. 5(a) and 5(d).

The zero bias maps only serve as a guidance in ZBP searching but does not guarantee "it is a ZBP when sweeping bias". For example, the two black curves in Fig. 5(b) show a "peak" above $2e^2/h$ in *B*- and V_{BG} scans, corresponding to the cyan region in the lower left part of Fig. 5(a) (right panel). This peak turns out not being a ZBP in further bias scan as shown in Fig. 5(c). Another example is shown in Fig. 5(f), a horizontal line-cut from Fig. 5(d), resolving a plateau feature in *B* scan at a nonquantized value of $\sim 0.3 \times 2e^2/h$. Further bias scan [Fig. 5(g)] on this plateau reveals split peaks instead of ZBPs [line-cuts taken from Fig. 6(d)].

Now we fix *B* and study the gate dependence of the large ZBPs. Figure 6(a) shows the zero-bias conductance map as a function of V_{BG} and V_{TG} at B = 0 T. We note the V_{BG} values and V_{TG} values for the *B* scans of ZBPs, i.e., Fig. 2, Fig. 4, and Fig. 16 (except for the two lower-right panels), are all within the scanned V_{BG} range and V_{TG} range as shown in Figs. 6(a) and 6(b). The several "red-line" features are likely due to states of unintentional quantum dots formed near the barrier. A horizontal line-cut (lower panel) resolves these dot states or levels as peaks in V_{TG} scan. Figure 6(c) further shows the



FIG. 7. (a) Three-color replot of Fig. 6(b), highlighting the conductance region near $2e^2/h$ (red) with $\pm 10\%$ variation. (b) $V_{\rm TG}$ scan at B = 1 T, corresponding to the green-dashed-horizontal line-cut in (a), outside the red islands. Lower panel, zero-bias line-cut. (c) $V_{\rm BG}$ scan at B = 1 T, corresponding to the orange-dashed-vertical linecut in (a). Lower panel, zero-bias line-cut (black) from the upper panel together with the vertical-dashed line-cut from (a) (orange), matching qualitatively. (d) dI/dV line-cuts from (c) within the $V_{\rm BG}$ range from -2.225 V to -2.099 V. Split peaks ($V_{\rm BG}$ labeled) and neighboring line-cuts are shown in blue for clarity.

bias dependence of this line-cut where these dot states do not reveal clear and robust ZBPs [see line-cuts in Fig. 6(e)]. At B = 1 T, in addition to the dot states as a background, continuous red islands of conductance near $2e^2/h$ are observed, see Fig. 6(b) whose V_{BG} range and V_{TG} range are almost the same as Fig. 6(a). Bias scan across this "island" resolves large ZBPs whose height oscillates around $2e^2/h$ [Fig. 6(d)], accompanied by peak splittings. Line-cuts of the ZBPs and split peaks are shown in Fig. 6(f) with corresponding V_{TG} ranges labeled. We note that Figs. 6(b) and 6(d) were measured under nominally the same gate voltage settings (V_{TG} range slightly different) with Figs. 6(a) and 6(c) but at 1 T. In addition, the lower panels of Figs. 6(a)-6(d) (zero-bias line-cuts) were measured at the same V_{BG} value. Therefore, comparing these four line-cuts allows to identify the sharp "jump-like" features and to what extent they can be reproduced upon remeasuring. For example, most of the oscillating features near $2e^2/h$ in the lower panel of Fig. 6(d) are reproducible based on the comparison with the lower panel of Fig. 6(b) where matches can be found. These features, despite being reminiscent of charge jumps, are reproducible sharp resonances (possibly dot levels) tuned by gate voltages. The left most "jump" in Fig. 6(d) $(V_{\rm TG} \sim -3.57 \text{ V})$ is a nonreproducible charge jump, which is absent in Fig. 6(b). Intuitively, one would expect that V_{TG} only tunes the barrier height thus a V_{TG}-driven ZBP-ZBD transition near $2e^2/h$. This is not observed in Fig. 6(d). A possible reason is that V_{TG} also strongly tunes the dot states, which disturb the zero-energy states. Reducing disorder-induced dot states is our future goal.



FIG. 8. [(a),(b),(c)] dI/dV at zero bias as a function of V_{BG} and V_{TG} at B = 0.95 T, 0.9 T, and 0.8 T, respectively. Right panels, three-color re-plots. (d) V_{BG} scan at 0.8 T with $V_{TG} = -3.51$ V, corresponding to the orange dashed line-cut in (c). Lower panel, zero-bias line-cut. (e) dI/dV line-cuts from (d), showing the back-and-forth oscillations between ZBP and split peaks (V_{BG} labeled). (f) V_{TG} scan at 0.8 T with $V_{BG} = -2.08$ V, corresponding to the green-dashed line-cut in (c). Middle panel, zero-bias line-cut (red and blue for ZBP and ZBD regions). Lower panel, G_N : the average of conductance at the most positive and negative bias available in the data. Red and blue curves are two vertical line-cuts from the upper panel (labeled by the corresponding color bars), resolving a ZBP and a ZBD near $2e^2/h$ [horizontal (V) axis not shown].

Figure 7(a) shows the three-color replot of Fig. 6(b), highlighting the red islands as regions close to $2e^2/h$. In Fig. 6(d) we have studied a horizontal line-cut across the red islands, showing large ZBPs. For comparison, Fig. 7(b) shows bias dependence of a horizontal line-cut outside the red islands [green-dashed line in Fig. 7(a)]. No clear and robust ZBPs are observed [occasionally there are nonrobust ZBPs due to sharp level crossing like Figs. 13(b) and 13(c)]. Next, in Fig. 7(c), we study a vertical line-cut across the red islands [orange-dashed line in Fig. 7(a)] where the bias dependence also resolves large ZBPs. These large ZBPs show a plateaulike feature near $2e^2/h$ in zero-bias conductance (lower panel) for V_{BG} from -2.225 V to -2.099 V. All the dI/dV line-cuts within this quoted V_{BG} range are shown in Fig. 7(d) (split peaks and their neighboring line-cuts in blue for clarity). The mean and standard deviation of the zero-bias conductance for these line-cuts (including the split peaks) in Fig. 7(d) is 0.95 \pm 0.07 in unit of $2e^2/h$. The split peaks may be due to non-negligible coupling (E_M) between quasi-MZMs at that particular gate voltage setting. For lower V_{BG} values (~ -2.25 V), the ZBP-height significantly exceeds $2e^2/h$ (maximally $\sim 1.2 \times 2e^2/h$), possibly due to non-negligible coupling of the second quasi-MZM to the barrier (Γ_2) enabled by smooth potential variation or purely a disorder effect [56]. See Fig. 18 in Appendix for more V_{BG} - and V_{TG} scans of the large ZBPs at this field of 1 T and Fig. 19 in Appendix more line-cuts from Fig. 6(b).

After the extensive gate scans at B = 1 T, we now tune B to other values of 0.95 T, 0.9 T, and 0.8 T, respectively, as shown in Figs. 8(a)-8(c). The right panels are the three-color replots with red islands highlighting regions close to $2e^2/h$. Comparing Figs. 8(a)-8(c) with Fig. 6(b), we can find matches for the main features (dot states or resonances) with minor overall gate voltage drifts, possibly due to small charge jumps happened between those measurements, which reset/shift the overall gate voltage. Figure 8(d) shows bias dependence for a vertical line-cut across the red islands in Fig. 8(c) (orangedashed line), resolving large ZBPs and split peaks near $2e^2/h$. The zero-bias conductance of these ZBPs and split peaks form a plateau-like feature with conductance fluctuating around $2e^2/h$ for V_{BG} between -2.238 V and -2.046 V. The mean and standard deviation of the zero-bias conductance within this $V_{\rm BG}$ range, including both ZBPs and split peaks, is 1.00 ± 0.08 in unit of $2e^2/h$. Figure 8(e) shows several dI/dV line-traces (with V_{BG} values labeled) from Fig. 8(d), illustrating the backand-forth oscillating behavior for ZBPs vs splitting peaks.



FIG. 9. (a) dI/dV vs V_{BG} and V_{TG} at B = 0.85 T with V = 0. Right panel, three-color replot. (b) dI/dV vs V by sweeping V_{TG} and V_{BG} simultaneously [following the blue-dashed line in (a)]. Lower panel, zero-bias line-cut (black) and the blue-dashed line-cut (blue) from (a). (c) Line-cuts from (b) within the V_{TG} range from -3.446 V to -2.963 V (see labeling). For clarity, only one third of the line-cuts are plotted (one for every three neighboring curves).

Figure 8(f) shows a V_{TG} scan of the ZBPs, corresponding to a horizontal line-cut from Fig. 8(c) (green-dashed line). The red and blue colors indicate the *B* ranges for ZBPs and ZBDs, which alternate for a total V_{TG} range from -3.642 V to -3.342 V. Within this range, the mean and standard deviation of zero-bias conductance is 1.06 ± 0.09 in unit of $2e^2/h$. The transitions between ZBP and ZBD regions are accompanied by sharp and reproducible resonances, possibly due to dot levels crossing zero energy. These dot levels, tuned by gate voltages, also interfere with the zero-bias conductance, causing sizable deviations/fluctuations from/around $2e^2/h$. All the dI/dV line-cuts within the V_{BG} and V_{TG} ranges mentioned above can be found in Fig. 20 in Appendix. For more vertical and horizontal line-cuts at 0.8 T [Fig. 8(c)] as well as 1.0 T [Fig. 6(b)], see Fig. 19 in Appendix.

So far, we have presented several quantized plateau-like features for the zero-bias conductance in *B* scan [Fig. 2, Figs. 4(a)–4(f)] and gate voltage scans [Fig. 7(c), Fig. 8(d)], as well as several other nonplateau scans at different *B* and gate voltage settings. These plateau-like features have noticeable fluctuations: some smaller and some larger, but generally within $\pm 10\%$ variation of the quantized value. Although perfect quantization of MZM is also not expected in realistic devices with finite temperature and short wire length as discussed before, we believe there is still much room for improvement regarding the flatness and accuracy of plateaus based on our current data quality. We further note that plateau-like features and nonplateau features are not "black vs white" with clear and sharp boundaries, as was partially discussed before in Fig. 4. For example, our V_{TG} scans [Fig. 6(d),

Fig. 8(f)] in general show larger fluctuation amplitudes, which probably can not be identified as plateau like. But they do show large ZBPs oscillating around $2e^2/h$, different from those nonplateau features: Figure 7(b) with no robust ZBPs and Figs. 13(b) and 13(c) with small ZBPs due to sharp level crossings (thus nonrobust). Therefore, these V_{TG} scans [Fig. 6(d), Fig. 8(f)] can be treated as intermediate cases or transitions between plateau-like and nonplateau features. With future device optimization, e.g., reducing disorder, these features may develop into plateau-like or plateau features [48].

Finally, we show a gate scan at B = 0.85 T where we find ZBDs as the dominating feature. Figure 9(a) shows the zerobias conductance map as a function of V_{BG} and V_{TG} . We notice a significant charge jump between the measurement of Fig. 9 and the rest majority of the data (Fig. 2 to Fig. 8). As a result, the main features (dot states) of the zero bias map at different fields [Fig. 6(b) and Figs. 8(a)-8(c)] do not show clear match with Fig. 9(a). Therefore, the data set of Fig. 9 is isolated and can not be compared directly with the rest (Fig. 2 to Fig. 8). Nevertheless, we can still resolve red islands as shown in the right panel. The blue-dashed line (tuning V_{BG} and V_{TG} simultaneously) marks a fine-tuned cut passing through the red islands, which resolves a plateau-like feature around $2e^2/h$ for V_{TG} from -3.323 V to -2.963 V, as shown in Fig. 9(b). The mean and standard deviation of the zero-bias conductance within this gate range is 1.05 ± 0.06 in unit of $2e^2/h$. dI/dVline-cuts [Fig. 9(c)] within the plateau-like region resolve zero bias dips (ZBD) with no clear and robust ZBPs. For quantized ZBDs due to quasi-MZMs or MZMs, thermal averaging effect at finite temperature tends to increase the zero-bias conductance above $2e^2/h$, contrary to the case of quantized ZBPs. We further note the small quasiperiodic oscillations for gate sweeps in Fig. 9(b) and Fig. 9(a). Occasionally, they resolve diamond-like shapes in bias versus gate scan as shown in Fig. 9(b) with a diamond size of ~ 0.2 mV or smaller, similar to Coulomb blockades. These small quasiperiodic oscillations can also be found in Figs. 6(a) and 6(b) and Figs. 8(a)-8(d). We do not know the origin of these oscillations but speculate that it may be related to the short length of the nanowire (the superconducting part \sim 658 nm), which might be treated as an open and large quantum dot with a small charging energy.

III. SUMMARY

To summarize, we have measured large zero bias peaks on the order of $2e^2/h$ in a thin InAs-Al hybrid nanowire device, using a four-terminal device design. At particular gate voltage settings, we observe a smooth transition between zero bias peaks and zero bias dips, driven by a magnetic field. The zero-bias conductance sticks close to $2e^2/h$ during this dip-to-peak transition, forming a plateau-like feature. Further gate scans of these zero bias peaks at finite magnetic field reveal plateau-like features (with fluctuations) around $2e^2/h$. We discuss our data with a possible (not necessary exclusive) interpretation based on quasi-Majorana zero modes, smooth potential variation, and disorder. A more conclusive demonstration of this interpretation would be a quantized zero bias peak-to-dip transition tuned by the tunnel gate voltage, which can build a direct link between the transition and the barrier



FIG. 10. Device fabrication, measurement set-up and data analysis. Device Fabrication: The InAs nanowires were grown by molecularbeam epitaxy followed by an in-situ growth of Al film. The nanowires were then transferred from the growth chip to a p-doped Si/SiO₂ substrate by wipes of clean room tissues. Part of the Al film was selectively etched using Transene Aluminum Etchant Type D at 50 °C for 10 seconds with etch windows patterned by electron-beam lithography (EBL). Electric contacts and side gates were fabricated in another round of EBL. Before metal deposition, a 40-s-long Argon plasma etching at a power of 50 W and pressure of 0.05 Torr was performed in the load-lock to ensure Ohmic contacts. Measurement set-up: A total bias voltage V_{bias} from a DC voltage source is first mixed with a lock-in excitation voltage into a summing module. This DC + AC voltage signal, after passing through a voltage divider (with $R_1 : R_2 = 1000:1$), is applied to the N1 contact with several filters in between (π filter at room temperature, copper powder filter and RC filter in the mixing chamber). The current (I and dI) is drained from the S1 contact, also passing through these three-stage filters, to a preamplifier (a current-voltage converter with an amplification gain of 1 μ A/V) and measured using lock-in 1 and a DC voltage meter. In addition, the voltage drop (V and dV) between the contacts N2 and S2 is measured using another DC voltage meter (after $100 \times$ amplification) and a second lock-in, which is synchronized with the first one. Therefore, the differential conductance dI/dV is directly calculated by taking dV from lock-in 2, and dI from lock-in 1, where V can be read directly from the voltage meter between N2 and S2. We use the lock-in X-components in calculating dI/dV for all data, and if using the R-components, we find dI/dV to be ~1% higher for conductance near $2e^2/h$. Therefore, this 1% sets a lower bar of the measurement uncertainty for conductance near $2e^2/h$. Gate voltages provided by three voltage sources are applied to corresponding gates, after passing through the three-stage filters (not shown in the figure). The mixing chamber of the dilution fridge has a base temperature ~ 20 mK. Between the room temperature π filters and mixing chamber filters, the fridge lines also have proper thermal anchoring at every stage. Data Analysis: For all the 2D color maps of dI/dV vs V and B (or gate voltages), a bias offset V_{offset} (~50 μ V) is estimated and subtracted from V, based on the symmetry of each data set, which relies on the particle-hole symmetry of the superconducting gap (see data repository for V_{offset} details). For the color-map plots, the dI/dV vs V curves are interpolated on to a regularly spaced V grid. Zero-bias line-cuts in bias scans are extracted based on the conductance value whose corresponding bias V is the closest to zero.

transmission. All the results in this paper are from a single device (except for Fig. 21 in Appendix from a different device), and more devices are needed to fully understand the impact of the thinner diameter. Future devices could be aiming at longer, thinner, and more uniform (nontapered) InAs-Al wires with better gate and dielectric designs to minimize the level of disorder, which hopefully may lead to better quantization: more flat and accurate plateaus.

Raw data and processing codes within this paper are available at Zenodo [57].

ACKNOWLEDGMENTS

We thank L. Kouwenhoven, S. Das Sarma, and Ö. Gül for valuable comments and a critical reading of the manuscript.

This work is supported by Tsinghua University Initiative Scientific Research Program, National Natural Science Foundation of China (Grants No. 92065106, No. 12004040, No. 61974138, and No. 12104053), Beijing Natural Science Foundation (Grant No. 1192017) and Alibaba Innovative Research Program. H.S. acknowledges China Postdoctoral Science Foundation (Grants No. 2020M670173 and No. 2020T130058), D.P. acknowledges the support from Youth Innovation Promotion Association, Chinese Academy of Sciences (Grants No. 2017156 and No. Y2021043).

APPENDIX: DEVICE DETAILS, ADDITIONAL DATA AND ANALYSIS

The Appendix includes Figs. 10-21 that are discussed in the main text.



FIG. 11. Device basic characterization. [(a)–(d)] Four additional line cuts from Fig. 1(c) with V_{TG} labeled. (e) Magnetic field dependence of the hard superconducting gap at $V_{\text{BG}} = -2.251$ V [same with Fig. 1(c) and Fig. 2]. Lower panel, zero-bias line-cut. Right panels, dI/dVline-cuts at different magnetic fields. The gap remains hard (zero-bias conductance ~ 0) at finite *B* (for *B* < 1.5 T). Note that this *B*-field range is also where we observe large zero bias peaks and dips. (f) Same with (e) but at $V_{\text{TG}} = -3.207$ V, which is close to the V_{TG} value of Fig. 2. Two subgap states detach from the gap edges and anticross at ~ 1.7 T, resulting in an increase of the zero-bias conductance for *B* > 1 T. Our large ZBPs were measured in open regime (finite subgap conductance). To reach tunneling regime, one needs to decrease either V_{TG} or V_{BG} , as was done in (e) or (f), both showing hard gaps. (g) V_{TG} scan of the superconducting gap for $V_{\text{BG}} = -8$ V, resolving a hard gap (see lower panel the line-cut). This V_{TG} range is close to V_{TG} values of the large ZBPs. (h) V_{BG} scan of the superconducting gap for $V_{\text{TG}} = -3$ V. (i) V_{BG} scan over a large voltage range resolves Andreev bound states (ABSs) due to unintentional quantum dots. Lower panels show line-cuts of different ABS cases (singlet, doublet, and degeneracy) with V_{BG} values labeled above (also indicated by the corresponding color bars in the upper panel).



FIG. 12. Effect of super gate. [(a),(b)] Super gate voltage (V_{SG}) scan near pinch-off (a) and in open regime (b) with parameters of *B*, bias and gate voltages labeled. V_{bias} is the total bias applied to N1 (see Fig. 1) where, due to offsets from the circuit, dI/dV at V_{bias} of 0.52 mV and -0.122 mV roughly correspond to G_N and zero-bias conductance, respectively. Tunnel gate was unintentionally floated due to a bad connection, realized after the measurement of this figure (and fixed). The conductance shows little response for V_{SG} scan over large gate voltage ranges. (c) dI/dV versus V and B, resolving large ZBPs near $2e^2/h$. Lower panel shows the zero-bias line-cut. (d) Waterfall plot of all the dI/dV line-traces from (c), red for large ZBPs near $2e^2/h$. (e) V_{SG} scan of the large ZBP at B = 1 T, over a large gate voltage range with the zero-bias line-cut shown in the lower panel. (f) All dI/dV line-traces from (e). Based on the unusual V_{SG} dependence of the device conductance and ZBPs over large super gate voltage ranges (several times larger than the V_{TG} - and V_{BG} ranges shown in the paper), we concluded that the super gate was not well functional and thus set to 0 V for the rest of the measurements (other figures within this paper). For this figure, due to noise fluctuations (likely because of the floating of tunnel gate), the dI/dV line-traces show unstable wiggles if plotted using our standard four-terminal method. To overcome this situation, the bias V in (c)–(f) was calibrated using the traditional two-terminal formula of $V = V_{bias} - I \times R_{series}$, different from all other figures where V was directly taken from the voltage meter between N2 and S2. R_{series} is estimated to be 19 $k\Omega$, which shows a good match for the ZBP-width and shape by plotting together with the wiggling line-trace using four-terminal method. A minimal smoothing is performed for I over three neighboring points. We note the conductance value dI/dV in this figure i



FIG. 13. Coarse search of ZBPs. (a) Zero-bias conductance map at B = 0.95 T. This map provides a coarse overview of the gate voltage parameter space, which serves as a guidance on where to "zoom-in" and search for large ZBPs. As shown in Figs. 5–7, large zero-bias conductance does not necessary resolve a zero bias peak in bias sweep. (b) An unsuccessful search in the upper left region of the "map" in (a). The zero-bias line-cut shows three peak features when sweeping V_{TG} . The first peak at $V_{TG} \sim -5.97$ V is not a zero bias peak in bias scan but split peaks. The other two peaks at $V_{TG} \sim -5.84$ V and -5.76 V are indeed zero bias peaks. But these two ZBPs (see yellow arrows) are (1) small in height and (2) not robust in V_{TG} scan and quickly split. These ZBPs, formed by sharp level crossings, are typical Andreev bound states. (c) Another zoom-in search in the bottom part of (a), showing no robust ZBPs either. The yellow arrow marks a nonrobust ZBP as a typical Andreev bound state. The large zero-bias conductance (lower panel, between -3 V and -2.8 V) does not show ZBPs in bias sweep.



FIG. 14. Line-cuts of ZBPs and split peaks at higher *B*. (a)–(d) correspond to Figs. 2(c), 4(b) 4(d), and 4(f), respectively. The dI/dV line-traces at higher *B* values (black curves) show a continuous decrease of zero-bias conductance, which finally leads to peak splittings.



FIG. 15. More about the numerical theory model. The Bogoliubov-de Gennes (BdG) Hamiltonian of the nanowire can be written as \mathcal{H}_{NW} = $\frac{1}{2}\int dx \Psi^{\dagger}(x) H_{\rm NW} \Psi(x)$ with $H_{\rm NW}$ describing a one-dimensional semiconductor nanowire coupled to a superconductor: $H_{\rm NW} = (\frac{P_{x}^{2}}{2\pi^{*}} - \mu + \mu)$ $\bar{V}(x)$ $\tau_z + \frac{\alpha}{\hbar} p_x \sigma_v \tau_z + V_z \sigma_x + \Delta_0 \tau_x$. $p_x = -i\hbar \partial_x$ is the momentum, m^* is the effective mass, μ is the chemical potential, V(x) is the electrostatic potential along the nanowire, α is the spin-orbit coupling strength, V_z is the Zeeman field, and Δ_0 is the proximity-induced superconducting gap. σ_i and τ_i (i = x, y, z) are Pauli matrices acting on spin and particle-hole space, respectively. An infinitesimal dissipation term $i\Gamma$ is also added in the Hamiltonian in order to smooth the conductance. The Nambu spinor basis is chosen as $\Psi(x) = (\psi_{\uparrow}(x), \psi_{\downarrow}(x), \psi_{\uparrow}^{\dagger}(x), -\psi_{\uparrow}^{\dagger}(x))^{T}$. We chose the parameters as $m^* = 0.05 m_e$, $\alpha = 30 \text{ meVnm}$, $\Delta_0 = 0.25 \text{ meV}$, $\mu = 2 \text{ meV}$. We use a triangle shape potential V(x), see the black curve in (a), middle panel (in Fig. 3, the potential has a tilted line shape). The energy spectrum of this nanowire is given in (b). Below the topological phase transition point ($V_z \sim 2 \text{ meV}$), a near-zero-energy state already appears as quasi-MZMs whose wavefunctions at $V_z = 1.3 \text{ meV}$ are shown in the lower panel of (a). Next we use a software package Kwant to calculate the conductance. We attach a lead to the left end of the nanowire and apply a bias voltage. A very narrow tunnel barrier with height V_b is also added between the lead and the nanowire, as shown by the vertical line in the middle panel of (a). V_b is larger than the shown range in the middle panel of (a) due to limited space [same for Fig. 3(a)]. We further assume that V_b is V_z dependent, which first slightly drops for a certain value and then increases with V_z as shown in (c). The calculated dI/dVvs bias voltage E and V_z is shown in (c) (middle panel). The zero-bias line-cut [lower panel (c)] shows a $2e^2/h$ quantized plateau in a V_z range labeled by the blue and red lines. dI/dV line-traces within this V_z range are plotted in (d) showing the dip-to-peak transition. For higher V_z , the zero-bias conductance starts to oscillate [also shown in Fig. 3(c)], due to overlapping of MZMs. These ZBPs (at high V_z) have very narrow peak width, which could easily be smoothed by thermal broadening (thus height drops below $2e^2/h$) in realistic situation with finite device temperature.



FIG. 16. Additional B scans of ZBPs at various gate voltage settings (labeled in each panel). Lower panels show the zero-bias line-cuts.



FIG. 17. Additional line-cuts of Fig. 5. (a) and (d) same as Figs. 5(a) and 5(d). [(b),(c)] and [(e),(f)] show more horizontal/vertical line-cuts in (a) and (d), respectively. The gate voltages and magnetic fields are labeled in each panel, also indicated by the dashed lines in the right panels of (a) and (d). The *B*-scan plateau in the middle panel of (b) shares almost the same gate voltage settings with Figs. 4(e) and 4(f) whose zero-bias line-cut shows similar plateau feature. The small plateau at $2e^2/h$ near zero *B* in the lower panel of (b) is likely not a ZBP based on the bias scan in Fig. 5(c). Another *B*-scan plateau in the lower panel of (e) have gate voltage settings very close to Figs. 4(a) and 4(b) and Fig. 7 (the first panel). As for the plateau above $2e^2/h$ in the middle panel of (e), we do not have the bias scan data near this parameter space and therefore could not identify it as ZBPs or non-ZBPs. The middle panel in (f) corresponds to Fig. 6(d) with the same V_{BG} and *B*.



FIG. 18. Additional gate scans of ZBPs at B = 1 T. [(a)–(c)] V_{BG} scans of the ZBP at different tunnel gate voltages labeled in each panel. Zero-bias line-cuts are shown in the lower panels. [(d)–(f)] V_{TG} scans of ZBPs at different back gate voltages labeled in each panel. Zero-bias line-cuts are shown in the lower panels. The zero-bias line-cut in (f) may possibly be argued as a plateau-like feature near $0.8 \times 2e^2/h$. Its back gate voltage corresponds to a horizontal line-cut near the edge of the red islands in Fig. 7(a), if assuming no gate voltage drift in between, while (d) and (e) correspond to line-cuts across the red islands. Therefore, we think (f) is an intermediate case (transition or crossover) from plateau-like to nonplateau situation. This also suggests that to fully establish MZM or quasi-MZM quantization with reasonably good quality, more efforts are needed, e.g., by minimizing disorder and observing more flat and accurate plateaus or providing additional experimental signatures (e.g., dip-to-peak transition).



FIG. 19. Additional line-cuts of the zero bias map at 1 T and 0.8 T. (a) and (d) are same as Fig. 6(b) [Fig. 7(a)] and Fig. 8(c). (b) and (c) show the horizontal and vertical line-cuts from (a) (see corresponding dashed lines). (e) and (f) show the horizontal and vertical line-cuts from (d) (see corresponding dashed lines).



FIG. 20. (a) All the line-cuts from Fig. 8(d) within the V_{BG} range from -2.238 V to -2.046 V. (b) All the line-cuts from Fig. 8(f) within the V_{TG} range from -3.642 V to -3.342 V.



FIG. 21. Zero bias dip and dip-to-peak transition near $2e^2/h$ in a second device. The device details can be found in Ref. [51]. (a), (c), and (e) are the magnetic field and gate scans with the zero-bias line cuts shown in the lower panels. (b), (d), and (f) are the corresponding waterfall plots. Blue and red are used to indicate the zero bias dips and peaks near $2e^2/h$. The pink background indicates the 10% tolerance bar, i.e., from 0.9 to 1.1 of $2e^2/h$. For the black curves, every other line cut is shown for clarity.

- N. Read and D. Green, Paired states of fermions in two dimensions with breaking of parity and time-reversal symmetries and the fractional quantum hall effect, Phys. Rev. B 61, 10267 (2000).
- [2] A. Y. Kitaev, Unpaired Majorana fermions in quantum wires, Phys. Usp. 44, 131 (2001).
- [3] R. M. Lutchyn, J. D. Sau, and S. Das Sarma, Majorana Fermions and a Topological Phase Transition in Semiconductor-Superconductor Heterostructures, Phys. Rev. Lett. 105, 077001 (2010).
- [4] Y. Oreg, G. Refael, and F. von Oppen, Helical Liquids and Majorana Bound States in Quantum Wires, Phys. Rev. Lett. 105, 177002 (2010).
- [5] E. Prada, P. San-Jose, M. W. de Moor, A. Geresdi, E. J. Lee, J. Klinovaja, D. Loss, J. Nygård, R. Aguado, and L. P. Kouwenhoven, From Andreev to Majorana bound states in hybrid superconductor–semiconductor nanowires, Nat. Rev. Phys. 2, 575 (2020).
- [6] R. M. Lutchyn, E. P. Bakkers, L. P. Kouwenhoven, P. Krogstrup, C. M. Marcus, and Y. Oreg, Majorana zero modes in superconductor–semiconductor heterostructures, Nat. Rev. Mater. 3, 52 (2018).
- [7] H. Zhang, Ö. Gül, S. Conesa-Boj, M. P. Nowak, M. Wimmer, K. Zuo, V. Mourik, F. K. De Vries, J. Van Veen, M. W. De Moor *et al.*, Ballistic superconductivity in semiconductor nanowires, Nat. Commun. 8, 16025 (2017).
- [8] J. Kammhuber, M. C. Cassidy, H. Zhang, Ö. Gül, F. Pei, M. W. de Moor, B. Nijholt, K. Watanabe, T. Taniguchi, D. Car *et al.*, Conductance quantization at zero magnetic field in InSb nanowires, Nano Lett. **16**, 3482 (2016).
- [9] W. Chang, S. Albrecht, T. Jespersen, F. Kuemmeth, P. Krogstrup, J. Nygård, and C. M. Marcus, Hard gap in epitaxial semiconductor-superconductor nanowires, Nat. Nanotechnol. 10, 232 (2015).
- [10] P. Krogstrup, N. Ziino, W. Chang, S. Albrecht, M. Madsen, E. Johnson, J. Nygård, C. Marcus, and T. Jespersen, Epitaxy of semiconductor-superconductor nanowires, Nat. Mater. 14, 400 (2015).
- [11] Ö. Gül, H. Zhang, F. K. de Vries, J. van Veen, K. Zuo, V. Mourik, S. Conesa-Boj, M. P. Nowak, D. J. van Woerkom, M. Quintero-Pérez *et al.*, Hard superconducting gap in InSb nanowires, Nano Lett. **17**, 2690 (2017).
- [12] S. Vaitiekėnas, M.-T. Deng, J. Nygård, P. Krogstrup, and C. M. Marcus, Effective g Factor of Subgap States in Hybrid Nanowires, Phys. Rev. Lett. **121**, 037703 (2018).
- [13] M. W. de Moor, J. D. Bommer, D. Xu, G. W. Winkler, A. E. Antipov, A. Bargerbos, G. Wang, N. Van Loo, R. L. O. het Veld, S. Gazibegovic *et al.*, Electric field tunable superconductor-semiconductor coupling in Majorana nanowires, New J. Phys. 20, 103049 (2018).
- [14] J. D. S. Bommer, H. Zhang, O. Gul, B. Nijholt, M. Wimmer, F. N. Rybakov, J. Garaud, D. Rodic, E. Babaev, M. Troyer, D. Car, S. R. Plissard, E. P. A. M. Bakkers, K. Watanabe, T. Taniguchi, and L. P. Kouwenhoven, Spin-Orbit Protection of Induced Superconductivity in Majorana Nanowires, Phys. Rev. Lett. **122**, 187702 (2019).
- [15] K. Sengupta, I. Žutić, H.-J. Kwon, V. M. Yakovenko, and S. Das Sarma, Midgap edge states and pairing symmetry of quasi-onedimensional organic superconductors, Phys. Rev. B 63, 144531 (2001).

- [16] K. T. Law, P. A. Lee, and T. K. Ng, Majorana Fermion Induced Resonant Andreev Reflection, Phys. Rev. Lett. 103, 237001 (2009).
- [17] K. Flensberg, Tunneling characteristics of a chain of Majorana bound states, Phys. Rev. B 82, 180516(R) (2010).
- [18] M. Wimmer, A. Akhmerov, J. Dahlhaus, and C. Beenakker, Quantum point contact as a probe of a topological superconductor, New J. Phys. 13, 053016 (2011).
- [19] V. Mourik, K. Zuo, S. M. Frolov, S. Plissard, E. P. Bakkers, and L. P. Kouwenhoven, Signatures of Majorana fermions in hybrid superconductor-semiconductor nanowire devices, Science 336, 1003 (2012).
- [20] M. Deng, C. Yu, G. Huang, M. Larsson, P. Caroff, and H. Xu, Anomalous zero-bias conductance peak in a Nb–InSb nanowire–Nb hybrid device, Nano Lett. 12, 6414 (2012).
- [21] A. Das, Y. Ronen, Y. Most, Y. Oreg, M. Heiblum, and H. Shtrikman, Zero-bias peaks and splitting in an Al–InAs nanowire topological superconductor as a signature of Majorana fermions, Nat. Phys. 8, 887 (2012).
- [22] H. O. H. Churchill, V. Fatemi, K. Grove-Rasmussen, M. T. Deng, P. Caroff, H. Q. Xu, and C. M. Marcus, Superconductornanowire devices from tunneling to the multichannel regime: Zero-bias oscillations and magnetoconductance crossover, Phys. Rev. B 87, 241401(R) (2013).
- [23] A. D. K. Finck, D. J. Van Harlingen, P. K. Mohseni, K. Jung, and X. Li, Anomalous Modulation of a Zero-Bias Peak in a Hybrid Nanowire-Superconductor Device, Phys. Rev. Lett. 110, 126406 (2013).
- [24] S. Takei, B. M. Fregoso, H.-Y. Hui, A. M. Lobos, and S. Das Sarma, Soft Superconducting Gap in Semiconductor Majorana Nanowires, Phys. Rev. Lett. **110**, 186803 (2013).
- [25] Ö. Gül, H. Zhang, J. D. Bommer, M. W. de Moor, D. Car, S. R. Plissard, E. P. Bakkers, A. Geresdi, K. Watanabe, T. Taniguchi *et al.*, Ballistic Majorana nanowire devices, Nat. Nanotechnol. 13, 192 (2018).
- [26] M. Deng, S. Vaitiekėnas, E. B. Hansen, J. Danon, M. Leijnse, K. Flensberg, J. Nygård, P. Krogstrup, and C. M. Marcus, Majorana bound state in a coupled quantum-dot hybrid-nanowire system, Science **354**, 1557 (2016).
- [27] F. Nichele, A. C. C. Drachmann, A. M. Whiticar, E. C. T. O'Farrell, H. J. Suominen, A. Fornieri, T. Wang, G. C. Gardner, C. Thomas, A. T. Hatke, P. Krogstrup, M. J. Manfra, K. Flensberg, and C. M. Marcus, Scaling of Majorana Zero-Bias Conductance Peaks, Phys. Rev. Lett. **119**, 136803 (2017).
- [28] H. Zhang, M. W. de Moor, J. D. Bommer, D. Xu, G. Wang, N. van Loo, C.-X. Liu, S. Gazibegovic, J. A. Logan, D. Car *et al.*, Large zero-bias peaks in InSb-Al hybrid semiconductorsuperconductor nanowire devices, arXiv:2101.11456.
- [29] J. D. S. Bommer, Zero-energy states in Majorana nanowire devices, Ph.D. thesis, Delft University of Technology, 2020.
- [30] C. Moore, C. Zeng, T. D. Stanescu, and S. Tewari, Quantized zero-bias conductance plateau in semiconductorsuperconductor heterostructures without topological Majorana zero modes, Phys. Rev. B 98, 155314 (2018).
- [31] A. Vuik, B. Nijholt, A. Akhmerov, and M. Wimmer, Reproducing topological properties with quasi-Majorana states, SciPost Phys. 7, 061 (2019).
- [32] E. Prada, P. San-Jose, and R. Aguado, Transport spectroscopy of n s nanowire junctions with Majorana fermions, Phys. Rev. B 86, 180503(R) (2012).

- [33] J. Cayao, E. Prada, P. San-Jose, and R. Aguado, SNS junctions in nanowires with spin-orbit coupling: Role of confinement and helicity on the subgap spectrum, Phys. Rev. B 91, 024514 (2015).
- [34] E. J. Lee, X. Jiang, M. Houzet, R. Aguado, C. M. Lieber, and S. De Franceschi, Spin-resolved Andreev levels and parity crossings in hybrid superconductor-semiconductor nanostructures, Nat. Nanotechnol. 9, 79 (2014).
- [35] C.-X. Liu, J. D. Sau, T. D. Stanescu, and S. Das Sarma, Andreev bound states versus Majorana bound states in quantum dot-nanowire-superconductor hybrid structures: Trivial versus topological zero-bias conductance peaks, Phys. Rev. B 96, 075161 (2017).
- [36] C. Reeg, O. Dmytruk, D. Chevallier, D. Loss, and J. Klinovaja, Zero-energy andreev bound states from quantum dots in proximitized rashba nanowires, Phys. Rev. B 98, 245407 (2018).
- [37] T. D. Stanescu, R. M. Lutchyn, and S. Das Sarma, Majorana fermions in semiconductor nanowires, Phys. Rev. B 84, 144522 (2011).
- [38] G. Kells, D. Meidan, and P. W. Brouwer, Near-zero-energy end states in topologically trivial spin-orbit coupled superconducting nanowires with a smooth confinement, Phys. Rev. B 86, 100503(R) (2012).
- [39] P. Yu, J. Chen, M. Gomanko, G. Badawy, E. Bakkers, K. Zuo, V. Mourik, and S. Frolov, Non-Majorana states yield nearly quantized conductance in proximatized nanowires, Nat. Phys. 17, 482 (2021).
- [40] F. Pientka, G. Kells, A. Romito, P. W. Brouwer, and F. von Oppen, Enhanced Zero-Bias Majorana Peak in the Differential Tunneling Conductance of Disordered Multisubband Quantum-Wire/Superconductor Junctions, Phys. Rev. Lett. 109, 227006 (2012).
- [41] D. Rainis, L. Trifunovic, J. Klinovaja, and D. Loss, Towards a realistic transport modeling in a superconducting nanowire with Majorana fermions, Phys. Rev. B 87, 024515 (2013).
- [42] A. E. Antipov, A. Bargerbos, G. W. Winkler, B. Bauer, E. Rossi, and R. M. Lutchyn, Effects of Gate-Induced Electric Fields on Semiconductor Majorana Nanowires, Phys. Rev. X 8, 031041 (2018).
- [43] P. Caroff, K. A. Dick, J. Johansson, M. E. Messing, K. Deppert, and L. Samuelson, Controlled polytypic and twin-plane superlattices in iii-v nanowires, Nat. Nanotechnol. 4, 50 (2009).
- [44] H. Shtrikman, R. Popovitz-Biro, A. Kretinin, L. Houben, M. Heiblum, M. Bukała, M. Galicka, R. Buczko, and P. Kacman,

Method for suppression of stacking faults in wurtzite III-V nanowires, Nano Lett. 9, 1506 (2009).

- [45] D. Pan, M. Fu, X. Yu, X. Wang, L. Zhu, S. Nie, S. Wang, Q. Chen, P. Xiong, S. von Molnár *et al.*, Controlled synthesis of phase-pure InAs nanowires on Si (111) by diminishing the diameter to 10 nm, Nano Lett. 14, 1214 (2014).
- [46] H. Pan and S. Das Sarma, Physical mechanisms for zero-bias conductance peaks in Majorana nanowires, Phys. Rev. Res. 2, 013377 (2020).
- [47] S. Das Sarma and H. Pan, Disorder-induced zero-bias peaks in Majorana nanowires, Phys. Rev. B 103, 195158 (2021).
- [48] C. Zeng, G. Sharma, S. Tewari, and T. Stanescu, Partially separated Majorana modes in a disordered medium, Phys. Rev. B 105, 205122 (2022).
- [49] D. Pan, H. Song, S. Zhang, L. Liu, L. Wen, D. Liao, R. Zhuo, Z. Wang, Z. Zhang, S. Yang, J. Ying, W. Miao, R. Shang, H. Zhang, and J. Zhao, In situ epitaxy of pure phase ultra-thin InAs-al nanowires for quantum devices, Chin. Phys. Lett. 39, 058101 (2022).
- [50] C.-X. Liu, J. D. Sau, and S. Das Sarma, Role of dissipation in realistic Majorana nanowires, Phys. Rev. B 95, 054502 (2017).
- [51] Z. Wang, H. Song, D. Pan, Z. Zhang, W. Miao, R. Li, Z. Cao, G. Zhang, L. Liu, L. Wen, R. Zhuo, D. E. Liu, K. He, R. Shang, J. Zhao, and H. Zhang, Observation of plateau regions for zero bias peaks within 5% of the quantized conductance value 2e²/h, arXiv:2205.06736.
- [52] H. Zhang, D. E. Liu, M. Wimmer, and L. P. Kouwenhoven, Next steps of quantum transport in Majorana nanowire devices, Nat. Commun. 10, 5128 (2019).
- [53] G. Blonder, M. M. Tinkham, and K. T. Klapwijk, Transition from metallic to tunneling regimes in superconducting microconstrictions: Excess current, charge imbalance, and supercurrent conversion, Phys. Rev. B 25, 4515 (1982).
- [54] C.-X. Liu, F. Setiawan, J. D. Sau, and S. Das Sarma, Phenomenology of the soft gap, zero-bias peak, and zero-mode splitting in ideal Majorana nanowires, Phys. Rev. B 96, 054520 (2017).
- [55] S. Das Sarma, J. D. Sau, and T. D. Stanescu, Splitting of the zero-bias conductance peak as smoking gun evidence for the existence of the Majorana mode in a superconductorsemiconductor nanowire, Phys. Rev. B 86, 220506(R) (2012).
- [56] H. Pan, C.-X. Liu, M. Wimmer, and S. Das Sarma, Quantized and unquantized zero-bias tunneling conductance peaks in Majorana nanowires: Conductance below and above $2e^2/h$, Phys. Rev. B **103**, 214502 (2021).
- [57] See, http://doi.org/10.5281/zenodo.7099164.