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Effect of Organic Electroactive Crystallites in a Dielectric Matrix on the Electrical Properties of a Polymer Dielectric

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Abstract The effects of inserting chargeable α -quaterthiophene (α 4T) crystallites in polystyrene (PS) multilayers used as a transistor gate and capacitor dielectric were investigated. X-ray diffraction (XRD), scanning electron microscopy with energy dispersive x-ray spectroscopy (SEM/EDS), and confocal microscopy indicated the formation of α 4T crystallites in the PS matrix. A modified saturation-regime current voltage relationship was used to estimate organic field-effect transistor (OFET) threshold voltage V_{TH} shifting, and in turn the quantities of stored charge that were observed as a result of dielectric charging. The crystallites increased the maximum charge storage capacity as well as the charge retention capability of the dielectrics. Kelvin probe force microscopy (KPFM) showed that charges were localized near the α 4T crystallites upon charging. Trilayer experiments validated the charge retention improvement of α 4T crystallite-embedded PS dielectrics. The crystallites also improved breakdown characteristics in PS used as a capacitor dielectric, suggesting their application to storage capacitors in addition to OFET logic.

1. Introduction

Field-effect transistors (FETs) are circuit elements that can be incorporated into logic systems or amplify and switch electrical signals. They are the primary building blocks of modern electronics. Beginning a few decades ago, the functions and capabilities of traditional metal-oxide-semiconductor field-effect transistors (MOSFETs) have expanded through active research in organic field-effect transistors (OFETs), which provide ease of molecular design, low cost, light weight, flexibility, and solution processability.¹⁻⁵

Among the components of FET or OFET devices, dielectric layers have been widely

investigated for their influence on FET performance via surface modification for favorable interfacial properties and/or for their charge trapping functionalities.⁶⁻⁸ However, compared to research on organic alternatives for other components in transistors, research on organic dielectrics has been relatively less emphasized.^{6,9} Surfaces of silicon oxide dielectric layers were modified to enhance performance of organic semiconductor (OSC) charge mobilities via self-assembled monolayers on gate dielectrics or to provide surface functionalization for electrical sensors.^{10,11} In a more direct application of organic dielectrics, organic polymers and copolymers have been investigated systematically based on the theory of percolative nanocomposite additives to increase the energy density of dielectrics for applications in capacitors or other forms of energy storage.¹²⁻¹⁴

Another aspect of dielectrics as active materials includes the ability to control threshold gate voltage (V_{TH} , related to the onset of channel current) shifts in OFETs by trapping injected charge, also known as bias stress effects.^{5,7} We have previously controlled pentacene OFET V_{TH} shifts by trapping injected charge in synthesized polystyrene (PS)-based gate dielectrics containing electroactive molecular subunits.¹⁵⁻¹⁹ In the present paper, we report the embedding of electroactive organic *crystallites* based on aggregating molecules in PS dielectric materials (relative permittivity of 2.4-2.7 from standard references and handbooks) and their incorporation into pentacene OFETs to investigate the effects of these aggregates on charge storage behavior and on the resilience of gate dielectrics.

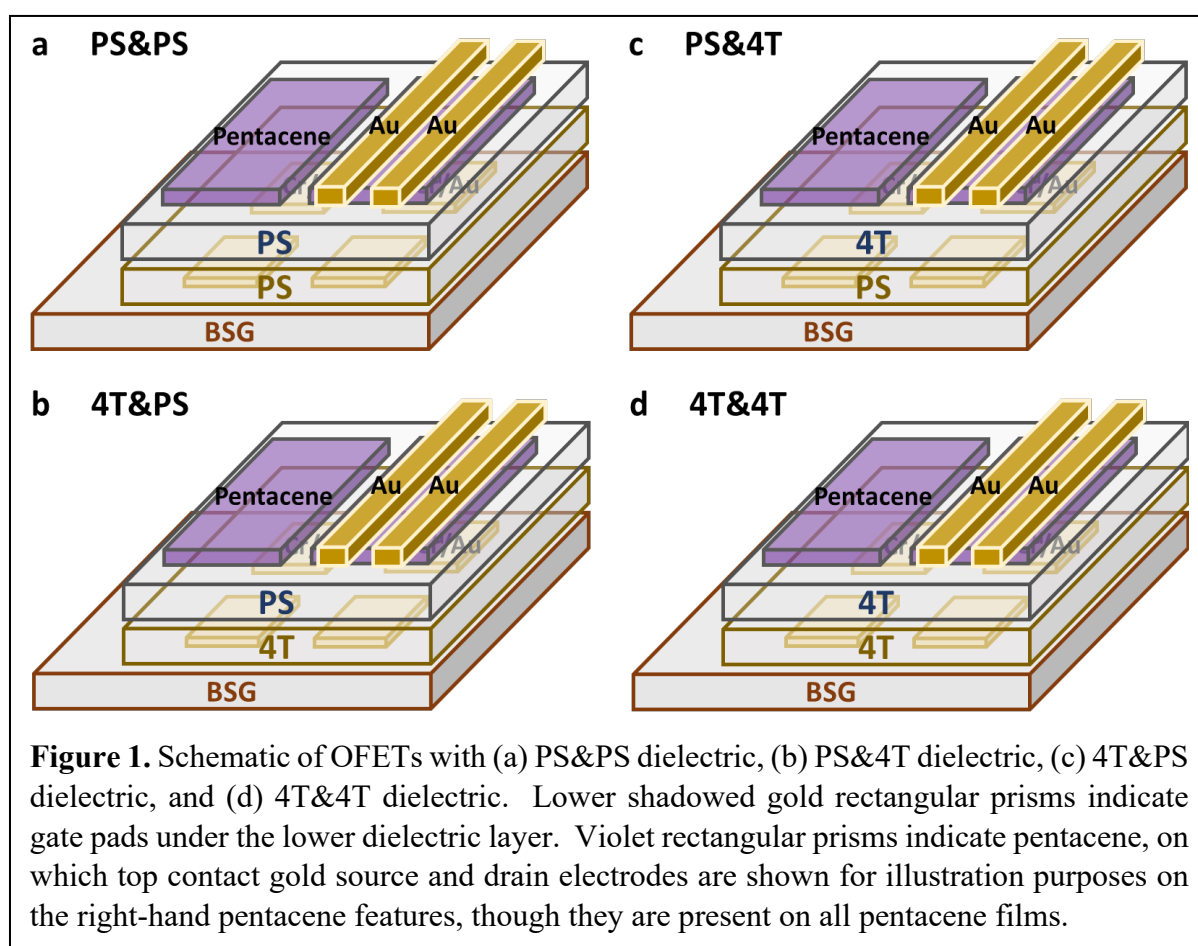
Quaterthiophene (α 4T) was chosen for the electroactive crystallites because of its ready formation of crystals from solution, low but finite solubility in ordinary organic solvents, and environmental stability.²⁰⁻²⁴ We analyzed the charge-trapping properties of α 4T crystallite-embedded PS dielectrics in OFETs and in stand-alone PS films. The structures of the crystallite-embedded films were analyzed using X-ray diffraction (XRD) to determine the existence of crystalline α 4T. 3D laser optical microscopy, scanning electron microscopy (SEM) with energy dispersive x-ray spectroscopy (EDS), and confocal microscopy were used to investigate the size and shape of the embedded α 4T crystallites. Kelvin probe force microscopy (KPFM) enabled us to observe localization of stored charge near the α 4T crystallites.

OFET V_{TH} shifts were used to investigate the influence of the α 4T crystallites on the maximum charge storage capacity and charge retention capability of the films. Varying the position of the α 4T crystallite-embedded layer in bilayer and trilayer dielectrics validated the activity of the crystallites as charge-storing entities, above and beyond the PS, and revealed

differences in charging response depending on the charge-injecting interfaces. Density functional theory calculations further supported the hypothesis that molecular crystallites could stabilize static charge better than would the same molecules in isolation. We additionally found that these crystallites can also decrease dielectric breakdown of PS dielectrics in capacitors.

2. Experimental section

Detailed experimental procedures, material sources, and instrument specifications are provided in the supporting information (SI). Briefly, polystyrene was spin-coated from 20 wt % solutions in tetrahydrofuran. Both stand-alone dielectric films and OFETs were fabricated on borosilicate glass (BSG) substrates. Gold electrodes and pentacene semiconductor layers were



vapor-deposited. Single dielectric layers, four types of dielectric bilayers (**Figure 1**), and trilayers were explored, with the α 4T in either the top or bottom layers or both in bilayers, and in the middle of a trilayers. The bilayers are denoted as PS&PS, PS&4T, 4T&PS, and 4T&4T (**Figure 1**). A modified method for determining the V_{TH} shift was developed and is also described in the SI. This method parameterized the shift of transfer curves without relying on

an exact numerical designation for V_{TH} itself.

3. Results and Discussion

3.1 DFT calculations.

Further motivation for using a π -conjugated oligomer crystallite as a charge-storing additive came from electronic modeling of modest-sized aggregates. Density functional theory (DFT) calculations were performed to study the electronic structures of the electroactive molecules in the gas phase as well as in polystyrene considered as the solvent. Molecular structures were optimized by DFT methods at the PW6B95-D3/def2-QZVPP level of theory^{25,26} using the ORCA software package.²⁷ The solution phase was implicitly modeled using the conductor-like polarizable continuum model (CPCM).²⁸

We determined the ionization energy by calculating the difference between the energies in the neutral and positive state. We chose 5-hexyl-2,2':5',2'':5'',2'''- α -quaterthiophene to represent α 4T in close proximity (or possibly attached) to a hydrocarbon backbone and that would be in an enforced parallel alignment as an aggregate. Its ionization potential evolved from 6.4 eV as a gas phase molecule to 5.5 eV in polystyrene solvent to 5.2 and 4.9 eV as four-molecule structure-optimized parallel arrays in gas phase and polystyrene, respectively. This supports the proposition that aggregated molecules such as α 4T could have greater charge-storing capability than would isolated molecular subunits, even in a solvent consisting largely of alkylbenzene rings. Analogous trends were observed when modeling 2-diethylaminoanthracene and "TIPS-pentacene", that also crystallize from nonpolar media and have similar electron donating capabilities.²⁹

3.2. Dielectric characterization

The morphology and chemical composition of the devices were characterized using 3D laser optical microscopy to determine the surface roughness, XRD to identify and prove the presence of α 4T crystallites in the PS dielectric matrix, and SEM/EDS as well as confocal microscopy to specifically locate α 4T crystallites on the surface of the α 4Tcrystallite embedded PS dielectric matrix.

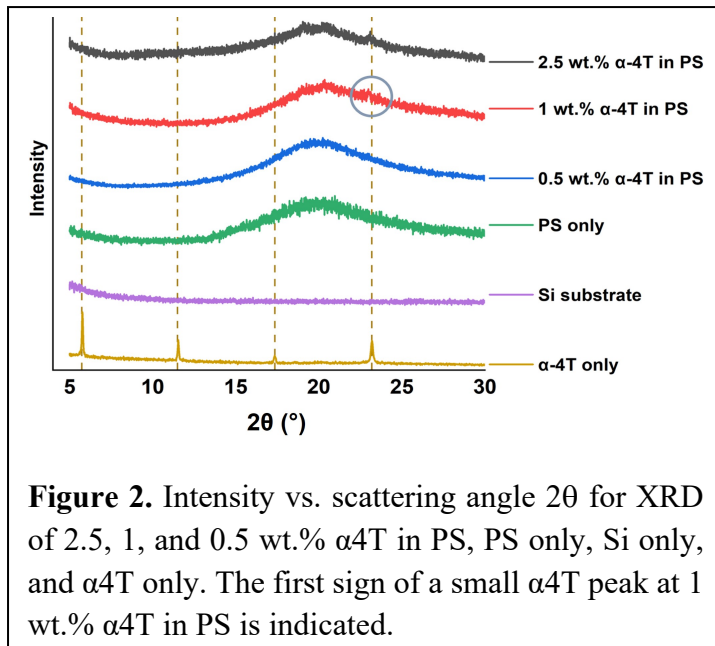
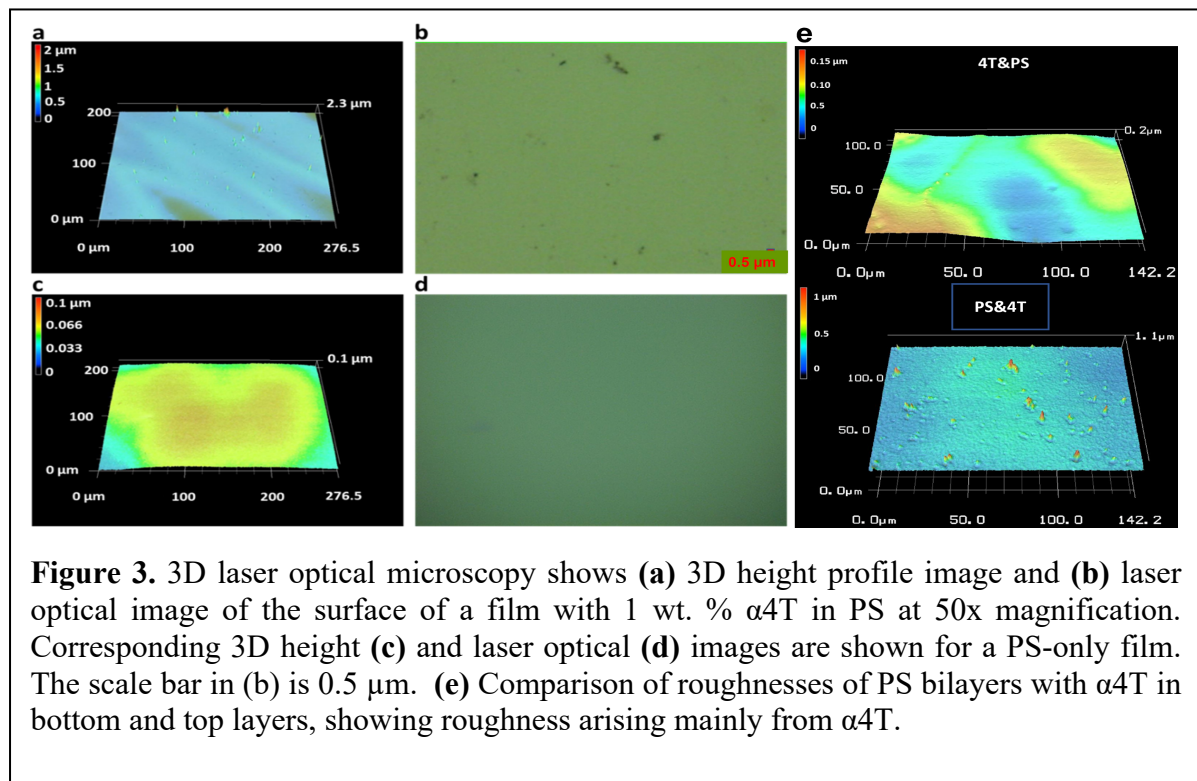


Figure 2 shows that 2.5 wt. % α 4T is a borderline concentration in PS at which the α 4T peak at scattering angle $2\theta = 23^\circ$ is visible compared to the neat powder, while it is marginal in the 1% sample. There was significant masking of the α 4T XRD signal from the amorphous peak of the PS. However, as our objective was to fabricate PS dielectrics with the smallest observable nanocrystals of α 4T embedded in it, 1 wt. % α 4T in PS

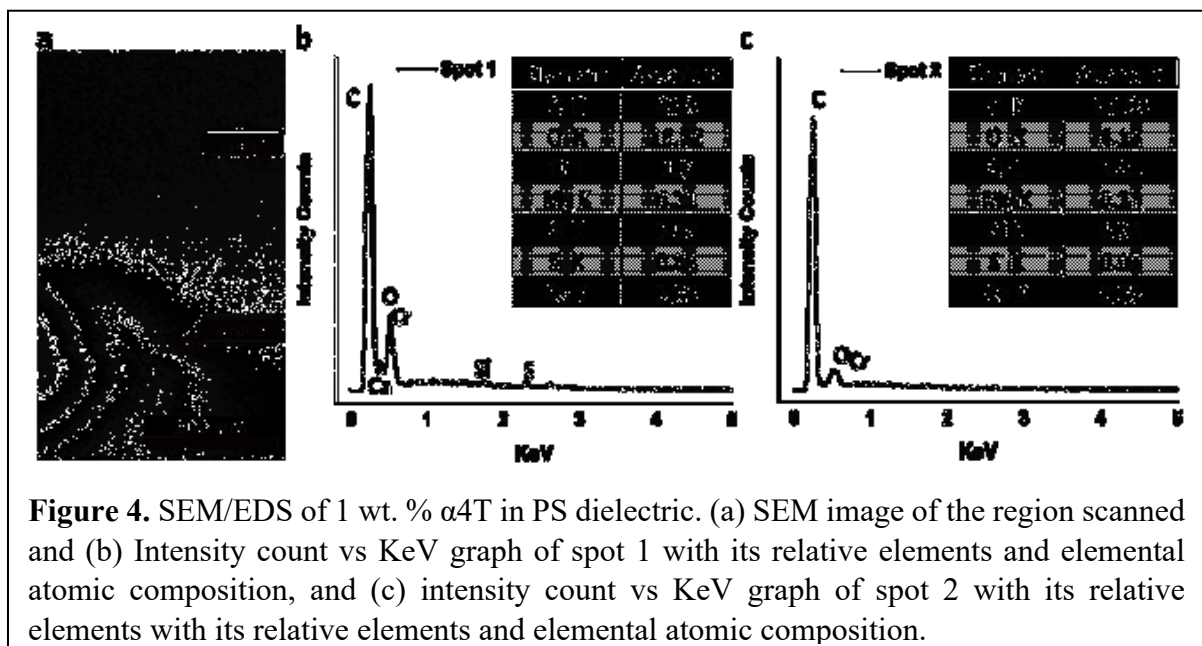
was chosen as the α 4T composition in the PS dielectric.

Figure 3 shows images taken by the 3D laser optical microscope at 50x magnification. **Figures**



3a and 3b show the surface of 1 wt. % α 4T in PS, where the protruding structures in **Figure 3a** and the black circular dots in **Figure 3b** appear to be mostly α 4T crystallites located near the surface. In contrast, pure PS films were flat and featureless, with less than 100 nm of surface

roughness (**Figures 3c and 3d**). The surfaces of the PS&PS sample was smoothest with 100 nm or less roughness (Figure 3c), and then 4T&PS with 200nm or less roughness (**Figure 3e**). This may be because the top layer being PS contributes to a smooth surface. PS&4T samples and 4T&4T samples had the 4T layer on top, which seems to have contributed to surface roughness exceeding 1 μ m. However, mobilities (discussed in SI section 2.3) were not strongly dependent on this roughness. An additional comparison of 4T&PS and PS&4T films is shown in **Figure 3e**.



The protruding structures in the films containing α 4T were analyzed via SEM/EDS (**Figure 4a**). **Figure 4** shows EDS spectra from a 1 wt.% α 4T sample, indicating the presence of α 4T in one of the crystallite-like structures via the presence of the element S (**Figure 4b**). **Figure 4c** shows a significantly diminished presence of sulfur in areas without the bright dots, implying the aggregated structures are α 4T crystallites. The substrate used for SEM/EDS was borosilicate glass (BSG) composed of silicon, boron, calcium, magnesium as well as other alkali oxides for enhancing mechanical properties.

Confocal microscopy of 1 wt. % α 4T in PS samples (**Figure 5.**) showed localized regions of fluorescence at wavelengths (448 nm excitation and 460-570 nm emission) where α 4T has been shown to fluoresce.³⁰ PS-only samples showed no such fluorescence, providing further support for the presence of α 4T crystallites for the 1 wt. % α 4T in PS samples.

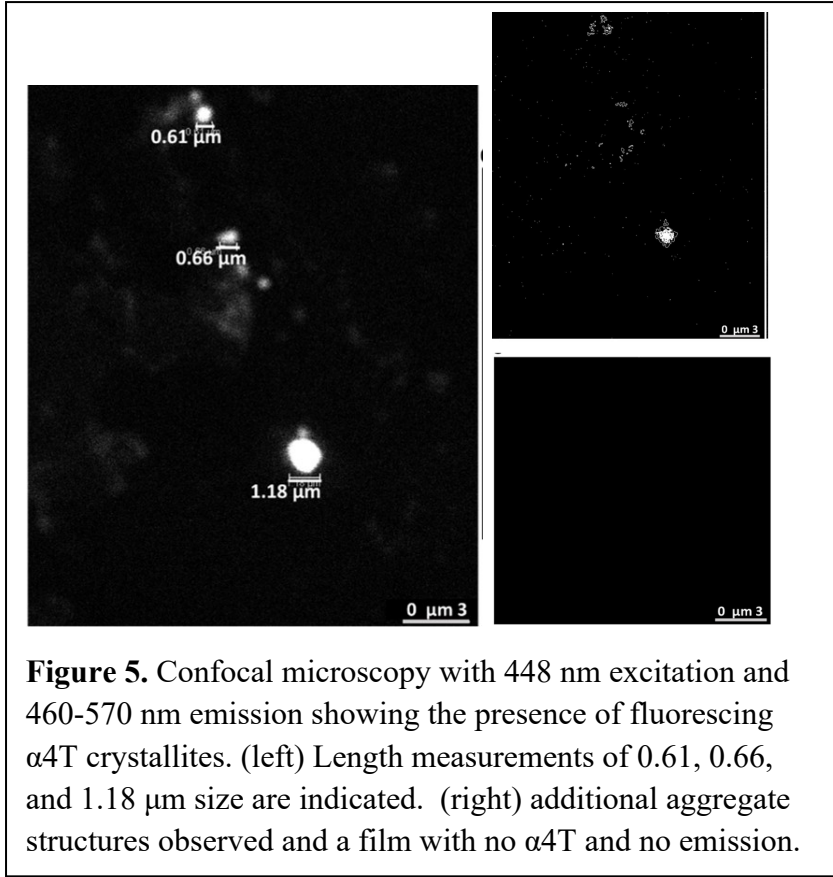


Figure 5. Confocal microscopy with 448 nm excitation and 460-570 nm emission showing the presence of fluorescing α 4T crystallites. (left) Length measurements of 0.61, 0.66, and 1.18 μm size are indicated. (right) additional aggregate structures observed and a film with no α 4T and no emission.

The sizes are larger than the resolution limit of the experiment, which was $<100\text{ nm}$.

3.3 Charge Storage Behavior of Dielectrics With and Without α 4T Crystallites

Figure 6 shows an image of a portion of an OFET structure using a laser optical microscope.

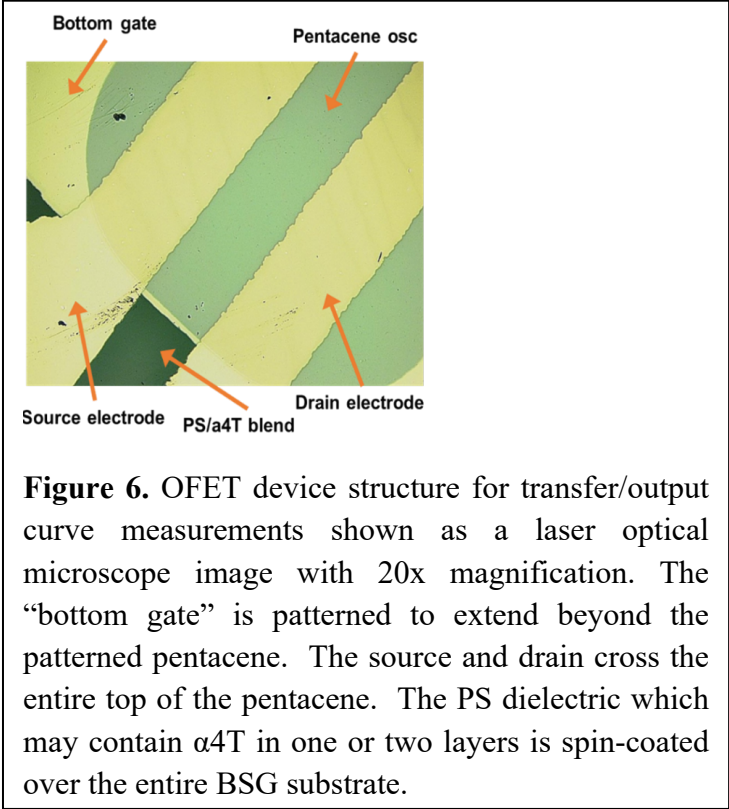


Figure 6. OFET device structure for transfer/output curve measurements shown as a laser optical microscope image with 20x magnification. The “bottom gate” is patterned to extend beyond the patterned pentacene. The source and drain cross the entire top of the pentacene. The PS dielectric which may contain α 4T in one or two layers is spin-coated over the entire BSG substrate.

Charging conditions refer to when there is -70V applied on the top pair of gold source and drain electrodes shown in **Figure 1** while 0V is applied to the gate of the OFET, while discharging conditions refer to when there is $+70\text{V}$ applied on the source and drain while 0V is applied to the gate of the OFET.

3.3.1. Charge Storage Calculations from transfer curve shifts.

Sequences of charging steps were performed on the OFETs to determine charging-induced curve shifts and the retention of those shifts. A charging/discharging sequence refers to transfer curves measured before charging, after -70V 5 min charging, after $+70\text{V}/5$ min discharging, and after another $+70\text{V}/5$ min discharging. Discharging/charging refers to transfer curves measured before charging, after $+70\text{V}/5$ min discharging, after $-70\text{V}/5$ min charging, and after another -70V 5 min charging. **Figures 7 and 8** show the general perturbations resulting from these charging and discharging procedures in the form of average normalized transfer curves. This allows multiple observations of each device configuration to contribute to the displayed trends.

As shown in **Figure 7**, for each of the four types of OFETs, in a discharging/charging sequence, the initial $+70\text{V}/5$ min discharging causes minimal change in the OFET transfer

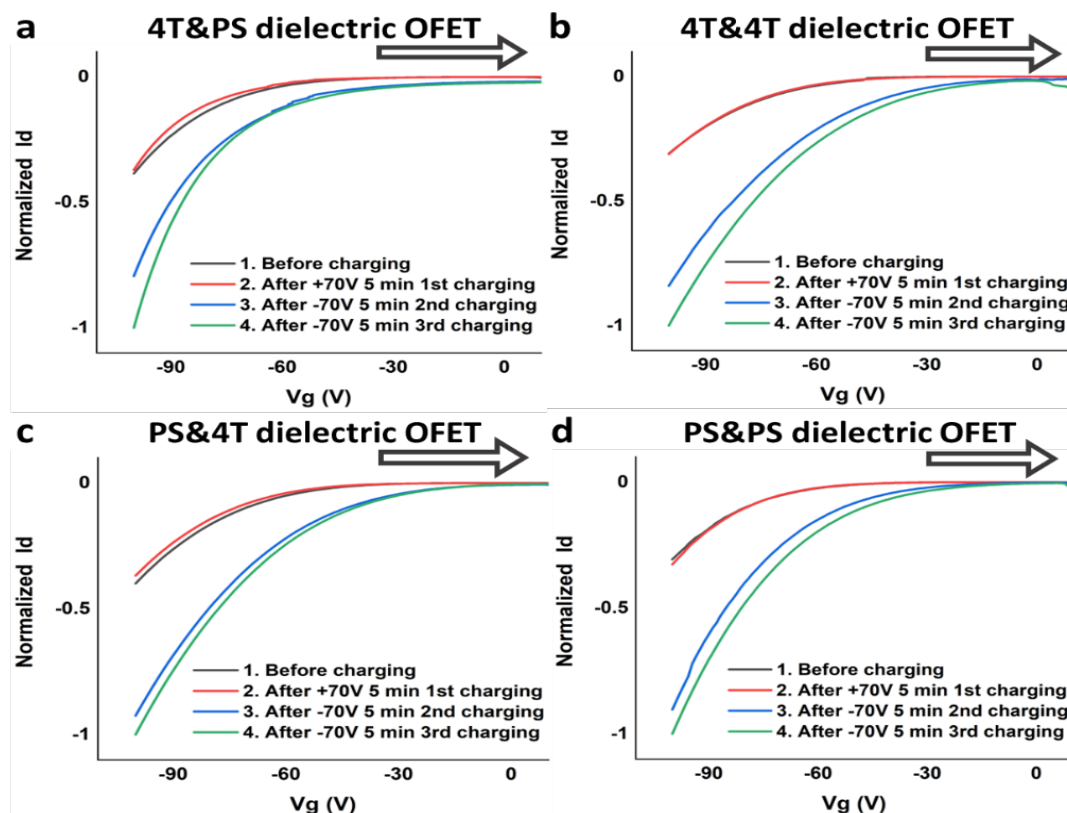


Figure 7. Normalized discharging/charging transfer curves for (a) 4T&PS dielectric OFET, (b) 4T&4T dielectric OFET, (c) PS&4T dielectric OFET, and (d) PS&PS dielectric OFET. Arrows indicate the approximate curve shifts for -70V charging. All normalization was

performed based on the value of I_D at $-100V$ for each graph. curve, and then the subsequent $-70V$ 5 min charging steps induce progressive shifts of the transfer curve to lower magnitude of the gate voltage (V_{GS}), corresponding to increased drain current (I_D) at a given V_{GS} . In contrast, in the charging/discharging sequences, the initial $-70V$ 5 min charging induces a large shift of the transfer curve to lower V_{GS} magnitude, and then the $+70V/5$ min discharging steps cause sequential shifts back to larger magnitude V_{GS} (Figure 8).

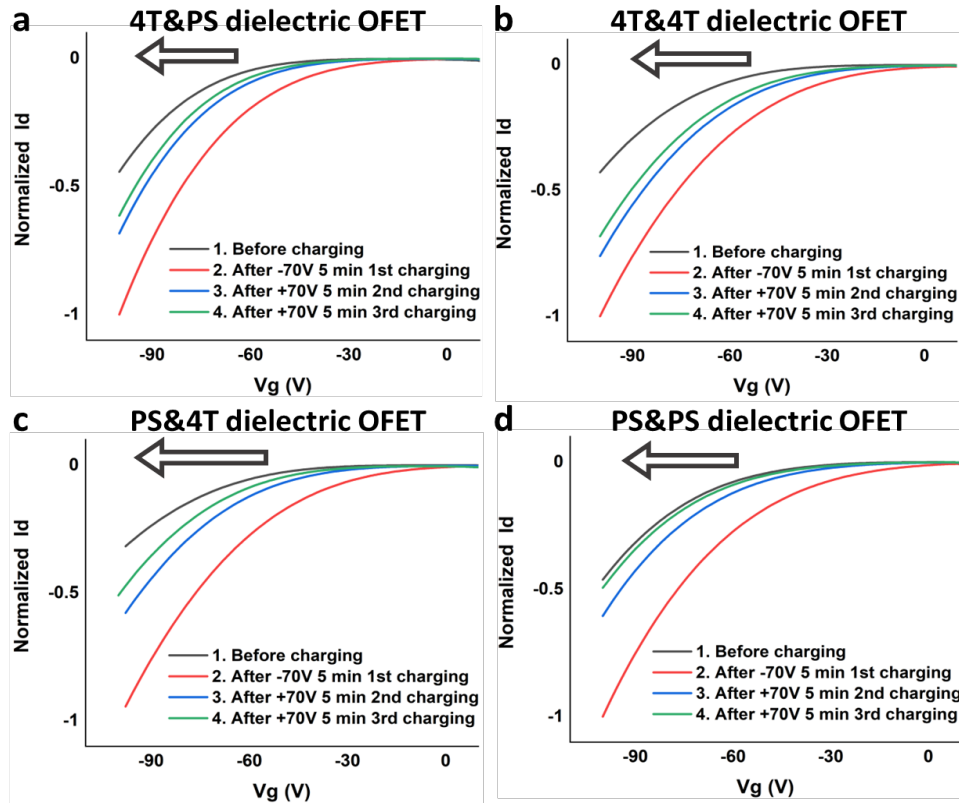


Figure 8. Normalized charging/discharging transfer curves for (a) 4T&PS dielectric OFET, (b) 4T&4T dielectric OFET, (c) PS&4T dielectric OFET, (d) PS&PS dielectric OFET. Arrows indicated the approximate curve shifts for $+70V$ discharging.

These curve shifts are associated with changes in V_{TH} , which are typically calculated as extrapolations to $I_D = 0$ of $\sqrt{|I_D|}$ vs V_{GS} graphs. The data presented in Figures 7 and 8 are replotted as $\sqrt{|I_D|}$ vs V_{GS} graphs in Figure S3 and Figure S4, respectively.³¹ The curvatures of these graphs make the necessary extrapolations imprecise. Therefore, derivatives of the individual $\sqrt{|I_D|}$ vs V_{GS} graphs from these experiments were taken with respect to V_{GS} to obtain plots of $\sqrt{|I_D|}'$ and then $\sqrt{|I_D|}''$ vs V_{GS} . These latter plots were used to calculate

values of “a” (the maxima in the $\sqrt{|I_D|}$ vs V_{GS} curves) whose shifts correspond to shifts in V_{TH} of each curve from procedures outlined in 2.3. of the SI as shown in **Figures S5-S8**. Subsequently, Δa was applied to Eq. S4 gave the total number of charges stored/released for each charging sequence. The same procedure was repeated for at least 3 samples per type of bilayer sample for each of the charging cycles. Average charges stored/released for each of the four sample types are shown in **Table 1** for charging/discharging cycles (**Table 1a**), and discharging/charging cycles (**Table 1b**). All three samples incorporating $\alpha 4T$ captured more charge from the “charging” process than did the one without $\alpha 4T$. It seems that the limit of charge incorporation for the field applied was reached with less than half of the crystallites that would be in a 4T&4T bilayer charged. The fact that increased charge storage occurred regardless of whether the $\alpha 4T$ was at the pentacene interface suggests that this is a bulk charge storage effect rather than interfacial polarization or Maxwell-Wagner effects. Note that the increase in stored charge from the presence of $\alpha 4T$ should not arise significantly from any increased relative permittivity of $\alpha 4T$; its solid state relative permittivity is about 3.5,³² only slightly higher than that of polystyrene, and it is present at a minimal volume fraction.

Charging/discharging cycles showed that PS&PS samples had the smallest degree of stored charge retention after the full charging/discharging cycle at around 16%. 4T&4T had the highest charge retention at around 55%, then 4T&PS at around 46% and PS&4T at around 29%. This suggests that charges stored in the dielectrics localize around the $\alpha 4T$ crystallites. KPFM measurements further validated this theory in the upcoming section, 3.3.2. Charges appear to be stored in a more stable state when stored in 4T&PS compared to PS&4T, with the charge-localizing $\alpha 4T$ crystallites further from the pentacene OSC in which charges may be recombined.

a

Charges stored/released *Positive – released *Negative – stored	after 1 70V 5 min ID	after 2 -70V 5 min ID	after 3 -70V 5 min ID	Volume charge density (charges / cm ³)
Ave. PS&PS	$3.0 \times 10^8 \pm 5.5 \times 10^7$	$-1.3 \times 10^9 \pm 5.2 \times 10^8$	$-1.1 \times 10^9 \pm 7.6 \times 10^8$	$7.0 \times 10^{15} \pm 4.3 \times 10^{15}$
Ave. PS&4T	$9.4 \times 10^7 \pm 6.4 \times 10^7$	$-3.0 \times 10^9 \pm 6.2 \times 10^8$	$-1.1 \times 10^9 \pm 6.2 \times 10^8$	$1.3 \times 10^{16} \pm 4.5 \times 10^{15}$
Ave. 4T&PS	$1.5 \times 10^8 \pm 1.3 \times 10^8$	$-2.8 \times 10^9 \pm 1.1 \times 10^9$	$-8.9 \times 10^8 \pm 4.6 \times 10^8$	$1.2 \times 10^{16} \pm 5.7 \times 10^{15}$
Ave. 4T&4T	$9.9 \times 10^6 \pm 2.4 \times 10^8$	$-2.8 \times 10^9 \pm 1.5 \times 10^9$	$-6.2 \times 10^9 \pm 1.7 \times 10^8$	$1.2 \times 10^{16} \pm 6.4 \times 10^{15}$

b

Charges stored/released *Positive – released *Negative – stored	after 1 -70V 5 min ID	after 2 70V 5 min ID	after 3 70V 5 min ID	Stored Charge Retention (%)
Ave. PS&PS	$-2.0 \times 10^9 \pm 3.9 \times 10^8$	$1.1 \times 10^9 \pm 1.6 \times 10^8$	$5.9 \times 10^8 \pm 4.0 \times 10^8$	16 %
Ave. PS&4T	$-3.1 \times 10^9 \pm 4.4 \times 10^8$	$1.6 \times 10^9 \pm 1.9 \times 10^8$	$5.3 \times 10^8 \pm 2.4 \times 10^8$	31 %
Ave. 4T&PS	$-3.9 \times 10^9 \pm 1.3 \times 10^9$	$1.6 \times 10^9 \pm 1.4 \times 10^9$	$4.4 \times 10^8 \pm 2.9 \times 10^8$	48 %
Ave. 4T&4T	$-3.8 \times 10^9 \pm 8.5 \times 10^8$	$9.8 \times 10^8 \pm 1.2 \times 10^8$	$7.4 \times 10^8 \pm 5.7 \times 10^8$	55 %

Table 1. (a) averaged value of number of charges stored/released for each of the transfer curves of the discharging/charging cycle to calculate total charges stored for all 4 types of samples. The total charges stored (3 + column 4 – column 2) divided by total volume of each sample (refer to section 2.5 of SI). **(b)** Averaged value of number of charges stored/released for each of the transfer curves of the charging/discharging cycle to calculate charge retention for all 4 types of samples. The stored charge retention is (column 2 – column 3 – column 4) / column 2.

Discharging/charging cycles showed that PS&PS had the lowest number density of charges stored, while all other samples containing α 4T crystallites had similar number of charges stored under the conditions of these experiments. We postulate that this was because the maximum charge storage capacity was not reached yet for the other three types of samples. Experiments on maximum charge storage capacity are discussed in section 3.3.3.

3.3.2. Localized Charge Storage Around α 4T via KPFM

Crystallite locations and dimensions as visualized by protrusions at the PS surface were first identified from AFM height profile scans as shown in **Figure 9a. and 10a.** Height and electrical potential scans were subsequently performed on the identified regions as shown in **Figure 9b and 10b.** The device used for charging is shown in **Figure S12.** All scans were performed between the top electrodes on the 4T&4T samples before charging and after charging as outlined in **Figure 9c.** Seven crystallites' (identified as P1-P7) height and electric potential were measured before charging as shown in **Figure 9d-f. and Figure 10c-f.** The seven identified crystallites (P1-P7) before charging had an average of 370 μ V maximum

electrical potential difference, average maximum protrusion of 9 nm, and average width of 1.1 μm . KPFM scans were also performed after charging for four crystallites, whose height and electric potential measurements are shown in **Figure 11**. The four crystallites measured by KPFM after charging (**Figure 11**) had an average of 1,300 μV maximum electrical potential difference, average maximum height of 7.8 nm, and average width of 0.75 μm . The measured average width of the crystallites is in good agreement with measurements from confocal microscopy (**Figure 5**). It is, however, possible that the protrusions included were conformal coatings of PS over crystallites embedded at a finite depth from the top surface.

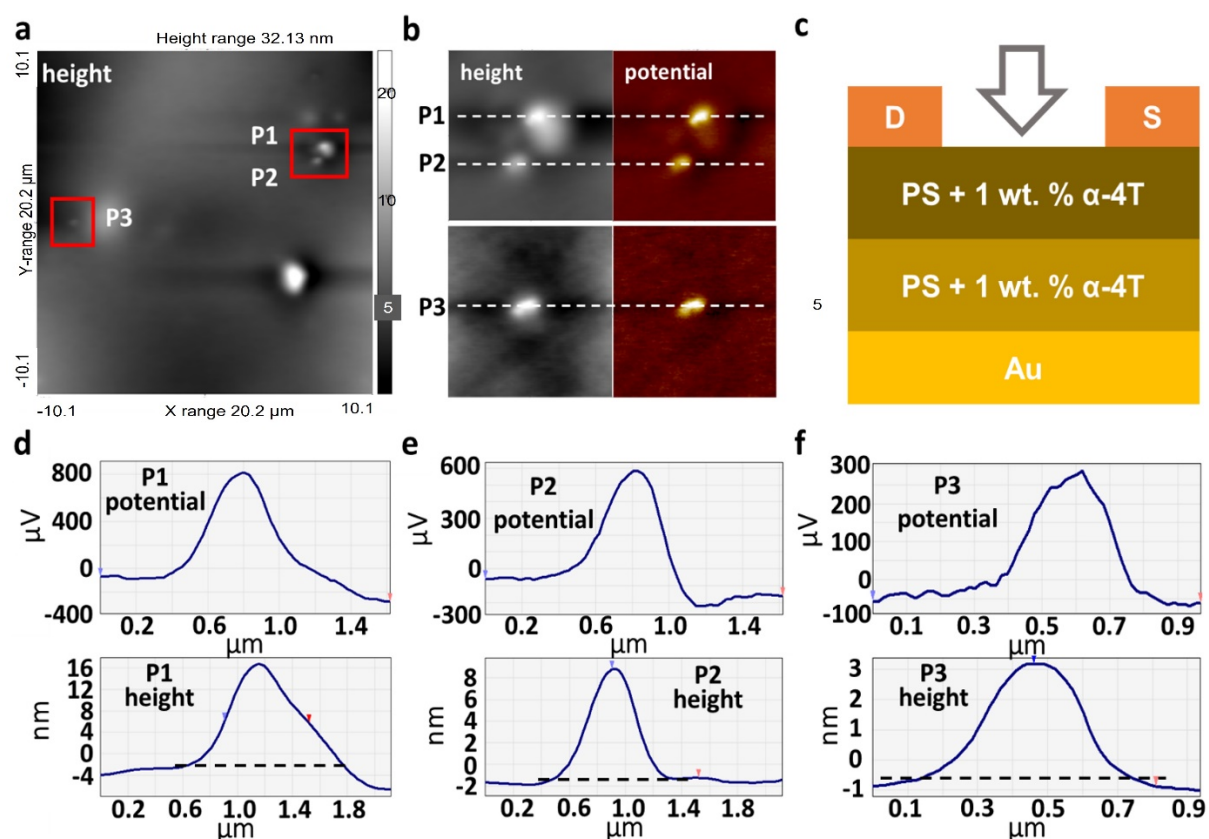


Figure 9. KPFM of 4T&4T bilayer sample before charging. **(a)** Height profile measurement using KPFM of 20.2 $\mu\text{m} \times 20.2 \mu\text{m}$ area with three observed crystallites P1, P2, and P3. **(b)** Height and electric potential measurement by KPFM over region identified for P1, P2, and P3. **(c)** Schematic of device structure identifying region of sample scanned via KPFM. **(d)** Potential and height measurements across line P1 in **(b)** containing P1 crystallite. **(e)** Potential and height measurements across line P2 in **(b)** containing P2 crystallite. **(f)** Potential and height measurements across line P3 in **(b)** containing P3 crystallite.

We assume that the amount of charge stored in a crystallite is proportional to the

capacitance between the tip and the PS *around* the crystallite, and the voltage measured over the crystallite. This only requires that the reference (silver paste) electrode equilibrate zero voltage to the gold electrodes, and thus to the polymer film in general (**Figure S12**), with the crystallites being perturbations of that reference voltage. We have measured the difference in film capacitance before and after charging and found it to be insignificant for samples with < 2 wt% of crystallites, where there is no percolation among crystallites.

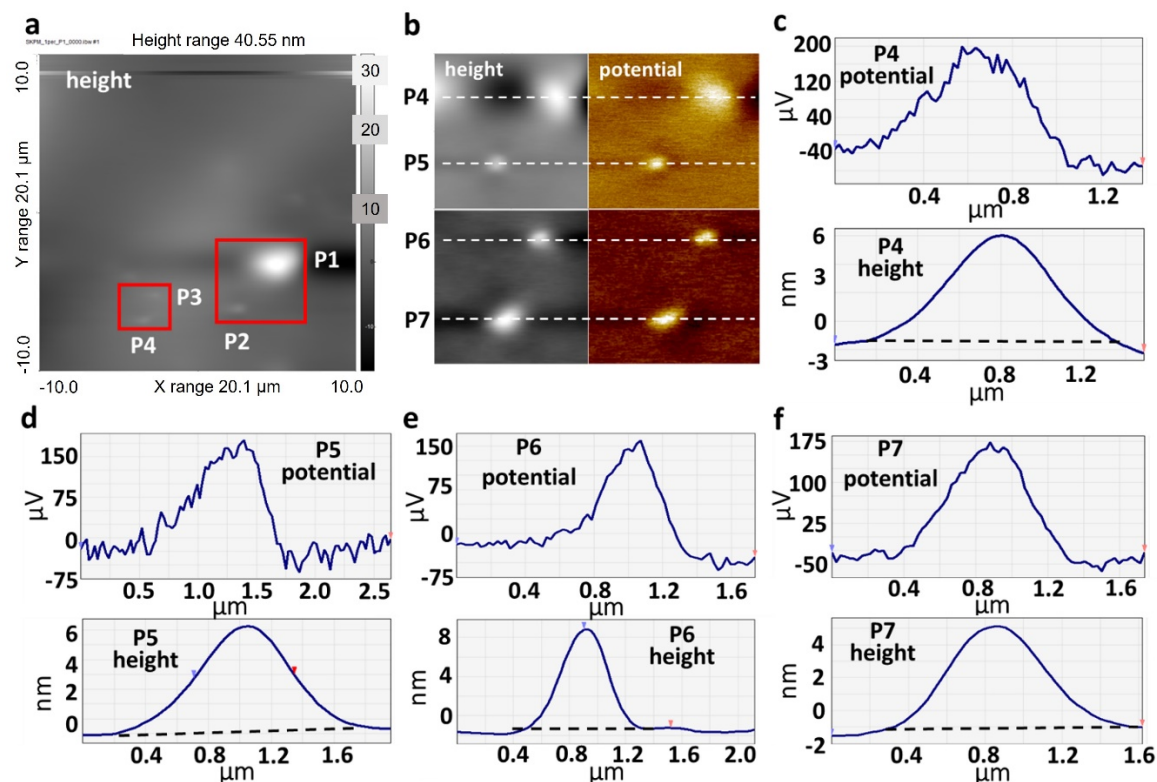


Figure 10. KPFM of 4T&4T bilayer sample before charging. **(a)** Height profile measurement using KPFM of 20.2 μm x 20.2 μm area with four observed crystallites P4, P5, P6, and P7. **(b)** Height and electric potential measurement by KPFM over region identified for P4, P5, P6, and P7. **(c)** Potential and height measurements across line P4 in **(b)** containing P4 crystallite. **(d)** Potential and height measurements across line P5 in **(b)** containing P5 crystallite. **(e)** Potential and height measurements across line P6 in **(b)** containing P6 crystallite. **(f)** Potential and height measurements across line P7 in **(b)** containing P7 crystallite.

The average tip-to-sample potential differences at the crystallites relative to the surroundings for before charging (**Table S3a, P1-P7**) and after charging (**Table S3b, P1A-P4A**) were calculated to be 370 μV and 1300 μV, respectively. This suggests that there is ~3.5x higher concentration of charges stored near the crystallites after charging compared to before charging, supporting the conclusion from **3.2.2** that charges are localized near the crystallites

in PS dielectrics when they are charged in the polarity that favors charge injection.

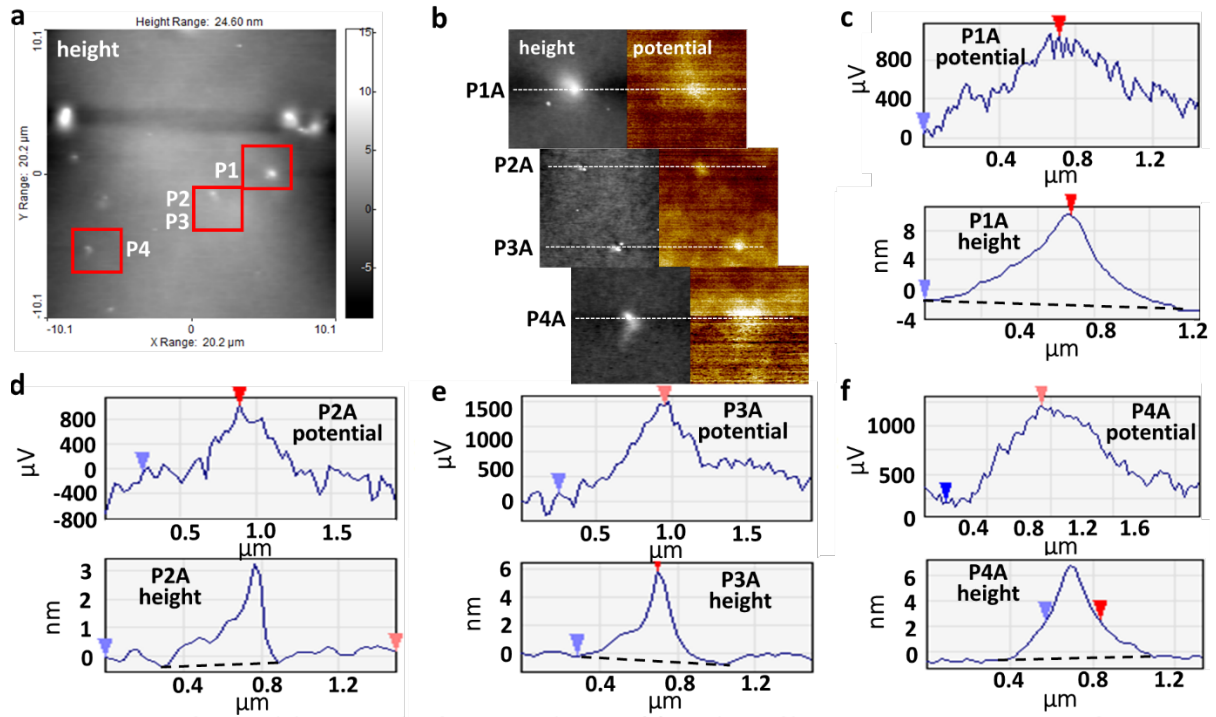


Figure 11. KPFM of 4T&4T bilayer sample after 5 min charging. (a) Height profile measurement using KPFM of 20.2 μm x 20.2 μm area with four observed crystallites P1A, P2A, P3A, and P4A. (b) Height and electric potential measurement by KPFM over region identified for P1A, P2A, P3A, and P4A. (c) Potential and height measurements across line P1A in (b) containing P1A crystallite. (d) Potential and height measurements across line P2A in (b) containing P2A crystallite. (e) Potential and height measurements across line P3A in (b) containing P3A crystallite. (f) Potential and height measurements across line P4A in (b) containing P4A crystallite.

3.3.3. Maximum Charge Storage Capacity of 4T&PS, 4T&4T, PS&4T, and PS&PS before dielectric discharge or loss of dielectric stability.

Once the maximum charge storage capacity of the dielectrics in OFETs is reached during charging, subsequent charging results in breakdown, with a net decrease in transfer curve current values from subsequent -70V charging cycles (**Figure 12**). This also serves as a test of operational endurance. Maximum charge storage capacity evaluations were made where OFETs were charged systematically until either *1. a decrease in drain current at $V_{GS} = -70 V$ compared to transfer curves before charging* or *2. a burst release of charges leading to destabilization of the dielectric was observed*. (These two criteria are similar to criteria established for dielectric breakdown voltages.)³³⁻³⁵ For testing efficiency, the first two charging

cycles were 90 and 60 minutes, with ensuing charging cycles of 10 minutes duration. OFET samples 4T&PS and PS&4T exhibited slow release of excess charges without burst release of charges. Decrease in drain current at $V_{GS} = -70$ V after charging was used as the criterion for maximum charge storage to have been reached for those samples. These samples were thus stable to at least five total hours of charging time (in air), which would be much more demanding than ordinary operational time. OFET samples 4T&4T and PS&PS additionally exhibited burst release of charges, as another indication of charge storage saturation for those dielectrics.

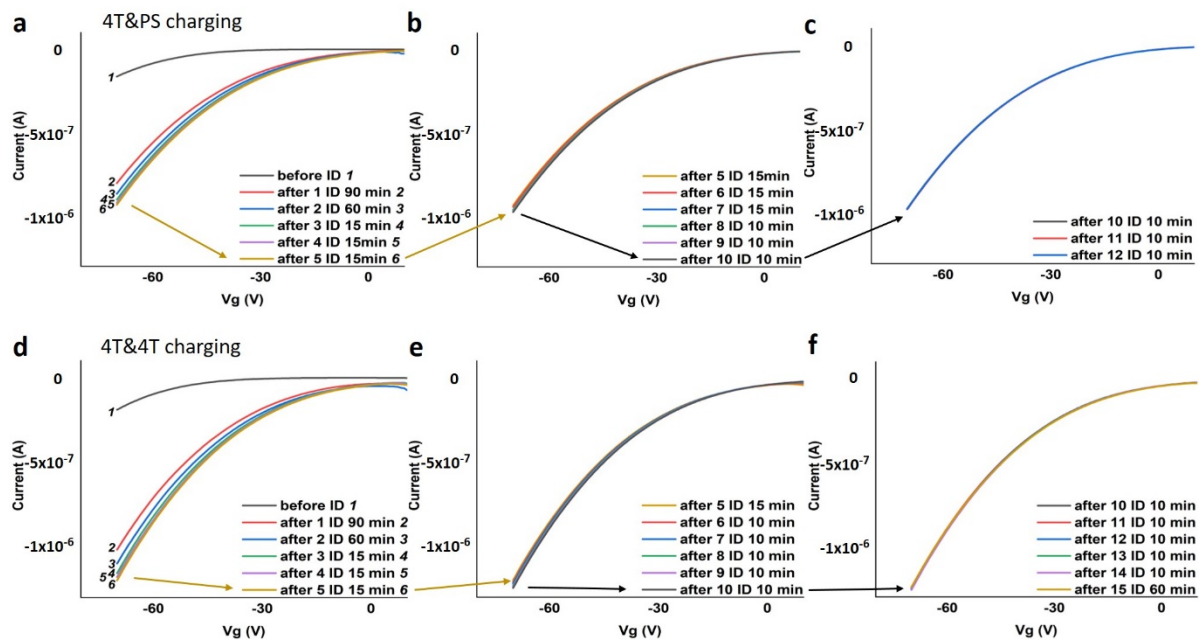


Figure 12. Charging to maximum capacity transfer curves after charging cycles. **(a)** Transfer curves for before and after 5 charging cycles for 4T&PS sample. **(b)** Transfer curves for after 5-10 charging cycles for 4T&PS sample. **(c)** Transfer curves for after 10-12 charging cycles for 4T&PS sample. Decrease in I_D at $V_{GS} = -70$ V was observed for transfer curve after 12 ID showing charge storage saturation of the 4T&PS sample. **(d)** Transfer curves for before and after 5 charging cycles for 4T&4T sample. **(e)** Transfer curves for after 5-10 charging cycles for 4T&4T sample. **(f)** Transfer curves for after 10-15 charging cycles for 4T&4T sample. Decrease in I_D at -70 V_{GS} was observed for the transfer curve after the 15th charging cycle showing charge storage saturation of the 4T&4T sample. “Before ID 1” means ID plotted before the first charging. “After x” means charging after curve x was recorded.

Charging to maximum capacity is shown in **Figure 12a-c.** for 4T&PS and **Figure 12d-f.** for 4T&4T. Maximum charging capacity was observed from decrease in drain current at $V_{GS} = -70$ V for both 4T&PS and 4T&4T transfer curves. **Figure S13** shows specifically at 2178

seconds (321.3 minutes) out of 3600 seconds during the 15th charging cycle, maximum charging capacity of the 4T&4T samples was reached as evident from the burst release of charges at this time, which we assume was a sign of breakdown of the charge-storing system. Sometimes a brief flash of light was observed at the time of this burst of charges. Charging to maximum capacity is shown in **Figure 13a-c.** for PS&4T and **Figure 13d.** for PS&PS. Maximum charging capacity was observed as a decrease in transfer curve current between curves 5 and 6 for PS&4T, while transfer curve destabilization was observed in PS&PS during acquisition of curve 4.

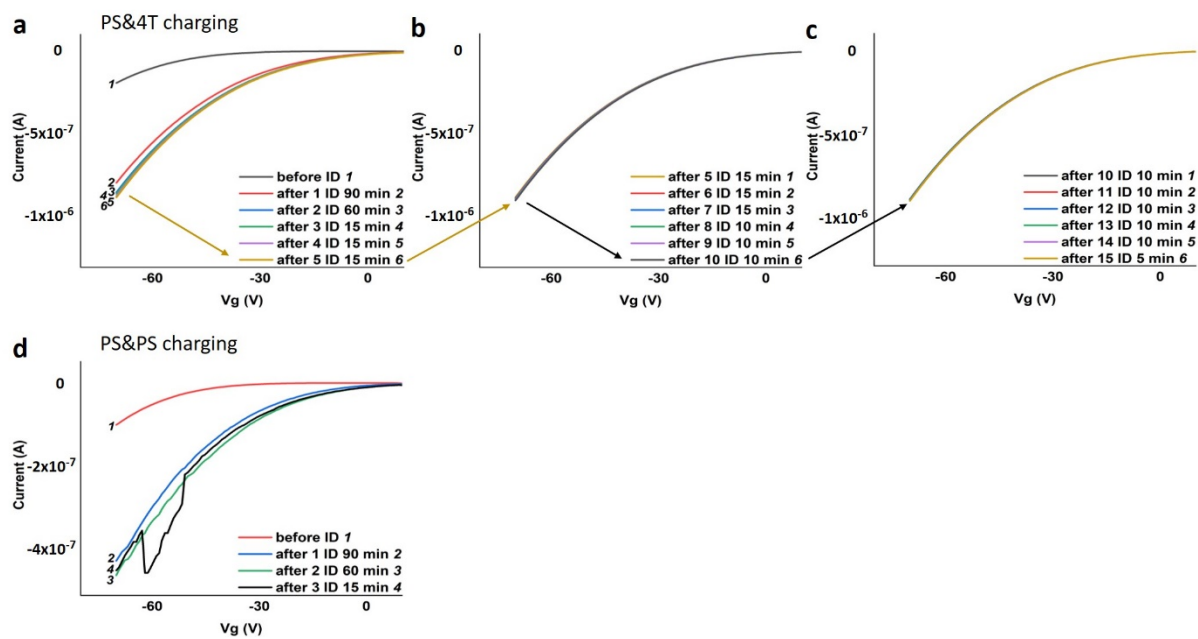


Figure 13. Charging to maximum capacity transfer curves after charging cycles. **(a)** Transfer curves for before and after 5 charging cycles for PS&4T sample. **(b)** Transfer curves for after 5-10 charging cycles for PS&4T sample. **(c)** Transfer curves for after 10-15 charging cycles for PS&4T sample. Decrease in I_D at $V_{GS} = -70$ V was observed for transfer curve after 15 ID showing saturation of dielectric of PS&4T sample. **(d)** Transfer curves for before and after 3 charging cycles for PS&PS sample. Destabilization of transfer curve after 3 ID indicates saturation of dielectric of PS&PS sample.

<i>Samples</i>	Before ID "a"	After 2 ID "a"	Max. Charges Stored	Charge Time to Max. Capacity	Max. Volume Charge Density (charges/cm ³)
PS&PS	10	-22	2.7x10 ⁹	Less than 150 min	8.9 x 10 ¹⁵ charges/cm ³
	Before ID "a"	After 14 ID "a"	Max. Charges Stored	Charge Time to Max. Capacity	Max. Volume Charge Density (charges/cm ³)
PS&4T	6.0	-32	3.6x10 ⁹	285 min	1.3 x 10 ¹⁶ charges/cm ³
	Before ID "a"	After 11 ID "a"	Max. Charges Stored	Charge Time to Max. Capacity	Max. Volume Charge Density (charges/cm ³)
4T&PS	3.6	-38	3.7x10 ⁹	265 min	1.3 x 10 ¹⁶ charges/cm ³
	Before ID "a"	After 14 ID "a"	Max. Charges Stored	Charge Time to Max. Capacity	Max. Volume Charge Density (charges/cm ³)
4T&4T	9.8	-41	4.3x10 ⁹	321.3 min	1.5 x 10 ¹⁶ charges/cm ³

Table 2. Derived parameter “a” values for calculating maximum charges stored for each of the four types of samples and the charge times to maximum capacity. The thickness of PS&PS was 5501nm, PS&4T was 5205nm, 4T&PS was 5061nm, and 4T&4T was 5113nm.

Two parameters, maximum charges stored as determined by Δa , and charging time to maximum capacity (a measure of durability as well as charge storage capacity) were analyzed from section 2.2.3. was used to derive parameter “a” for transfer curves in **Figures 12 and 13.** Subsequently, the product of dielectric capacitance and Δa was used to calculate maximum cumulative charges stored, shown in **Table 2.** Similar results were observed for both parameters for 4T&PS and PS&4T samples, showing that the α 4T crystallite content is the critical factor controlling the maximum volume charge density (VCD) of dielectrics. **Table 2.** Shows that compared to PS&PS, PS&4T as well as 4T&PS had 46% increased maximum VCD while 4T&4T had 69% more maximum VCD. The percentage of maximum VCD reached after 5 min charging (**Table 3.**) was calculated by comparing the maximum VCD (from **Table 2**) to volume charge density after 5 min -70V charging (from **Table 1**). PS&PS reached 75% of maximum VCD after 5 min charging whereas samples containing 4T reached an average 91% of maximum VCD after 5 min charging.

	Ave. Volume Charge Density (charges/cm ³) after -70V 5min (Table 1b.)	Max. Volume Charge Density (charges/cm ³) from Table 2	% of Max. Volume Charge Density reached after 5 min charging
PS&PS	$6.7 \times 10^{15} \pm 1.6 \times 10^8$	8.9×10^{15}	75 %
PS&4T	$1.1 \times 10^9 \pm 1.9 \times 10^8$	1.3×10^{16}	85 %
4T&PS	$1.3 \times 10^9 \pm 1.4 \times 10^9$	1.3×10^{16}	100 %
4T&4T	$1.3 \times 10^8 \pm 1.2 \times 10^8$	1.5×10^{16}	87 %

Table 3. Percentage of maximum VCD reached after 5 min charging was calculated by comparing the maximum VCD from **Table 2** to volume charge density after 5 min -70V charging from **Table 1**.

3.3.4. Dielectric Breakdown Experiments to Investigate Charge Movement under Electrical Bias

The free-volume theory of dielectric breakdown postulates that dielectric breakdown of polymers in high electric field occurs by electrons accelerating in free volume in polymer dielectrics at high electric fields.¹³ It has been proposed that for polystyrene specifically, strong electric fields induce pi-pi reorganization perpendicular to the electric field, forming a path in which charge carriers can accelerate to cause an avalanche of electrons, breaking down the polystyrene dielectric.¹⁴

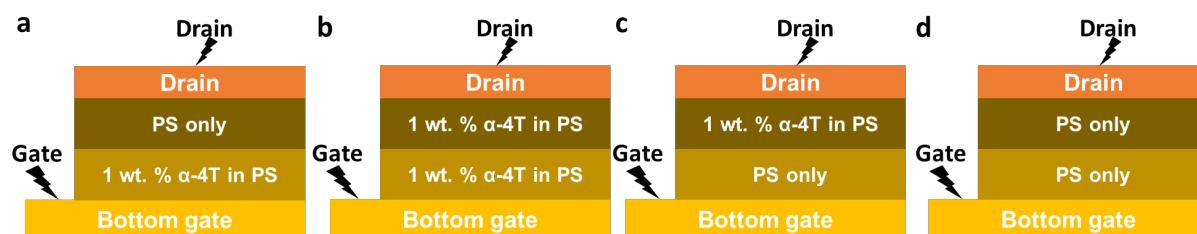


Figure 14. Device structure for dielectric breakdown experiments of (a) 4T&PS, (b) 4T&4T, (c) PS&4T, (d) PS&PS.

Based on this, we performed dielectric breakdown experiments on the four dielectric configurations in capacitor structures (**Figure 14**). 4T&PS did not show notable dielectric breakdown even at 300V net electrical bias, which was a clearly discernable improvement over the other bilayers. Further details are given in the Supporting Information.

4. Discussion and Conclusion

Positive charging at the source and drain electrodes (discharging) can result in electrons in the dielectric being drawn into the pentacene OSC as shown in **Figure 15a**. This would lead to hole trapping in the pentacene OSC by those electrons, decreasing the conductance, a form of bias stress. This increases V_{TH} , because more gate voltage needs to be applied to fill the traps. On the other hand, negative charging at the source drain electrodes leads to creation of holes in the pentacene left behind by electrons injected into the dielectric, as shown in **Figure 15b**, increasing the conductance. This decreases V_{TH} , because these newly created holes can fill traps, so less gate voltage is needed before mobile holes are induced. Another way to view this V_{TH} decrease is as an additional electric field imposed in series with the applied gate field, with the positive voltage near the gate electrode and the negative voltage in the dielectric, again as shown in **Figure 15b**.

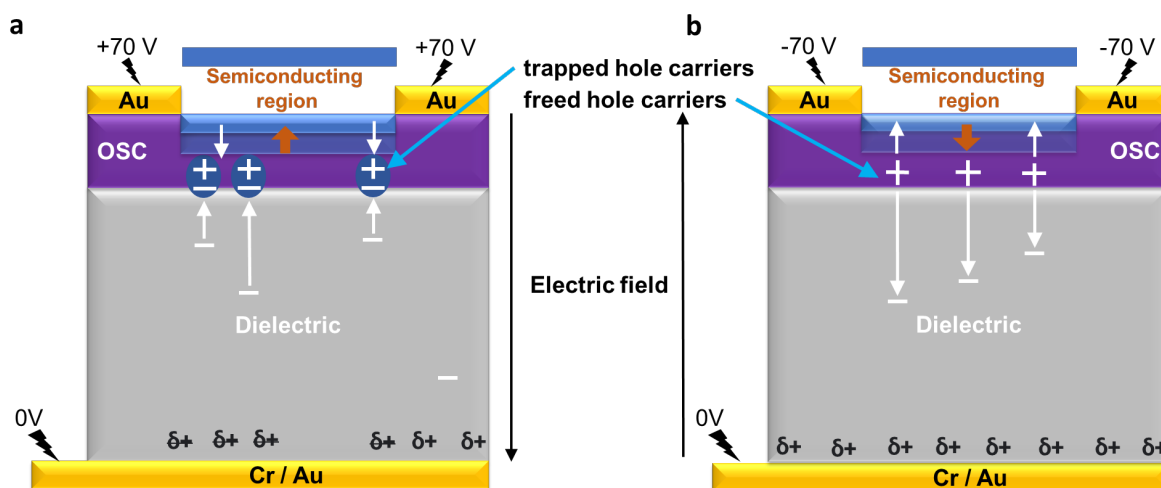


Figure 15. (a) Schematic showing movement of charges in OFET devices with dielectric made of PS-only during positive charging. (b) Movement of charges in the same device with negative charging.

OFETs with $\alpha 4T$ in PS show analogous but in many cases differentiated charging responses. Positive charging gave less bias stress with $\alpha 4T$ present, possibly because electrons were held more tightly by positive charges localized on crystallites, inhibiting their injection into pentacene, as shown schematically in **Figure 16a**. Negative charging gave larger current increases as electrons were injected more readily into the dielectric, where they would be partially compensated by positively charged crystallites, and also stabilize holes left behind in the pentacene possibly because of a higher electric field in series with the gate, as shown schematically in **Figure 16b**.

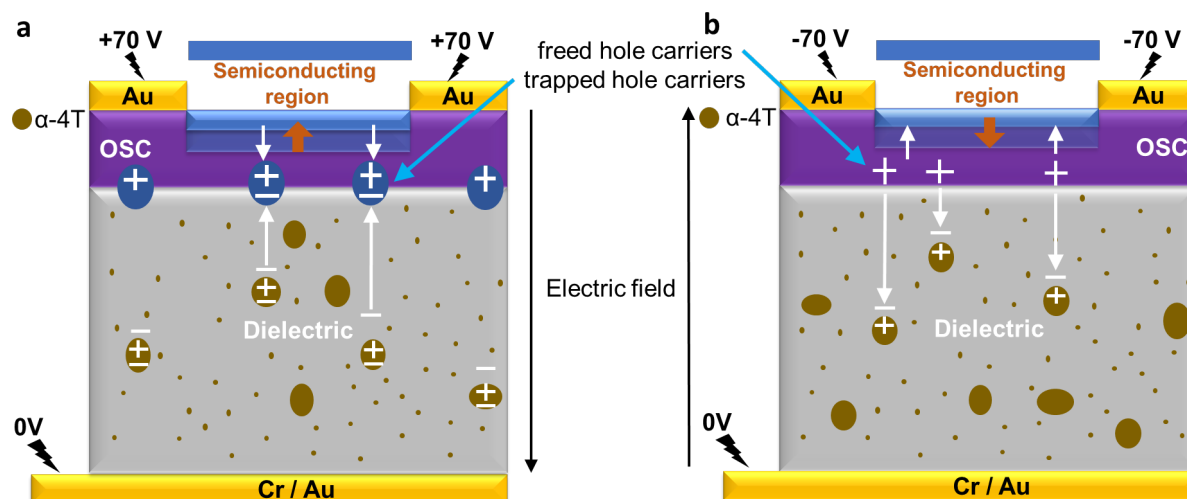


Figure 16. (a) Schematic showing movement of charges in OFET devices with dielectric made of PS with α 4T crystallites during positive charging. Electron injection into the pentacene from the dielectric is inhibited by the electrons being attracted to positively charged crystallites. (b) Schematic showing movement of charges in the same devices during negative charging. Electron injection from pentacene into the dielectric is promoted, increasing the concentration of mobile holes in the pentacene.

The behavior of charges in and around electroactive α 4T crystallites in dielectric PS matrix at varying positions was investigated. XRD, SEM/EDS, and confocal microscopy proved the formation of α 4T crystallites in the PS matrix. A modified saturation-regime current voltage relationship was used to derive parameter “a”, a marker of shifting V_{TH} , to calculate Δa for OFET transfer curves before and after charging. Charges stored/released upon charging of OFET devices were quantified by the Δa and capacitance measurements. Results showed that α 4T crystallites increased maximum charge storage capacity as well as charge retention capability of dielectrics. KPFM experiments showed that charges localize near the α 4T crystallites upon charging, validating the point that α 4T crystallites in PS affect the electrical properties of the dielectric and therefore OFET devices. Trilayer experiments (section 3.3.4. of SI) validated the charge retention improvement of α 4T crystallite embedded PS dielectrics. It was also noted that better charge retention was observed for 4T&PS dielectric OFETs compared to PS&4T dielectric OFETs. This was attributed to 4T&PS storing of injected electrons farther from the source of potential bias. Similar phenomena were observed in dielectric breakdown experiments (section 3.3.5. of SI) of 4T&PS to prevent dielectric breakdown, highlighting the importance of the location of electroactive crystallites on the electrical properties of engineered electrical devices. These bulk effects are more likely

contributors to charge storage than the minimal interfacial polarization expected from a pentacene-PS interface.³⁶ The observed charge storage behavior of electroactive crystallite embedded dielectric layers may be harnessed to enhance dielectrics used for capacitive energy storage.³⁷⁻³⁸

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Supporting information. The supporting information document contains details about film and device fabrication,³⁹⁻⁴⁵ film and device characterization,^{46,47} film characterization and measurements, V_{TH} calculation,⁴⁸⁻⁵⁰ charge storage calculation, charge retention, and dielectric breakdown.^{51,52}

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31. See supplemental material at xx for film device fabrication methods; film characterization and measurements; mobility and parameter “a” calculations; calculations for number of charges stored; IV curve showing burst release of 4T&4T sample during charging; charge retention and dielectric breakdown experiments; and representative individual transfer curves of OFET devices. The Supplemental Material also contains Refs. [7,16,17,22,30]

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