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Phys. Rev. Lett. **124**, 067701 — Published 14 February 2020

DOI: [10.1103/PhysRevLett.124.067701](https://doi.org/10.1103/PhysRevLett.124.067701)

Fast gate-based readout of silicon quantum dots using Josephson parametric amplification

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(Dated: January 23, 2020)

Spins in silicon quantum devices are promising candidates for large-scale quantum computing. Gate-based sensing of spin qubits offers compact and scalable readout with high fidelity, however further improvements in sensitivity are required to meet the fidelity thresholds and measurement timescales needed for the implementation of fast-feedback in error correction protocols. Here, we combine radio-frequency gate-based sensing at 622 MHz with a Josephson parametric amplifier (JPA), that operates in the 500–800 MHz band, to reduce the integration time required to read the state of a silicon double quantum dot formed in a nanowire transistor. Based on our achieved signal-to-noise ratio (SNR), we estimate that singlet-triplet single-shot readout with an average fidelity of 99.7% could be performed in 1 μ s, well-below the requirements for fault-tolerant readout and 30 times faster than without the JPA. Additionally, the JPA allows operation at a lower RF power while maintaining identical SNR. We determine a noise temperature of 200 mK with a contribution from the JPA (25%), cryogenic amplifier (25%) and the resonator (50%), showing routes to further increase the read-out speed.

Quantum computers require high-fidelity qubit measurement, which must be performed on a timescale faster than the decoherence time to perform quantum error correction [1]. Spin qubits formed in quantum dots (QDs) or donors in silicon are one of the most promising platforms for scalable quantum information processing due to their long coherence times and large integration density [2–7]. When scaling to large arrays of dense qubits [2–4, 16] space for additional electrometers and reservoirs, typically required for readout based on spin dependent tunneling [8, 9], is limited. Gate-based dispersive RF readout eliminates the need for such additional local structures by embedding the gates that define the QD into a resonant circuit and using Pauli spin blockade [17–22, 24]. Recently, single-shot readout of the singlet-triplet states in a double QD has been demonstrated with gate-based sensors, using a variety of resonator parameters to achieve a range of readout fidelities (for a given integration time): 73% (2.6 ms) [25], 82.9% (300 μ s) [26], 98% (6 μ s) [27] to 99% (1 ms; using ancillary ‘sensor’ QD and reservoir) [28].

Amplifiers based on Josephson junctions have greatly improved signal-to-noise ratios (SNRs) in the field of superconducting circuits [29–36]. Adopting such approaches in the measurement of QDs at RF/microwave frequencies is expected to lead to corresponding improvements in SNR. This can in principle be achieved at operating frequencies of 4–8 GHz that are typical for Josephson-junction based amplifiers, as demonstrated us-

ing an InAs double QD, Josephson parametric amplifier (JPA) and coplanar waveguide resonator [37]. However, lower frequency operation (\lesssim 1 GHz) becomes necessary [38] for studying lower QD tunneling rates, at which exchange interaction is more easily controlled, and for enabling off-chip resonator fabrication. Suitable amplifiers are available in such a frequency range, for example: a JPA operating at 600 MHz with a noise temperature of $T_{\text{JPA}} = 105$ mK [39] or a SQUID amplifier chain with $T_{\text{SQUID}} = 52$ mK at 538 MHz [40]. Building on such developments, readout of a GaAs based quantum dot at 196 MHz with a noise temperature of 490 mK was recently reported using a SQUID amplifier [41].

In this Letter, we combine RF capacitive gate-based sensing of silicon QDs with Josephson parametric amplification to push the bounds of SNR that can be achieved using this technique. We use a well-matched lumped-element high Q resonator containing a NbN spiral inductor and a JPA that operates in the 500–800 MHz band and obtain an overall noise temperature $T_{\text{noise}} = 200$ mK at 621.9 MHz. We benchmark the sensitivity of the method using electronic transitions in a silicon multi-dot devices with large gate-coupling (wrap-around geometry) fabricated following CMOS processes.

The setup shown in Fig. 1(a) consists of i) the cryogenic RF delivery and amplification chain including the JPA (pink background); ii) a lumped-element LC resonator (green); and iii) the silicon quantum dot device (blue), see [49] for details. The LC resonator is probed

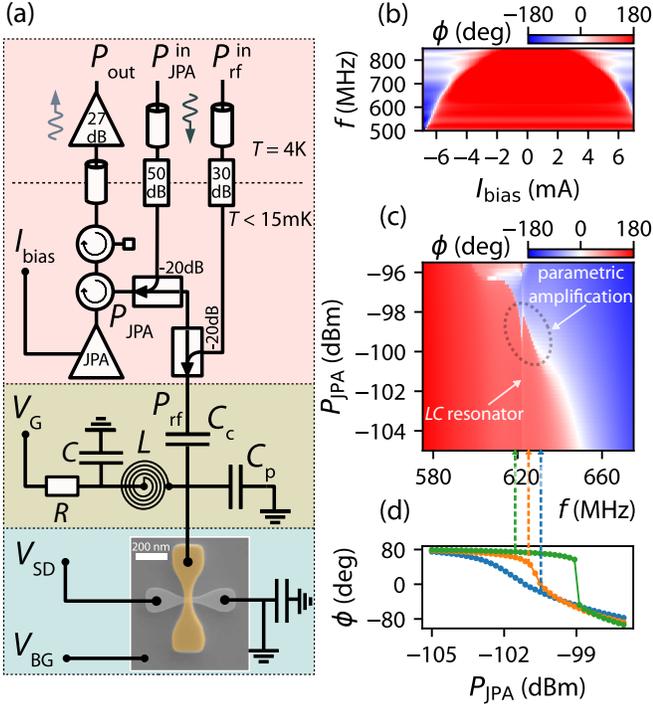


FIG. 1. **Set-up and Josephson Parametric Amplifier (JPA).** (a) Schematic of the quantum dot readout setup consisting of microwave components (including a JPA), readout resonator and CMOS quantum dot device (false-colored SEM micro-graph). (b) Phase response of the JPA as a function of flux bias I_{bias} demonstrating frequency tuning of the JPA. (c) JPA phase response as a function pump power and frequency. The regime of parametric amplification and the readout resonator are indicated. (d) JPA transfer function obtained from line-cuts at indicated frequencies in (c). The bi-stable regime is observed as an abrupt jump in ϕ .

using an RF tone with power P_{rf} near resonant frequency f_{rf} . At this frequency, parametric changes in device capacitance, ΔC_d , due to cyclic single-electron tunneling produce changes in the reflection coefficient $\Delta\Gamma = |\frac{\partial\Gamma}{\partial C_d}\Delta C_d|$ [42]. This effect translates into a change in the reflected power with an SNR given by

$$\text{SNR} = |\Delta\Gamma|^2 \frac{P_{\text{rf}}}{P_n},$$

where P_n is the noise power.

The optimal SNR is achieved by maximizing $\Delta\Gamma$, maximizing P_{rf} (while remaining below power broadening) and minimizing P_n . $\Delta\Gamma$ is maximized for large loaded quality factor and small parasitic capacitance, which translates into a large resonator impedance, combined with large gate coupling and a well-matched resonator [23].

The noise power for an amplifier with gain G can be defined as $P_{n,\text{out}} = Gk_B(T_{\text{sys}} + T_n)B$, where T_{sys} and T_n are the system and amplifier noise temperature (noise added by the amplifier) respectively, k_B is Boltzmann's con-

stant and B is the amplifier bandwidth. In semiconductor QD measurements, cryogenic high electron mobility transistor (HEMT) amplifiers operating at 4 K typically limit the effective noise temperature (T_{HEMT}) to a few Kelvin. By including an additional amplifier (such as a JPA) with gain $G_{\text{JPA}} (\gg 1)$ and lower noise temperature (T_{JPA}) at the beginning of the amplification chain, the effective noise temperature T_{noise} can be reduced:

$$T_{\text{noise}} = T_{\text{sys}} + T_{\text{JPA}} + \frac{T_{\text{HEMT}}}{G_{\text{JPA}}}. \quad (1)$$

For a JPA operating at $T = 10$ mK we expect a minimum of $T_{\text{JPA}} = \frac{\hbar\omega}{2k_B} \coth\left(\frac{\hbar\omega}{2k_B T}\right) = 16.5$ mK.

As shown in Fig. 1(a), the RF signal reflected from the quantum device passes the JPA (which works in reflection) via a circulator and is amplified at 4 K followed by further amplification and quadrature demodulation at room-temperature (not shown). Our JPA is a low quality factor ($Q_{\text{JPA}} < 100$) superconducting resonator consisting of a SQUID loop array with tunable inductance shunted by a fixed capacitance [43], and is tunable in frequency from 500–800 MHz, as shown in Fig. 1(b), by passing a current I_{bias} through a coil that changes the flux through the nearby SQUIDS. The JPA is pumped via the signal input port and with power P_{JPA} . At low drive power the JPA behaves like a linear resonator, while at high power the non-linearity of the Josephson junctions manifests in a frequency shift of the JPA to lower frequency until eventually the JPA reaches a bi-stable regime as shown in Fig. 1(c) [43]. In the regime useful for parametric amplification, the phase of the reflected pump signal varies rapidly with the pump power, as shown in Fig. 1(d), which represents the transfer function of the JPA.

The dynamic range of this JPA is of the order of -130 dBm, making it unsuitable for the signal powers commonly used in previous reflectometry measurements (-90 to -80 dBm) [18]. Here, we overcome this limitation using a high quality factor LC resonator that is well-coupled to the input line, achieving a drive amplitude required to obtain a dispersive response at lower input power. The resonator circuit is formed by the parallel combination of a NbN spiral inductor $L = 170$ nH, parasitic capacitance and the geometric quantum dot device capacitance ($C_p + C_d = 380$ fF), all coupled to the RF line via a coupling capacitor ($C_c = 37$ fF). We observe a resonance in the reflection coefficient $\Gamma = |\Gamma| \exp(i\phi)$ at $f_{\text{rf}} = 1/2\pi\sqrt{L(C_c + C_p + C_d)} = 621.9$ MHz with a loaded quality factor of $Q_{\text{load}} = 966$, impedance $Z = \sqrt{L/(C_p + C_c + C_d)} = 650\Omega$, return loss of 3 dB and phase shift $> 180^\circ$ (over-coupled) as shown in Fig. 2(b). When operating at a charge instability in the QD device, the resonator reaches perfect matching. We operate the JPA in phase-preserving mode, where there is an offset $\Delta f = f_{\text{JPA}} - f_{\text{rf}}$ between the JPA pump frequency

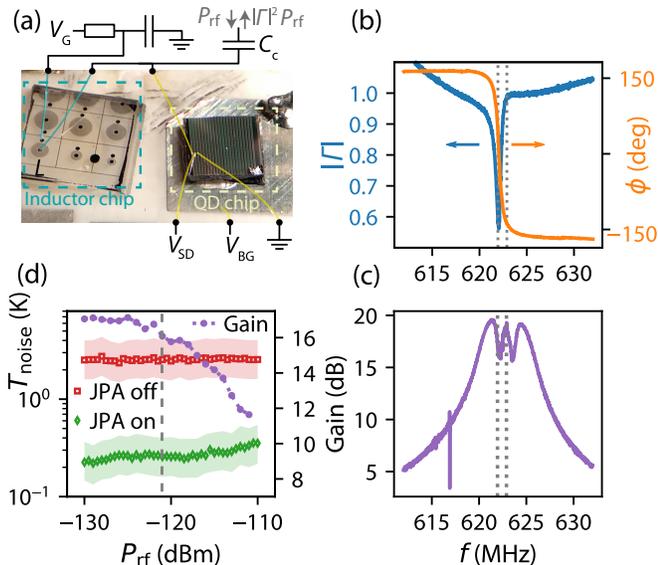


FIG. 2. **Characterizing the LC resonator and its operation with the JPA.** (a) Shows the resonant circuit, including a schematic of the on-PCB components and bond-wires, the NbN spiral inductor, and the quantum dot device on a separate chip. (b) Magnitude and phase of the reflection coefficient showing the readout resonator ($Q_{\text{load}} = 966$). (c) Gain profile of the JPA when tuned close to the readout resonator frequency (3-dB-bandwidth of 6 MHz). (d) JPA gain and estimated system noise temperature at f_{rf} as a function of RF input power showing saturation at high input power (1-dB-compression at -121 dBm).

(f_{JPA}) and f_{rf} , so power from the JPA pump is transferred onto f_{rf} and $f_{\text{JPA}} + \Delta f$ (four-wave mixing) via double-sideband phase modulation as illustrated by the transfer function. We select $\Delta f = 1$ MHz to fall between the bandwidth of the resonator $\Delta f_{\text{rf}}^{3\text{dB}} = 0.65$ MHz and the JPA $\Delta f_{\text{JPA}}^{3\text{dB}} = 6$ MHz. This puts f_{JPA} at the edge of the readout resonator to avoid power broadening due to leakage of the pump signal while maximizing gain at f_{rf} . When tuned and pumped, we achieve a gain of 17 dB at f_{rf} as shown in Fig. 2(c). The decrease in gain near f_{rf} is likely due to large impedance variations of the resonator close to resonance and imperfect matching to 50Ω .

Figure 2(d) shows the JPA gain and the effective noise temperature close to f_{rf} as a function of P_{rf} . We identify 1-dB-compression at -121 dBm. Based on amplifier gain estimations ($G_{\text{HEMT}} = 27 \pm 2$ dB) we obtain an effective noise temperature $T_{\text{noise}} = 2.5_{-0.9}^{+1.4}$ K with the JPA off and a minimum noise temperature of $T_{\text{noise}} = 200_{-73}^{+110}$ mK based on the SNR improvement with the JPA on. The effective noise temperature with the JPA on increases with increasing power due to saturation. We calculate the contribution of the cryogenic amplifier to T_{noise} (see Eq. 1) as $\frac{T_{\text{HEMT}}}{G_{\text{JPA}}} = 50_{-18}^{+28}$ mK and estimate T_{JPA} and T_{sys} by comparing T_{noise} when operating the QD device away from or at a charge instability. T_{sys} can

have contributions from the resonator circuit (T_{res}) and the QD device (T_{QD}): $T_{\text{sys}} = (1 - |r|^2)T_{\text{res}} + kT_{\text{QD}}$ [44]. As tunneling between the QD and reservoir occurs adiabatically, no power is dissipated in the device, hence $k = 0$. Based on an increase in T_{noise} of 35_{-13}^{+24} mK when operating at a charge transition (where $|r|$ decreases from 0.5 to 0), we estimate $T_{\text{JPA}} = 47_{-30}^{+35}$ mK and $T_{\text{res}} = 142_{-54}^{+94}$ mK. We relate T_{res} to thermal noise consistent with typical electron temperatures observed for QDs and we note a JPA efficiency of 36% of the quantum limit (equivalent to ~ 1.5 photons) that is compatible with previous results for operation close to a bifurcation point [34, 45, 46].

Next, we characterize and compare the improvements in the SNR of gate-based readout using a quantum dot-to-reservoir transition (DRT) in one CMOS silicon nanowire field-effect transistor device and an inter-donor/dot charge transition (IDT) in another device with nominally identical dimensions [13, 22, 49]. Although we cannot unequivocally determine the nature of the impurity, it presents signatures of a phosphorous atom (see [49] for doping concentration and details on the IDT). QDs form in the corners of the nanowire, as shown in schematic line-cuts of the device along the gate and source-drain direction in Fig. 3(a,b), and have a strong gate coupling $\alpha_{\text{DRT}} = 0.86$, while the donor resides deeper in the channel. When operating at the DRT we observe a signal in the phase as shown in Fig. 3(c) due to a capacitive shift of the resonance corresponding to $\Delta C_{\text{d}} = 0.5$ fF at the maximum. In Fig. 3(d) power broadening of the transition is shown. No broadening occurs for P_{rf} below -120 dBm, while the transition is significantly broadened above -110 dBm. Due to the high Q_{load} of the resonator, only a small input power, compatible with the dynamic range and saturation of the JPA, is required to perform readout and we calculate the RF disturbance at the device gate as $V_{\text{rf}}^{\text{PP}} = \frac{2C_{\text{c}}}{C_{\text{c}} + C_{\text{p}} + C_{\text{d}}} Q_{\text{load}} V_{\text{in}}^{\text{PP}}$ (e.g. $V_{\text{rf}}^{\text{PP}} = 13 \mu\text{V}$ at $P_{\text{rf}} = -130$ dBm).

Next, we use conventional methods to measure the charge sensitivity [14, 49] with and without the JPA, which provides a device-specific benchmark on the performance of our gate-based sensor normalized to the gate charge. The SNR as a function of P_{rf} , with and without the JPA, is shown in Fig. 3(e), when operating at a small gate voltage modulation of 50 kHz as indicated in Fig. 3(c). We observe an improvement of up to 8 dB in SNR with the JPA at low RF power. Irrespective of whether the JPA is used, for P_{rf} between -130 and -120 dBm the SNR levels off as the DRT begins to become power broadened, and it drops abruptly for powers above -110 dBm. With the JPA on saturation leads to an additional decrease in SNR above -120 dBm. The JPA can either be used to increase the SNR beyond what could otherwise be achieved, and/or to provide the same SNR but at about 10 dB less RF power, with the corre-

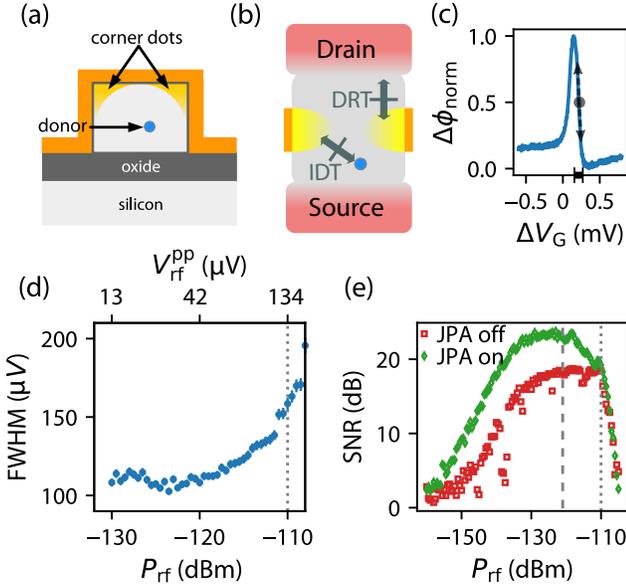


FIG. 3. **Dot-to-reservoir transition (DRT) charge sensitivity.** (a) Schematic cross-section of the QD devices along the gate, and (b) along the source and drain, showing two QDs in the top corners of the nanowire and a donor in the channel. Transitions at which measurements are performed are indicated. (c) Typical phase response across a DRT ($P_{\text{rf}} = -125$ dBm). (d) Full-width half maximum (FWHM) of the DRT as a function of power (power broadened at $P_{\text{rf}} > 110$ dBm). (e) SNR of the DRT as a function of RF power. The power at which the JPA saturates (dashed line) and significant power broadening occurs (dotted line) is indicated.

sponding reduction in the disturbance of the QD being measured, and its neighbors. When operating well below power broadening ($P_{\text{rf}} = -130$ dBm), the charge sensitivity achieved with the JPA is $0.25 \mu\text{e}/\sqrt{\text{Hz}}$ compared to $0.5 \mu\text{e}/\sqrt{\text{Hz}}$ without the JPA, outperforming previous measurements using RF-SET [15] and gate-based approaches [23].

Finally, we estimate spin-readout fidelity using a donor-dot IDT of even parity that exhibits features of spin blockade [20, 49]. Figure 4(a) shows the IDT in the normalized phase response as a function of V_G and back-gate voltage (V_{BG}), where the donor transition can be identified due to a stronger coupling to V_{BG} . We determined the effective charge occupation of the donor and QD using the magnetic field response and we calculate a gate coupling $\alpha_{\text{IDT}} = 0.36$ and tunnel coupling $\Delta_c = 20.9 \mu\text{eV}$, corresponding to a capacitive shift of $\Delta C_d = 0.5$ fF [49]. Figure 4(b) shows an example of the phase response across the IDT, along the detuning axis ε shown in Fig. 4(a) for $\tau_{\text{int}} = 1 \mu\text{s}$ and $\tau_{\text{int}} = 52 \mu\text{s}$. We determine the power SNR from the amplitude $\Delta\phi$ of the IDT signal and the RMS amplitude σ of the noise as $\text{SNR} = \frac{\Delta\phi^2}{\sigma^2}$. Figure 4(c) shows the SNR as a function of integration time with the JPA on and off. Us-

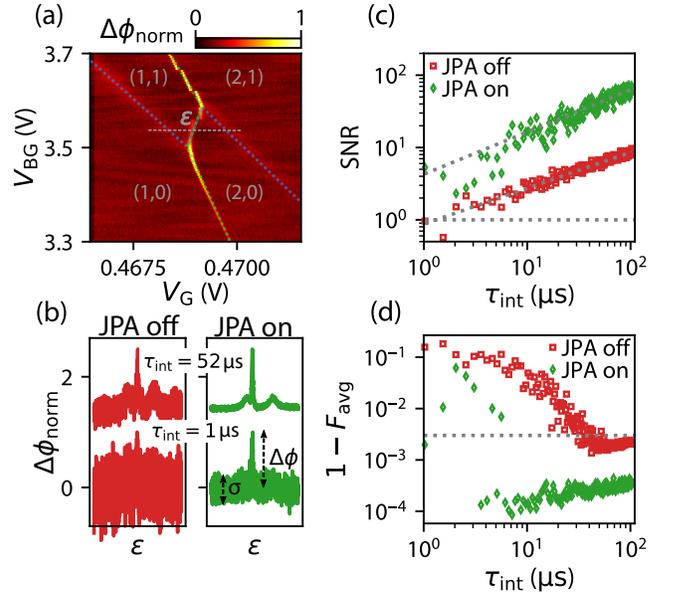


FIG. 4. **Inter-donor/dot charge transition (IDT).** (a) Even-parity IDT between a donor and QD in the device. Electron occupation ($N_{\text{dot}}, N_{\text{donor}}$) indicated, up to an arbitrary offset. (b) Normalized phase response along the detuning axis ε in (a) with and without the JPA for two different integration times (traces offset by 1.5 in $\Delta\phi$ for clarity). (c) SNR obtained from traces as shown in (b) as a function of integration time with JPA on and off. Linear extrapolation and $\text{SNR} = 1$ is indicated using dotted lines. (d) Simulated average readout infidelity $1 - F_{\text{avg}}$ as a function of τ_{int} . The horizontal dotted line indicates $F_{\text{avg}} = 0.997$.

ing an extrapolation (dotted lines in Fig. 4(c)) we infer an SNR of unity at $\tau_{\text{int}}^{\text{off}} = 1.2 \mu\text{s}$ and $\tau_{\text{int}}^{\text{on}} = 80$ ns with the JPA off and on, respectively. However, the limited bandwidth of our resonator prohibits measurements faster than $1.5 \mu\text{s}$ ($\Delta f_{\text{rf}}^{3\text{db}} = 0.65$ MHz). Additionally, we observe that multiple measurements of the SNR with the JPA on for $\tau_{\text{int}} < 10 \mu\text{s}$ deviate from the extrapolation, which could be due to noise introduced by the JPA pump signal which is operated only 1 MHz offset the RF signal. Based on the signal and noise levels (as shown in Fig. 4(c)) we simulate the singlet and triplet readout probability densities to obtain a readout fidelity [11, 49]. The model includes terms to account for relaxation of the triplet during measurement and we assume $T_1 = 4.5$ ms [25] reported in a similar $^{\text{nat}}\text{Si}$ -MOS QD device to demonstrate the improvement in integration time that can be achieved with the JPA. We obtain an average readout infidelity as a function of integration time which is shown Fig. 4(d) for the JPA off and on. We find that $F_{\text{avg}} > 0.997$ can be reached at a $\text{SNR} > 5$ corresponding to an integration time of at least $\tau_{\text{int}}^{\text{off}} = 32 \mu\text{s}$ with the JPA off and $\tau_{\text{int}}^{\text{on}} = 1 \mu\text{s}$ with the JPA on allowing readout faster than the coherence time of electron spins in ^{28}Si ($T_2^* = 120 \mu\text{s}$ [47]).

We have demonstrated that the SNR of RF gate-based

readout of quantum dot devices can be improved using a JPA. We observe a SNR improvement of 8 dB for both dot-to-reservoir and inter-donor/dot transitions when the JPA is operated closed to the RF frequency in phase-preserving mode at 17 dB gain. We have analyzed the performance of the JPA in an external magnetic field, commonly applied in spin qubit devices, and find no disturbance on the JPA performance up to a field of $B_z = 3$ T at the device [49]. The SNR improvement we see is currently limited by the gain of the JPA. Assuming a JPA gain of 23 dB or more, the contribution of the cryogenic amplifier would become negligible. Additionally, the noise added by the JPA can be squeezed below the quantum limit when operating in phase-sensitive mode. Changes in the circuit such as additional isolators between the JPA and readout resonator as well as additional line attenuation and filtering could be beneficial towards achieving larger gain, a lower system noise temperatures and prevent leakage of the JPA pump signal into the readout resonator. In addition, the measurement speed in this implementation is, in principle, limited by the bandwidth of our high- Q readout resonator: increasing the coupling to the line or, preferentially, moving to a higher frequency of the resonator circuit while maintaining high loaded Q should allow sub-microsecond fault-tolerant gate-based spin readout. Further development could reduce the footprint of the high Q resonators, to achieve an integrated and scalable readout architecture [48] with the potential of reduced circuit losses and parasitics. Using a traveling wave parametric amplifier (TWPA) with increased bandwidth, frequency multiplexing of multiple resonators could be achieved.

This research has received funding from the European Union's Horizon 2020 research and innovation programme under grant agreement No 688539 (<http://mos-quito.eu>) and Seventh Framework Programme (FP7/2007-2013) through Grant Agreement No. 318397 (<http://www.tolop.eu>); as well as by the Engineering and Physical Sciences Research Council (EPSRC) through the Centre for Doctoral Training in Delivering Quantum Technologies (EP/L015242/1), UNDEDD (EP/K025945/1) and QUES2T (EP/N015118/1). J.W.A.R. acknowledges funding from the EPSRC through International Network and Programme Grants (EP/P026311/1; EP/N017242/1). JPA development was supported by the Army Research Office under Grant No. W911NF-14-1-0078. M.F.G.Z. acknowledge support from the Royal Society and Winton Programme for the Physics of Sustainability.

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