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Ning Ye, Joseph P. Feser, Sridhar Sadasivam, Timothy S. Fisher, Tianshi Wang, Chaoying Ni, and Anderson Janotti

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Thermal Transport Across Metal Silicide-Silicon Interfaces: An Experimental Comparison between Epitaxial and Non-epitaxial Interfaces

Ning Ye and Joseph P Feser*

Department of Mechanical Engineering, University of Delaware 130 Academy St., Newark, DE, 19716, USA

Sridhar Sadasivam and Timothy S. Fisher

*School of Mechanical Engineering and Birck Nanotechnology Center,
Purdue University, 1205 W. State St., West Lafayette, IN 47907 USA*

Tianshi Wang, Chaoying Ni, and Anderson Janotti

Materials Science and Engineering Department, University of Delaware, 201 Dupont Hall, Newark, DE, 19716, USA

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Silicides are used extensively in nano- and microdevices due to their low electrical resistivity, low contact resistance to silicon, and their process compatibility. In this work, the thermal interface conductance of TiSi_2 , CoSi_2 , NiSi and PtSi are studied using time-domain thermoreflectance. Exploiting the fact that most silicides formed on $\text{Si}(111)$ substrates grow epitaxially, while most silicides on $\text{Si}(100)$ do not, we study the effect of epitaxy, and show that for a wide variety of interfaces there is no dependence of interface conductance on the detailed structure of the interface. In particular, there is no difference in the thermal interface conductance between epitaxial and non-epitaxial silicide/silicon interfaces, nor between epitaxial interfaces with different interface orientations. While these silicide-based interfaces yield the highest reported interface conductances of any known interface with silicon, none of the interfaces studied are found to operate close to the phonon radiation limit, indicating that phonon transmission coefficients are non-unity in all cases and yet remain insensitive to interfacial structure. In the case of CoSi_2 , a comparison is made with detailed computational models using (1) full-dispersion diffuse mismatch modeling (DMM) including the effect of near-interfacial strain, and (2) an atomistic Green's function (AGF) approach that integrates near-interface changes in the interatomic force constants obtained through density functional perturbation theory. Above 100K the AGF approach significantly underpredicts interface conductance suggesting that energy transport does not occur purely by coherent transmission of phonons, even for epitaxial interfaces. The full-dispersion DMM closely predicts the experimentally observed interface conductances for CoSi_2 , NiSi , and TiSi_2 interfaces, while it remains an open question whether inelastic scattering, cross-interfacial electron-phonon coupling, or other mechanisms could also account for the high temperature behavior. The effect of degenerate semiconductor dopant concentration on metal-semiconductor thermal interface conductance was also investigated with the result that we have found no dependencies of the thermal interface conductances up to (n-type or p-type) $\approx 1 \times 10^{19} \text{ cm}^{-3}$, indicating that there is no significant direct electronic transport and no transport effects which depend on long-range metal-semiconductor band alignment.

I. INTRODUCTION

Metal silicide thin films are present in nearly all modern silicon microelectronic devices. In particular, the silicides PtSi , WSi_2 , TiSi_2 , CoSi_2 , NiSi are used extensively due to their low electrical contact resistance to Si, low resistivity, and chemical process compatibility, as well as the low thermal budget associated with their formation.^{1,2} They can serve a wide range of roles including ohmic contacts, Schottky barrier contacts, gate electrodes, local interconnects, and diffusion barriers. While many silicides are excellent thermal conductors due to their low electronic resistivity, they are generally applied as thin films with nanoscale thicknesses, such that interfacial properties are expected to dominate thermal transport locally.³⁻⁵ This work reports the experimental measurements of thermal interface conductance on a wide range of technologically relevant metallic silicide-silicon interfaces, and shows that they are the highest thermal interface conductances ever measured for a metal-silicon

interface on silicon, and are comparable to the highest metal-dielectric thermal interface conductances ever measured.

In addition to the practical implications to thermal management in microelectronics, silicide interfaces represent a unique opportunity for studying the fundamental physics of thermal transport across interfaces. In general, a lack of experimental data exists regarding the role of disorder on thermal interface conductance, and in particular data for which the interfacial structure is known is scarce. Despite a large number of investigations of thermal interface conductance in literature, there are just a few which directly measure the thermal conductance of epitaxial metals on crystalline substrates. Stoner and Maris have reported the thermal interface conductance of single crystal Au grown perpendicular to $[2110]$ Al_2O_3 , and found it to be more than 3 times higher than for similar non-epitaxial samples. Compared to theory, epitaxial $\text{Au}/\text{Al}_2\text{O}_3$ interfacial conductance greatly exceeded lattice dynamics calculations and some measurements were

in excess of the phonon radiation limit⁶. Costescu et al⁷ have measured TiN thin films grown on MgO and Al₂O₃ substrates. They compared thermal interface conductance of epitaxial TiN/MgO(001) to TiN/MgO(111) growth and found no difference in their values, in spite of the large number of stacking faults in the latter case. Their data neither fit a coherent lattice dynamics model (which overestimated conductance by $\approx 70\%$), nor a Debye-based diffuse mismatch model (which overestimated by $\approx 300\%$), though if optical modes were excluded from mode-conversion better agreement was found ($\approx 50\%$). Wilson⁸ has studied the thermal interface conductance of epitaxial SrRuO₃ grown on SrTiO₃ and estimated a lower bound of $G_{\text{SrRuO}_3/\text{SrTiO}_3} \approx 800$ MW/m²-K. In the case of interfaces with silicon, only a few reports of direct measurements of interface conductance exist in which the substrate was cleaned and the oxide was removed prior to interface formation; thus, the detailed lattice structure of the interface is typically unclear, and the presence of native oxide is virtually assured. A couple reports^{9,10} of thermal interface conductance measurements of polycrystalline Al(111) growth on HF dipped Si(100) substrates exist and show that clean interfaces have substantially higher thermal interface conductance than untreated surfaces. However, epitaxial Al does not readily form on Si, and thus comparisons between experiment and theories considering interface structure are still lacking. Liu¹¹ recently reported the first thermal conductance measurement of an epitaxial metal with silicon: a NiSi₂/Si interface within a Si nanowire created by a reactive method using an in-situ electron beam heating technique. The thermal interface conductance reported was unusually high: $G = 500$ MW/m²-K at 300K. Taken together, these measurements show that epitaxial interfaces can produce record-breaking phonon-dominated thermal interface conductances. References 6–8, and 11 also serve as the only metal-dielectric interface thermal conductance measurements performed where the interfaces were simultaneously structurally and thermally characterized. Thus, to date they are the only experiments with which direct theoretical comparisons can be made. Despite this, it would appear that no such comparisons have been made using modern computational tools. Consequently, there are substantial open questions about the physics of transport across epitaxial as well as disordered interfaces.

Silicide-silicon interfaces are a great testing platform for the effect of disorder on thermal interface conductance because many metal silicide interfaces can be grown either as epitaxial or non-epitaxial interfaces depending on the synthetic process and substrate orientation. Also, many silicides are metallic and optically opaque, which enables their direct use in modern optical thermal interface conductance characterization methods such as time-domain thermoreflectance (TDTR). Epitaxial growth of metal silicides on silicon has been previously demonstrated for most known silicides including PtSi¹², CoSi₂^{13,14}, NiSi¹⁵, C54 TiSi₂¹⁶,

C49 TiSi₂¹⁷, VSi₂¹⁸, CrSi₂¹⁹, γ - & β -FeSi₂²⁰, YSi₂²¹, YSi₂²², GdSi₂²², TbSi₂²², DySi₂²², HoSi₂²², ErSi₂²², TmSi₂²², YbSi₂²², LuSi₂²², MoSi₂²³, Pd₂Si²⁴, TaSi₂²⁵, WSi₂²⁶, OsSi₂²⁷, and IrSi₂²⁸. For many silicide compounds including PtSi (orthorhombic), NiSi (orthorhombic), and CoSi₂ (fluorite), epitaxy occurs most readily on $\langle 111 \rangle$ substrates, though for lattice-matched fluorite structure compounds, epitaxy on $\langle 100 \rangle$ substrates is still possible under some preparation conditions. For example, CoSi₂ and Si are both cubic with similar lattice parameters 5.3Å and 5.43Å respectively, and CoSi₂ can be grown epitaxially using high-temperature codeposition onto $\langle 100 \rangle$ substrates.

In this work, we systematically study the thermal interface conductance of epitaxial and non-epitaxial interfaces of the metal-silicide TiSi₂, CoSi₂, NiSi and PtSi with silicon using time-domain thermoreflectance (TDTR) and compare the results to the most advanced available theories.

II. EXPERIMENTAL

A. Epitaxial silicide growth

TiSi₂, CoSi₂, NiSi and PtSi were fabricated under a wide range of conditions. We studied films: (i) on both Si(100) and Si(111) substrates, (ii) using a wide range of Si substrate doping concentrations, (iii) using different surface cleaning methods, and (iv) two different growth techniques. The two different substrates orientations were used in order to generate different interfacial structures, since it is known that the rhombohedral compounds PtSi^{29–32} and NiSi^{33,34} films grow epitaxially on Si(111) surfaces, while these form polycrystalline structures on Si(100) surfaces. All the silicides studied here were grown by thermally induced reactions of the pure metal: Ti, Co, Ni, or Pt were deposited by RF-sputtering onto a Si substrate at 300K. Samples were then annealed at high temperature (PtSi: 400 °C; NiSi: 400°C; TiSi₂: 750°C; CoSi₂: 750°C for 30 minutes) within the sputtering chamber to induce the reactive growth of the appropriate silicide layer (~ 110 nm thick). With the exception of TiSi₂ (C54 phase), the silicides here form epitaxial interfaces on Si(111) when grown by the thermal method. None of the silicides form epitaxial interfaces when grown on Si(100) by this method. In the case of CoSi₂ we also grew samples by co-sputtering of elemental Si and Co at 750°C, which allowed the formation of epitaxial interfaces on Si(100) substrates, unlike the thermal method. We pre-cleaned all Si wafers using acid piranha followed by either (1) an in-situ RF sputtering substrate bias cleaning, followed by a 750° substrate anneal or (2) an HF dip performed ~ 30 sec prior to loadlocking the samples into the sputtering chamber. The latter approach produced smoother final surfaces according XRR characterization. Samples with substrate doping levels ranging from $n = 1 \times 10^{19}$ cm³ to $p = 1 \times 10^{19}$ cm³ were also cre-

ated to explore electronic effects on the thermal interface conductance of metal-semiconductor junctions.

X-ray diffraction (XRD) was used to characterize the structure of the films, and confirmed epitaxial growth in the cases of NiSi, PtSi and CoSi₂ on Si(111) substrates (regardless of which surface preparation was used) and also in the case of co-sputtered CoSi₂ on Si(100). Figure 1 shows XRD ω - 2θ scans of different silicides grown on intrinsic Si(100) and Si(111) wafers, and the JCPDS index cards of the corresponding materials are given for comparison. No peaks from elemental Ti, Co, Ni or Pt are observed, indicating there is no unreacted metal detected in the films. Scans for co-sputtered CoSi₂ on HF-pretreated Si(111) and Si(100) show peaks at CoSi₂(111)/(222) and CoSi₂(200)/(400) respectively (Figure 1a). For NiSi on Si(111) only NiSi(200)/(400) peaks at 34.4° and 72.5° respectively are observed (Figure 1b). In contrast, the NiSi on Si(100) shows primary peaks from NiSi(210) and Ni(211) diffraction planes (36.1° and 47.5°), indicating oriented polycrystalline growth. The XRD patterns of PtSi/Si(111) and PtSi/Si(100) show similar behavior: PtSi films formed on Si(111) shows only PtSi(020)/PtSi(040) planes and the PtSi film grown on Si(100) exhibits a polycrystalline structure with almost random orientations. For TiSi₂ on Si(111) and Si(100), both substrate orientations yield the same characteristic peaks corresponding to the C54 face-centered orthorhombic phase of TiSi₂ (the strongest of which are TiSi₂(311) and TiSi₂(004)), which is the same phase used in many microelectronics applications. We found no preferential growth direction in the case of TiSi₂ on Si(111) or Si(100). The growth of silicides on doped Si wafers was found to be identical to that on the intrinsic wafers.

XRD ϕ -scans of the diffraction planes perpendicular to the sample surface (in-plane diffraction planes) were performed to confirm epitaxy of the silicides. Figure 1(e) shows the in-plane XRD ϕ scans of sample PtSi(020)/Si(111), NiSi(200)/Si(111) and CoSi₂(111)/Si(111). For PtSi(020)/Si(111), the in-plane diffraction peak of PtSi(200) plane, corresponding to $2\theta=30.06^\circ$, was taken while rotating the sample 360° with its out-of-plane axis. The pattern shows six-fold symmetry. While the crystal structure of PtSi is orthorhombic, the in-plane XRD ϕ scan shows a six-fold symmetry rather than two fold because there are 3 equivalent PtSi epitaxies conforming to the pseudo-hexagonal structure of the Si(111) surface.³⁰ The same is expected to occur on orthorhombic NiSi on Si(111).³⁵ The XRD ϕ scan of the in-plane diffraction peak of NiSi(020) plane with $2\theta=54.94^\circ$ on the NiSi(200)/Si(111) sample shows a six-fold symmetry, which confirms a pseudo-hexagonal epitaxial growth of NiSi on Si(111). The XRD ϕ scan of CoSi₂(111)/Si(111) sample also indicates a six-fold symmetry of the in-plane diffraction peak of CoSi₂(2-20) plane with $2\theta=48.15^\circ$. This result suggests the epitaxial growth of the CoSi₂(111) on Si(111). The in-plane lattice mismatch between the epitaxial silicide films and Si wafer are calculated to be $\approx 11\%$ for PtSi(020)/Si(111), $\approx 5\%$

for NiSi(200)/Si(111) and $\approx 1\%$ for CoSi₂(111)/Si(111). Despite the large mismatch in the case of PtSi, the interface is known to form epitaxially by relieving strain using a undulating interface³⁶.

B. Transport Characterization

Thermal interface conductance and substrate thermal conductivity measurements were performed using time-domain thermoreflectance (TDTR). Our system is based on the two-tint approach described by Kang and Cahill³⁷. The measurement system and methods of data reduction have been described in detail previously.^{37,38} The time evolution of surface temperature is measured through temperature-dependent changes in the reflectivity, i.e., the thermoreflectance. We analyze the ratio of in-phase V_{in} , and out-of-phase V_{out} variations in the intensity of the reflected probe beam at the modulation frequency (12.6 MHz unless otherwise specified) of the pump beam as a function of delay time between pump and probe. The wavelength of the mode-locked Ti:sapphire laser is $\lambda=785$ nm and the $1/e^2$ radius of both focused beams is $25 \mu\text{m}$ with a repetition rate of 76 MHz.

One unique aspect of this work is that we use the metal silicide itself as the metal transducer. Unless otherwise noted, TDTR data reduction consisted of simultaneous non-linear least square extraction of the substrate thermal conductivity and thermal interface conductance between the silicide and silicon substrate. In order to perform data reduction, it is necessary to know the thickness of the silicide films, the heat capacity of all the layers, and the thermal conductivity of any layers for which data reduction is not being performed. The thickness of the silicides was determined by calibrating the thickness of pure metal deposited under the same conditions, measured by X-ray reflectivity, and using knowledge of the silicide lattice constant and stoichiometry. The characteristic light absorption length of the silicides ranged from 21-36nm (the details of this calculation are given in the Supporting Information), which is ≈ 3 -fold larger compared to a traditional Al transducer. Therefore the silicide transducer layers were grown to be $\sim 110\text{nm}$ to ensure full absorption of the laser and to avoid anomalous signals at short time delays due to electron-hole pair modulations of the reflectivity. The absorption process was approximated using the bilayer technique described by Cahill³⁸, though the particular model used was not found to affect the experimental regression because the fit was performed at long time delays (300ps-3700ps) where details of the initial heat deposition profile no longer matter. Most of the sensitivity to interface conductance occurs at the largest time delays where this is especially unimportant (details of the sensitivity analysis and errorbar estimation are given in the Supporting Information). Heat capacities of all the films were determined by density functional perturbation theory (DFPT) through our own DFPT calculations. Results from the heat capacity calculations for

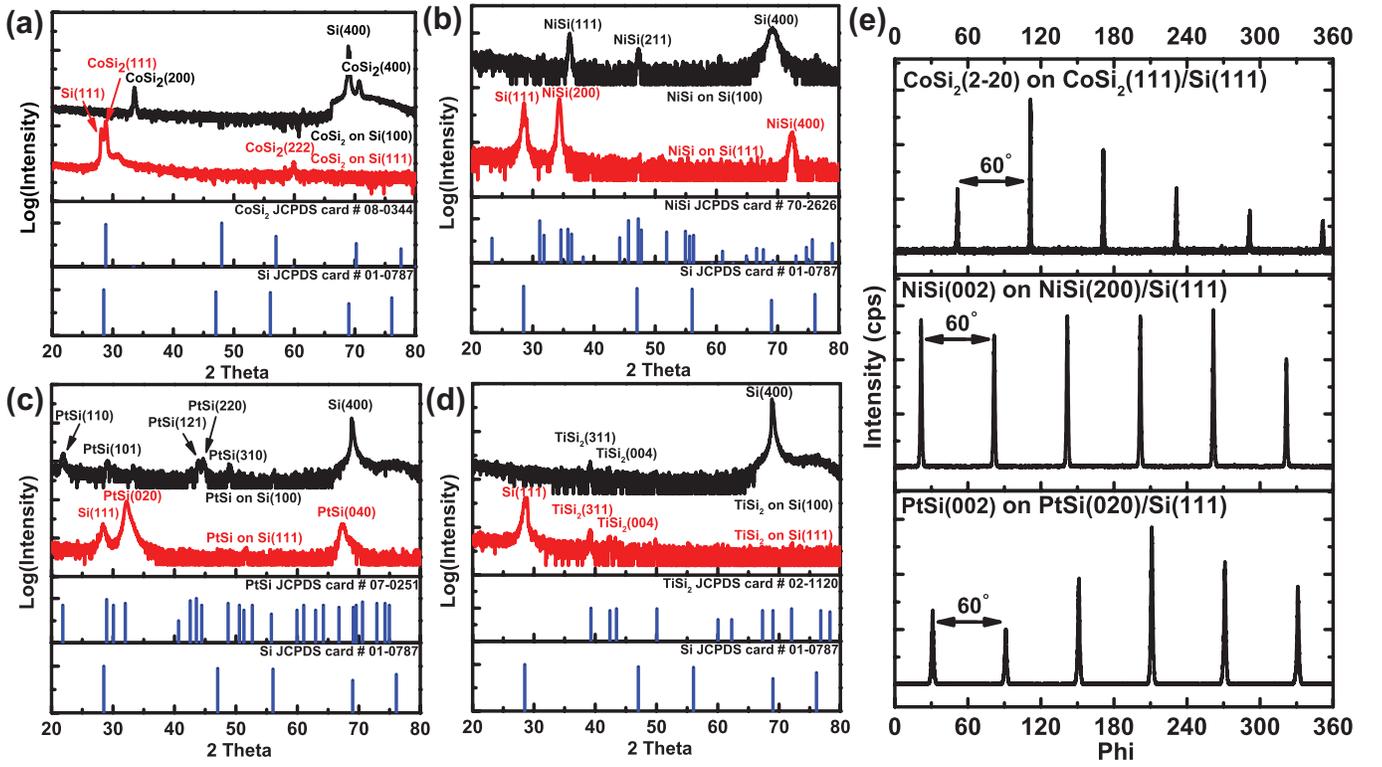


FIG. 1. (a) XRD results of CoSi_2 on intrinsic Si(100) and Si(111) wafer; (b) XRD results of NiSi on intrinsic Si(100) and Si(111) wafer; (c) XRD results of PtSi on intrinsic Si(100) and Si(111) wafer; (d) XRD results of CoSi_2 on intrinsic Si(100) and Si(111) wafer; (e) XRD phi scan of the in-plane diffraction for PtSi(020)/Si(111), NiSi(200)/Si(111) and CoSi_2 (111)/Si(111) samples.

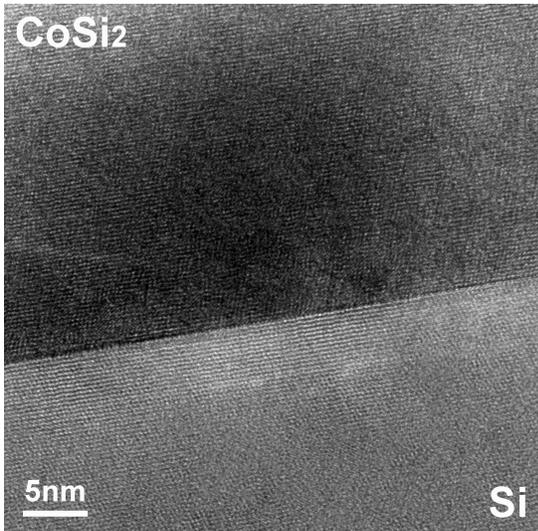


FIG. 2. HRTEM of an epitaxial CoSi_2 -Si interface.

CoSi_2 , TiSi_2 , NiSi and PtSi can be found in the Supporting Information. The sheet resistance/electrical conductivity of the silicide films were measured at room temperature using an inline four point probe with regression to an I-V curve. The resulting electrical resistivities were

used to estimate the electronic component to thermal conductivity using the Wiedemann-Franz law, assuming the degenerate Lorenz number $L_0=2.44 \times 10^{-8} \text{ W}\Omega/\text{K}^2$. The results are given in Table I. The thermal conductivity of silicides was high enough to yield good sensitivity to thermal interface conductance, and the electronic thermal conductivity was verified to be a good approximation of the total thermal conductivity with TDTR using observations at time-scales below 400 ps, where sensitivity of the signal to the metal transducers thermal conductivity is strongest and most independent. The temperature dependent electrical resistivity of CoSi_2 , TiSi_2 and NiSi used in this work were estimated by using the literature-reported temperature coefficient of resistance³⁹⁻⁴¹ combined with our measured room temperature electrical resistivity values (coefficients and equations given in Supporting Information). Note that errorbars associated with the extracted thermal interface conductance are found to be much less sensitive to the transducer thermal conductivity than the extracted substrate thermal conductivity. For example, for CoSi_2 at 300K a 10% uncertainty in silicide thermal conductivity corresponds to an errorbar of 4.6% in the extracted interface conductance and 13.6% in substrate thermal conductivity. For the case of PtSi, there are no previously reported temperature coefficients of resistance. The PtSi/Si temperature dependent TDTR data were therefore analyzed

TABLE I. Silicide properties at 300K

Silicide	ρ ($\mu\Omega\text{-cm}$)	κ_e (W/m-K)	C_V (10^6 J/m ³ -K)
CoSi ₂	16	44 (calc)	2.74
TiSi ₂	19	38 (calc)	2.52
NiSi	20	36 (calc)	2.99
PtSi	40	18 (calc)	2.49

by including PtSi silicide thermal conductivity as a third fitting parameter. At room temperature, the thermal interface conductance value of PtSi/Si obtained this way was within 5% of that obtained in Fig. 4 using the measured thermal conductivity.

In the cases of PtSi, NiSi, CoSi₂, TiSi₂ (C54), we found that the temperature dependence of reflectivity at 300K is comparable to the best previously reported materials⁴² at $\lambda=785$ nm. The thermoreflectance coefficients of CoSi₂ films had a positive value, while TiSi₂, NiSi and PtSi exhibited negative values. In the case of CoSi₂, the thermoreflectance coefficient was found to switch signs near 600K, allowing substrate thermoreflectance effects to become experimentally visible and thus complicating the data analysis. For this reason we restrict our experimental results for CoSi₂ to room temperature and below. This effect was not observed in PtSi, NiSi and TiSi₂, which allowed measurements from 77K-700K for these materials.

III. MODELING

Two forms of phonon transport modeling have been used, here: (1) interface thermal conductance calculations from full-phonon-dispersion diffuse mismatch modeling⁴³, using phonon dispersions obtained from DFPT, and (2) atomistic Green's function simulations employing density functional theory (DFT) to calculate interatomic force constants, including the effect of bonding changes near the interface. First-principles calculations for phonon dispersion were performed in the case of CoSi₂ and Si under the density functional theory (DFT) framework using Quantum Espresso⁴⁴, with a planewave basis set. The exchange correlation energy was approximated under the generalized gradient approximation (GGA) using the Perdew-Burke-Ernzerhof (PBE) functional form. Rappe-Rabe-Kaxiras-Joannopoulos (RRKJ) ultrasoft pseudopotentials were used for both Si and Co atoms. The relaxed bulk lattice constant of Si and CoSi₂ were found to be 5.46 Å and 5.36 Å which compares well with the experimental lattice constants of 5.43 Å (Si) and 5.36 Å (CoSi₂). While the density functional perturbation theory (DFPT) calculations on bulk Si and bulk CoSi₂ provide the bulk inter-atomic force constants (IFCs), the AGF method also requires as input the interfacial force constants at the Si-CoSi₂ interface. We perform DFT/DFPT calculations on a Si (111)-CoSi₂ (111) interface supercell shown in Figure 3. This inter-

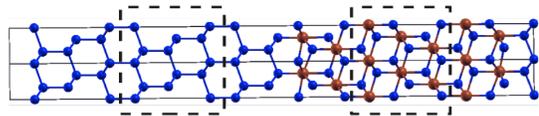


FIG. 3. Schematic of the Si-CoSi₂ interface supercell with a 8B interfacial atomic configuration. The dashed rectangular boxes indicate the unit cells of bulk Si and bulk strained CoSi₂.

TABLE II. DFPT Details for CoSi₂ / Si interface modeling

Parameter	Bulk Si	Bulk unstrained CoSi ₂	Bulk strained CoSi ₂	Si-CoSi ₂ supercell
Kinetic energy cutoff	680	820	820	820
Charge density cutoff	6800	8200	8200	8200
Electron k-point grid	12×12×9	14×14×14	16×16×12	16×16×1
Phonon q-point grid	4×4×3	4×4×4	4×4×3	4×4×1

face supercell corresponds to the 8B interfacial atomic configuration that has been identified to have the lowest interfacial energy in prior first-principles calculations of the Si-CoSi₂ interface,⁴⁵ and was also experimentally observed using TEM (Fig. 2). A tensile strain of 1.8% is imposed on CoSi₂ to match its lattice with Si. All the atomic positions and the lattice constant along the c-direction (heat transport direction) are relaxed for the interface supercell while the in-plane lattice constants of the interface supercell are fixed to that of bulk Si. Because the AGF simulations are performed on an interface between Si and strained CoSi₂, the bulk phonon dispersion and IFCs of strained CoSi₂ are also determined using a separate DFT/DFPT calculation. The unit cell in DFT calculations of bulk strained CoSi₂ corresponds to a 9 atom unit cell as shown the dashed box in Figure 3. We also use a 6 atom unit cell for calculations on bulk Si with one of the lattice vectors aligned along the [111] direction (see Figure 3). Table II shows the k-point grids and the cutoff energies used in DFT calculations of the bulk and interface structures. The phonon dynamical matrices are computed using DFPT on a Monkhorst-Pack q-point grid, and the phonon dispersion at arbitrary q-points are obtained using Fourier interpolation.

The bulk phonon dispersions of Si and CoSi₂ are used to obtain predictions for the thermal interface conductance using the diffuse mismatch model, and an upper limit for the elastic interface conductance is obtained from the radiation limit. Our DMM predictions use the exact phonon dispersion of bulk Si and bulk CoSi₂ as opposed to a Debye approximation that is commonly used in the literature. The procedure for full-dispersion DMM is described in Ref. 43, and the details of the radiation limit are provided in Ref. 46. The AGF method uses har-

monic inter-atomic force constants (IFCs) to determine the phonon transmission function that is then used in the Landauer approach to determine the thermal interface conductance.

An important development in the present work is the prediction of cross-interface force constants directly from DFPT calculations on a Si (111)-CoSi₂ (111) interface supercell. Our approach is a significant improvement in comparison with common heuristic approximations such as averaging of bulk force constants to obtain interface force constants. Such rigorous predictions of interface bonding strength is important since the phonon transmission function is strongly sensitive to the strength and nature of interfacial bonding. Since the AGF simulations consider strained CoSi₂, we also perform DMM calculations using the bulk phonon dispersion of strained CoSi₂. AGF simulations model specular reflection and transmission of phonons at the interface while the DMM assumes that the interface destroys all phase and direction information for the phonons incident on the interface. Hence, the AGF and DMM approaches are expected to represent perfectly smooth and rough interfaces respectively.

In the case of PtSi, the interatomic force constants (IFC) and phonon frequencies were calculated using DFT and PBE revised for solids (PBEsol)⁴⁷ as implemented in the Vienna *Ab initio* simulation package (VASP)^{48,49}. Projector augmented wave (PAW) potentials⁵⁰ are used to describe the interaction between the valence electrons and the ion cores, and an energy cutoff of 500 eV was used for plane wave expansion. We first determined the lattice parameter of PtSi by using a primitive cell with 8 atoms, in the orthorhombic structure, and a 888 Monkhorst-Pack mesh for integrations over the Brillouin zone. The calculated lattice parameters of $a=3.60$, $b=5.59$ and $c=5.92\text{\AA}$ are in good agreement with experimental values of 3.59, 5.57, and 5.91 \AA ⁵¹. The second order IFCs were calculated using the Phonopy code⁵² with a supercell of 64 atoms, which is a $2\times 2\times 2$ repetition of the 8-atom primitive cell, and a $2\times 2\times 2$ mesh of special k -points were used in these calculations. The IFCs matrix in real space was converted to dynamic matrix in reciprocal space by Fourier transforms. The phonon frequencies were then obtained by solving the eigenvalue problem of the dynamic matrix for phonon q vectors in the Brillouin zone sampled by a $50\times 50\times 50$ mesh in ShengBTE code⁵³. DMM modeling for NiSi and TiSi₂ were done in an analogous way, but using their corresponding lattice structures.

IV. RESULTS AND DISCUSSION

A. Thermal Interface Conductance of Epitaxial and non-epitaxial silicide-silicon interfaces

The results for room temperature thermal interface conductance of the silicide/Si interfaces on intrinsic silicon substrates are given in Figure 4. The thermal con-

ductance of the CoSi₂/Si and TiSi₂/Si were both near 480 MW/m²-K, similar to a recent report for an epitaxial NiSi₂/Si interface,¹¹ but greatly exceeding the highest interfacial thermal conductance for all other previously measured interfaces on silicon, including HF dipped Al/Si interfaces. To within the experimental uncertainty, there was no difference between the measured values of interfacial conductance formed on Si(100) vs Si(111). In other words, the epitaxial interfaces show nearly the same thermal interface conductance as the non-epitaxial interfaces in all cases. However, it appears that if the sources of error in TDTR are systematic (as they are usually are), the non-epitaxial interfaces may even have marginally larger thermal interface conductance than the epitaxial interfaces. Furthermore, in the case of CoSi₂/Si interfaces, we found no dependence of the thermal interface conductance on the surface preparation method (in situ RF-bias cleaning vs. HF dipped) or the method of silicide formation (reactive method vs. co-sputtering). The interfacial thermal conductance of silicide interfaces is thus found to be quite robust and high so long as the wafer surface is cleaned before the silicide formation. NiSi/Si

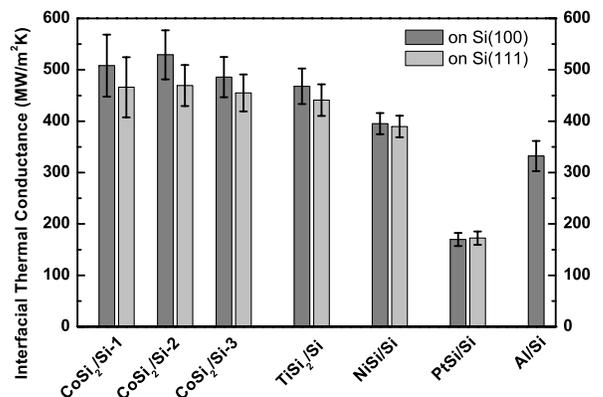


FIG. 4. The interfacial thermal conductance of CoSi₂, TiSi₂, NiSi and PtSi on intrinsic Si(100) and Si(111) wafer. CoSi₂/Si-1, CoSi₂/Si-2 and CoSi₂/Si-3 represent samples made under different conditions. CoSi₂/Si-1: HF treated wafer + films deposited by co-sputtering, CoSi₂/Si-2: HF treated wafer + films made by reactive growth method, CoSi₂/Si-3: RF bias treated wafer + films made by reactive growth method. For TiSi₂, NiSi and PtSi films are made by reactive growth method on RF-bias cleaned substrates. The interfacial thermal conductance of Al/Si(100) is also attached as a reference.

interfacial thermal conductance is also relatively high, $G_{\text{NiSi/Si}} \approx 400$ MW/m²-K. PtSi has much larger acoustic and phonon density of states contrast with Si compared to the other materials, and as expected its thermal interface conductance is substantially smaller than the other silicides studied, with $G_{\text{PtSi/Si}} \approx 170$ MW/m²-K.

While the observation may seem surprising that the thermal interface conductance of epitaxial silicides is essentially identical to those of non-epitaxial silicides, it is not without precedent. Similar results were reported for

epitaxial TiN(001)/MgO(001), TiN(111)/MgO(111) and TiN(111)/Al₂O₃(0001) interfaces,⁷ where it was found that, despite significant differences in lattice mismatch (8% when comparing O-O and N-N distances in the case of TiN/Al₂O₃(0001)) and the presence of stacking faults in the case of both TiN(111)/Al₂O₃(0001) and TiN(111)/MgO(111), all the interfaces showed the same interface thermal conductance. In that work the authors cite two possible reasons why this might be the case: (1) all samples undergo strong phonon scattering at the interface (including the seemingly perfectly structured ones) and therefore, all samples satisfy the assumptions of the diffuse mismatch model or (2) the interface disorder in all samples (including the more disordered ones) are weak and the transmission coefficient is always close to unity. We should note that the authors explicitly calculated the DMM for these cases and did not find good agreement. However, they implemented a relatively crude approach to perform the diffuse mismatch model calculations (Debye model). It is well established now that using full phonon dispersions produces substantially different DMM predictions under most circumstances. In addition, the authors utilized lattice dynamics (LD) calculations to predict the results for a perfect epitaxial interface. However, they did not consider local changes in bonding characteristics near the interface, which may also have been important. The DMM and AGF calculations here take these into account. Also, by comparing the experimental data to the calculated radiation limit (the maximum interface conductance consistent with detailed balance in the elastic limit) using full-dispersion relations, we are able to test the hypothesis (2) directly. In all cases, we find that silicide-silicon interfaces are not close to the radiation limit and thus the transmission coefficients are not close to unity (or rather the maximum allowable) for all modes.

Figure 5(a) shows a comparison between temperature-dependent CoSi₂ experiments and our DMM and AGF calculations.⁵⁴ Since the experimental Si(111)-CoSi₂(111) interfaces considered here are epitaxial with submonolayer interfacial roughness it would be reasonable to expect the AGF method to be applicable. We observe, however, that the experimental value of thermal interface conductance (≈ 500 MW/m²-K) exceeds the AGF prediction by more than 50%. At room temperature a full dispersion DMM not accounting for interfacial strain nearly accounts for the data at high temperature. However, employing a modified DMM that incorporates the effect of strain yields slightly worse agreement ($\approx 10\%$).

Given the lack of dependence of the interface conductance upon interface structure (i.e. epitaxial vs. not in Figure 4) and the reasonable agreement between experiments and the DMM model in Figure 5, it may be tempting to assume that interfaces really act as diffusely to phonons, even for epitaxial interfaces. However, we would caution that the observed high temperature discrepancies could also arise without the diffuse assumption, through a combination of inelastic interfacial pro-

cesses and inter- and intra-material electron-phonon coupling. Neither these processes is included current the model. Electron-phonon coupling within the metal provides a series resistance to the phonon-phonon interface resistance, while cross-interface electron-phonon coupling provides a parallel pathway for coupling between the primary energy carriers of metal and the semiconductor. Sadasivam et al.⁵⁵ performed first-principles calculations of electron-phonon coupling near a C49 TiSi₂-Si interface and found that the coupling of electrons with joint or interfacial phonon modes can potentially produce a conductance similar to the phonon-phonon interfacial conductance (note: in the present paper, we obtained the C54 phase of TiSi₂ which is the lower resistivity phase and is more commonly used for semiconductor applications). Inelastic phonon scattering has been identified as an important transport mechanism for material combinations with a large acoustic mismatch such as Pb and diamond^{56,57}. In the case of CoSi₂ of Si(111), we show in a forthcoming publication that the high temperature behavior of interface conductance can be matched quite well by invoking these mechanisms⁵⁴. It remains unclear, however, whether these mechanisms are insensitive to interfacial structure.

B. The effect of substrate carrier concentration

While we are not aware of any experimental methods capable of isolating the cross-interface electron-phonon coupling component of thermal conductance from phonon-phonon transport across the interface, we have studied the effect of doping the silicon wafer on the interfacial thermal conductance at room temperature (Figure 6). To our knowledge, there are no previous reports of the substrate carrier concentration dependence of thermal interface conductance for any metal-semiconductor interface. If the thermal interface conductance is phonon-dominated, the doping level of the substrate would not be expected to have any effect on the thermal interface conductance, perhaps justifying the lack of existing experiments. On the other hand, if cross-interface electron-phonon coupling is dependent on either band-bending, through Schottky barrier height and depth, or electronic screening, which depends on carrier concentration through the screening length, then we reason that substantial changes to the substrate carrier concentration could affect the electron-phonon coupling component of the thermal interface conductance.

We have studied a wide range of carrier concentrations from degenerate n- to degenerate p-type doping and have found that the thermal interface conductance of the silicides do not show any carrier concentration dependence. In fact, the interface conductance is remarkably consistent and stable against changes to both doping level/type and substrate/film orientation as shown in Figure 5. As has been previously reported, however, we do find that the substrate thermal conductivity is ap-

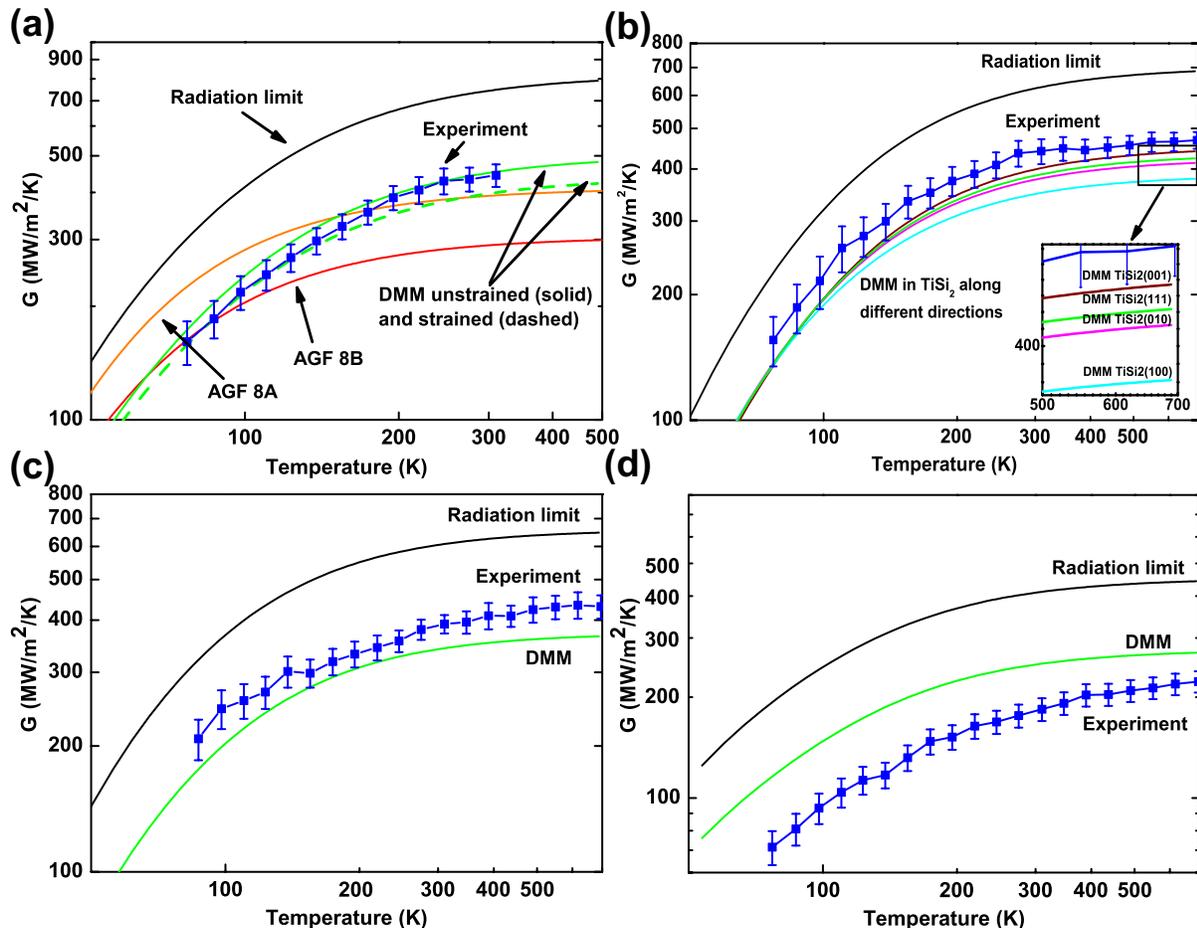


FIG. 5. (a) Modeling results for $\text{CoSi}_2(111)\text{-Si}(111)$ interfaces using various models: the full-dispersion diffuse mismatch model (green), the atomistic Greens function method for interface of 8B (red) and 8A (orange) and the radiation limit (black). Experimental data at room temperature is shown for comparison (blue squares); (b) Comparison between experimental thermal interface conductance of $\text{TiSi}_2\text{-Si}(111)$ interface (blue squares), the full-dispersion DMM calculation of $\text{TiSi}_2(001)$, $\text{TiSi}_2(010)$, $\text{TiSi}_2(100)$, $\text{TiSi}_2(111)\text{-Si}(111)$ and the radiation limit (black); (c) Comparison between experimental thermal interface conductance of an epitaxial $\text{NiSi}(200)\text{-Si}(111)$ interface (blue squares), the full-dispersion DMM calculation (green) and the radiation limit (black); (d) Comparison between experimental thermal interface conductance of an epitaxial $\text{PtSi}(020)\text{-Si}(111)$ interface (blue squares), the full-dispersion DMM calculation (green) and the radiation limit (black).

preciably reduced for degenerate levels of either p- or n-type dopants (red squares in Figure 6). Note that the extracted substrate conductivities were consistent across the various silicides, which gives additional confidence in the values of heat capacity for the silicides used to extract TDTR data. The decrease in the thermal conductivity of the silicon wafer with increase of the doping concentration (Figure 5 in supplemental document) is due to the phonon scattering by the impurity atoms and electrons (or holes)⁵⁸. The measured thermal conductivity of the silicon wafer is consistent with past measurements of intrinsic/doped Si performed by TDTR.¹⁰

V. CONCLUSIONS

In summary, the thermal interface conductance of CoSi_2 , NiSi and PtSi , TiSi_2 , and Al films with silicon were measured using a series of clean and well-characterized interfaces using TDTR. A few of these (CoSi_2 , TiSi_2 and NiSi) are demonstrated to be extremely good thermal interface conductance materials for Si, and are some of the highest ever measured for a metal-semiconductor interface. Interestingly, we find that the interfacial thermal conductance is not dependent on whether the interface is epitaxially grown or what the substrate orientation is. Above 100K a coherent AGF approach significantly underpredicts interface conductance in the case of CoSi_2 suggesting that energy trans-

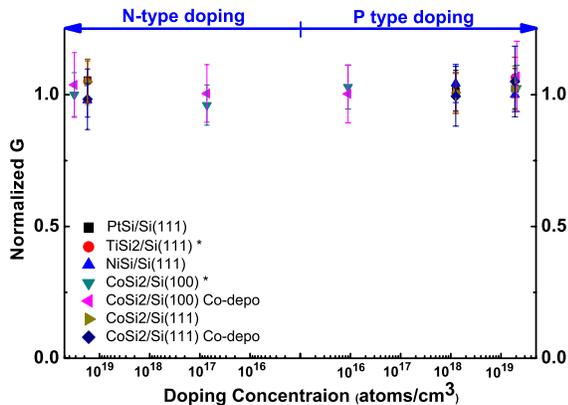


FIG. 6. The substrate doping effects on the interfacial thermal conductance. The thermal conductance values are normalized with their correspondent undoped values. This includes the interfacial thermal conductance of $\text{CoSi}_2/\text{Si}(111)$ and $\text{CoSi}_2/\text{Si}(100)$ made by co-deposition, $\text{CoSi}_2/\text{Si}(111)$, $\text{CoSi}_2/\text{Si}(100)$, $\text{TiSi}_2/\text{Si}(111)$, $\text{NiSi}/\text{Si}(111)$ and $\text{PtSi}/\text{Si}(111)$ made by reactive growth. * indicates that the interface is not epitaxial. (For original data please refer to figure 5 of the supplemental document)

port does not occur purely by coherent transmission of phonons, even for epitaxial interfaces. A full-dispersion diffuse mismatch model closely predicts the experimentally observed interface conductances for CoSi_2 , NiSi , and TiSi_2 interfaces, while it remains an open question whether inelastic scattering, cross-interfacial electron-phonon coupling, or other mechanisms could also account for the high temperature behavior⁵⁴. The effect of degenerate semiconductor dopant concentration on metal-semiconductor thermal interface conductance was also investigated with the result that we have found no dependencies of the thermal interface conductances up to (n-type or p-type) $\approx 1 \times 10^{19} \text{ cm}^{-3}$, indicating that there is no significant direct electronic transport and no transport effects which depend on long-range metal-semiconductor band alignment.

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