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Two-dimensional epitaxial superconductor-semiconductor heterostructures: A platform for topological superconducting networks

J. Shabani¹, M. Kjaergaard², H. J. Suominen², Younghyun Kim³, F. Nichele², K. Pakrouski⁴, T. Stankevic², R. M. Lutchyn⁶, P. Krogstrup², R. Feidenhansl², S. Kraemer⁵, C. Nayak^{3,6}, M. Troyer⁴, C. M. Marcus², and C. J. Palmström^{1,5,7}

¹California NanoSystems Institute, University of California, Santa Barbara, CA 93106, USA

²Center for Quantum Devices and Station Q Copenhagen,

Niels Bohr Institute, University of Copenhagen, 2100 Copenhagen, Denmark

³Department of Physics, University of California, Santa Barbara, CA 93106, USA

⁴Theoretical Physics and Station Q Zurich, ETH Zurich, 8093 Zürich, Switzerland

⁵Materials Research Laboratories, University of California, Santa Barbara, CA 93106, USA

⁶Microsoft Research, Station Q, University of California, Santa Barbara, CA 93106, USA

⁷Department of Electrical Engineering, University of California, Santa Barbara, CA 93106, USA

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Progress in the emergent field of topological superconductivity relies on synthesis of new material combinations, combining superconductivity, low density, and spin-orbit coupling (SOC). For example, theory¹⁻⁴ indicates that the interface between a one-dimensional (1D) semiconductor (Sm) with strong SOC and a superconductor (S) hosts Majorana modes with nontrivial topological properties^{2,5-7}. Recently, epitaxial growth of Al on InAs nanowires was shown to yield a high quality S-Sm system with uniformly transparent interfaces⁸ and a hard induced gap, indicated by strongly suppressed subgap tunneling conductance⁹. Here we report the realization of a two-dimensional (2D) InAs/InGaAs heterostructure with epitaxial Al, yielding a planar S-Sm system with structural and transport characteristics as good as the epitaxial wires. The realization of 2D epitaxial S-Sm systems represent a significant advance over wires, allowing extended networks via top-down processing. Among numerous potential applications, this new material system can serve as a platform for complex networks of topological superconductors with gate-controlled Majorana zero modes¹⁻⁴. We demonstrate gateable Josephson junctions and a highly transparent 2D S-Sm interface based on the product of excess current and normal state resistance.

The recent focus on topological states in solid state systems has revealed new directions in condensed matter physics with potential applications in topological quantum information^{10,11}. In an exciting development, it was realized one could readily engineer an effective one-dimensional (1D) spinless superconductor using the proximity effect from conventional superconductors (Al, Nb) in nanowires with strong SOC (InAs, InSb), and that Majorana zero modes would naturally emerge at the ends of the wire^{1,3,4,12}. First experiments on nanowires grown by chemical vapor deposition (CVD) revealed striking evidence of Majorana zero modes states¹³⁻¹⁸. In order to eventually move beyond demonstrations of braiding¹⁹⁻²², to larger-scale Majorana networks²³ it is likely that a top-down patterning approach will be needed. Molecular beam epitaxy (MBE) growth of large-area 2D S-Sm systems can form the basis for such an approach, but to date have not been available.

Narrow bandgap semiconductors such as InAs and InSb are

natural choices for the Sm component due to large g factors and strong SOC, which are important for the stability of an emergent topological phase in S-Sm heterostructures, with the topological gap proportional to the SOC strength²⁴. There are, however, significant challenges in growing high quality quantum wells in these systems. The lack of insulating lattice-matched substrates and difficulty in device fabrication, compared to well-developed GaAs material system, has restricted their use in mesoscopic devices. Nevertheless, it has long been known²⁵ that surface level pinning in InAs could allow for fabrication of transparent contact to superconductors and high quality S-Sm-S devices have been reported using in-situ ion milling of the native oxide^{26,27}. In this work we adopt a different approach by growing epitaxial layers of Al on 2D InAs/InGaAs quantum wells. These systems represent the ideal scenario in achieving a flat, abrupt and impurity-free interface^{28,29}. We show that our material system, Al-InAs, satisfies all the requirements necessary to reach the topological superconducting regime.

The recipe for creating a hybrid system that supports topological superconductivity requires a balance between proximity and segregation of constituent materials³⁰. The interface must allow electrons to inherit superconducting correlations from the s-wave superconductor while retaining large SOC and large g -factor from the semiconductor. This balance depends on how the electron wave function resides in both materials. Theory³¹ suggests that the average time spent by a quasiparticle in the Sm region is determined by the hybridization with the metallic states in the S region, $1/\Gamma$ (i.e., escape time from a quantum well to the normal metal) whereas the average time spent in the S region is given by the Heisenberg uncertainty time \hbar/Δ with Δ being the quasiparticle gap. An optimal balance is achieved when $\hbar\Gamma \sim \Delta$, i.e., when a quasiparticle spends roughly equal time in the S and Sm regions. Thus, in order to realize robust topological superconductivity, it is not only important to achieve highly transparent and disorder-free contacts between the active electrons in the Sm and the S, but also necessary to tune the tunneling between Sm and S regions with a barrier. This could be achieved, for instance, by inserting a potential barrier (e.g., a layer of InGaAs) between Sm and S. A calculation of $\hbar\Gamma$ versus barrier thickness d is shown in Fig. 1b for the case of Al and Nb as

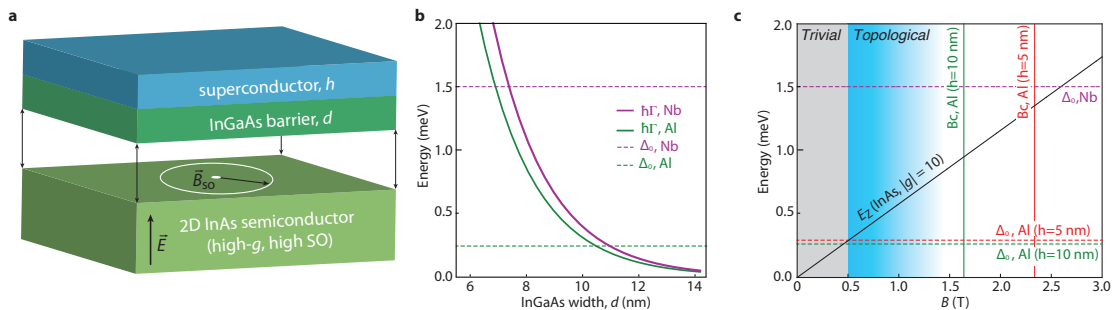


FIG. 1. (Color online) (a) Proposed structure for two-dimensional superconductor-semiconductor interface. The sketch is exploded in the barrier/InAs interface to highlight the spin-orbit field in the plane of the InAs. (b) Tunneling rate of electrons, $h\Gamma$, for Al and Nb as a function of top barrier thickness, d , see text. (c) Plot of Zeeman energy, E_Z , as a function of B for InAs ($|g| = 10$). Critical fields and superconducting gaps of Al for two thicknesses of 5 and 10 nm are also shown.

S and InAs as Sm materials. Because of the different bulk superconducting gaps, Δ_0 , and different Fermi energies, optimal barrier thicknesses differs in the two cases.

A quantum phase transition from trivial to topological superconducting state can be driven by an external magnetic field, B ¹². This requires a superconductor that can tolerate magnetic fields exceeding $\Delta/(g\mu_B)$, where μ_B is the Bohr magneton and g is the g factor in the semiconductor^{1,2}. Bulk aluminum has a critical field B_c of the order of 50 mT, too low to drive the system in the topological regime, even with $g \sim 10$ or larger in the semiconductor. However, few-nm thick Al film can sustain in-plane fields in excess of 2 T, readily exceeding $\Delta/(g\mu_B)$ ³². Figure 1c plots two energies, Δ and $E_Z = g\mu_B B$ for Al. The in-plane critical fields for Al depends on the thickness of Al films. We note that material structure such as Fig. 1a, where the confinement potential is highly asymmetric, can enhance SOC over the bulk values, leading to a larger quasiparticle gap.

I. HIGH MOBILITY, HIGH SPIN-ORBIT, NEAR SURFACE TWO-DIMENSIONAL ELECTRON SYSTEM

We first present the structural and electronic properties of our near surface InAs quantum wells. Figure 2a shows the schematics of the material stack^{33–35}. The structures are grown on a semi-insulating InP (001) substrate with $\text{In}_x\text{Al}_{1-x}\text{As}$ buffer where the indium content is step graded from $x = 0.52$ to 0.81. The quantum well consists of $\text{In}_{0.81}\text{Ga}_{0.19}\text{As}$ and InAs layers. X-ray diffraction analysis shows that the upper functional layers of samples are typically tilted with respect to the InP substrate from $0.15(2)^\circ$ to $0.8(2)^\circ$. It also exhibits isotropic mosaicity of the InAlAs layer in the range of 0.4° at FWHM, originating from the cross hatched pattern due to strain relaxation by dislocations.

Reciprocal space maps (RSM) of the (2-24) and (-224) Bragg peaks of the semiconductor are shown in Fig. 2b, The maps are aligned with $[-110]$ and $[001]$ directions of InAlAs on the axes. A smooth transition is evident from InP to InAlAs through the graded buffer. However, there is a notable asymmetry of InP peak position in the two RSMs. The InP

peaks are shifted from the relaxation line clockwise along the Debye-Scherrer ring (red dotted line in Fig. 2b), which corresponds to the crystal tilt between the layers above the buffer and the underlying substrate. The two InGaAs layers and InAlAs have very similar lattice constants; therefore we cannot distinguish between them and they all contribute to the peak labeled as InAlAs. Strain and composition of $\text{In}_x\text{Al}_{1-x}\text{As}$ were calculated from the peak positions assuming Vegard's law and using bulk lattice parameters and elastic constants of InAs and AlAs. The InAs layer is seen as the weakest peak at lowest out-of-plane Q values. It is fully strained with respect to the underlying InGaAs and InAlAs, which corresponds to compressive in-plane strain of $\epsilon_{xx}=1.6(2)\%$ (with a corresponding out-of plane strain of $\epsilon_{zz}=1.1(2)\%$, consistent with reported values for Poisson's ratio).

The surface InAs quantum wells have relatively low electron mobilities (under $10,000 \text{ cm}^2/\text{Vs}$), mostly due to the direct contact of electrons to scattering impurities at the S-Sm interface. A top barrier (thickness d) can improve the situation by separating the quantum well from the Al interface. InAlAs barriers could be used, but would likely result in a too abrupt wave function confinement, not allowing sufficient overlap with the S region. InGaAs is a more suitable choice because the smaller electron mass increase the length over which the wave function decays in the barrier region. Electron density distribution with an InGaAs barrier, calculated using a self-consistent Poisson-Schrodinger solver, is shown in Fig. 2c. As d is increased the charge distribution is moved away from the surface resulting in a mobility increase. Figure 2d shows the summary of the sample mobilities as a function of the InGaAs top barrier, d . Two sets of data are shown on wafers without in-situ growth of Al (S) and with Al but removed after growth using a selective wet etch. We find that, even for the same d , the mobility of electrons is higher when Al is not chemically etched. This indicates that the surface treatments are crucial and special care must be taken to ensure no mobility degradation. A possible way to avoid this chemical reaction is the full oxidation of the Al film.

In a perpendicular magnetic field, the system exhibits Shubnikov-de Haas (SdH) oscillations in the $d = 5 \text{ nm}$ wafer with an onset of oscillation about 2 T as shown in Fig. 2e. The

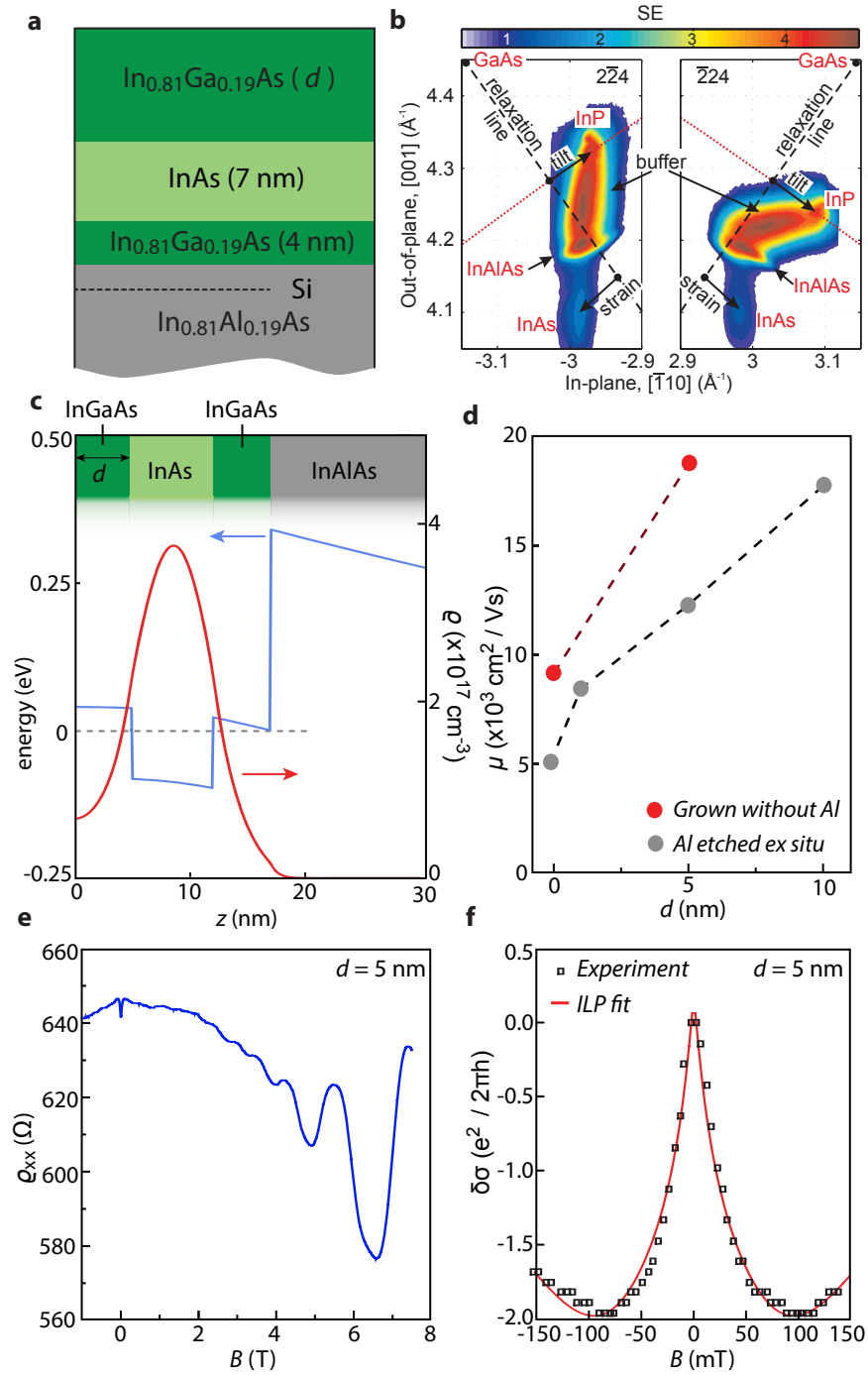


FIG. 2. (Color online) (a) Layer structure of our InAs quantum well near surface. (b) Reciprocal space maps of the two opposite Bragg peaks for the semiconductor: $2\bar{2}4$ and $\bar{2}24$. The dashed line is the relaxation line and the red dotted line is the Debye-Scherrer ring. (c) Charge distribution calculated for $d = 5$ nm. (d) Electron mobilities as a function of top barrier thickness, d . Red (Black) symbols show structures that are grown without Al (with Al and then etched). (e) Magneto-transport measurements of $d = 5$ nm at $n = 1 \times 10^{12} \text{ cm}^{-2}$. (f) Weak antilocalization signal, $\delta\sigma = \sigma(B) - \sigma(B = 0)$, near zero magnetic field. The red is the ILP fit to the data, see text.

weak antilocalization in this wafer is analyzed using the theory developed by Iordanski, Lyanda-Geller, and Pikus (ILP) for two-dimensional electron systems (2DESs)^{36,37}. The theory is valid when either Rashba or linear Dresselhaus SOC is dominating the other. To reduce the number of free fit-

ting parameters we fixed the value of cubic Dresselhaus SOC, γ as the bulk value of InAs 26.9 eV \AA^3 calculated from the $\vec{k} \cdot \vec{p}$ theory^{37,38}. The resulting linear Dresselhaus SOC for our 2DES can be estimated using $\alpha_D = \gamma(\langle k_z^2 \rangle - \frac{1}{4}k_F^2)$, where $\langle k_z^2 \rangle$ is the average squared wavevector in the growth direc-

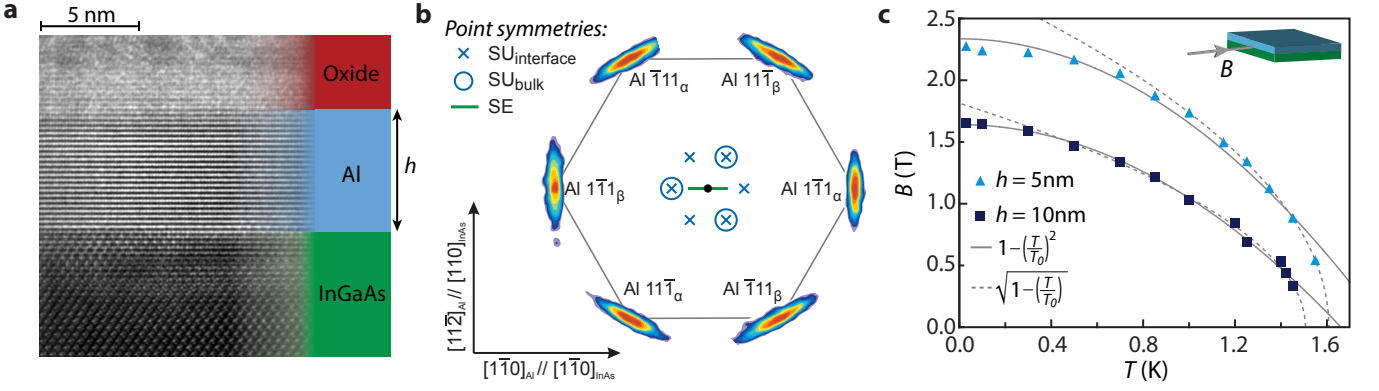


FIG. 3. (Color online) (a) High-resolution transmission electron microscope image showing that the Al forms a sharp and uniform interface to the InGaAs layer. (b) Reciprocal space maps for the [111] out-of-plane orientation shows six non-specular (111) peaks indicating that two grain orientations are present. For details on the degenerate interfacial grain orientations, see Ref.⁸. (c) Critical temperature and magnetic field measurements for a 5 nm (triangles) and 10 nm (squares) Al films on In_{0.81}Ga_{0.19}As/InAs. The solid and dashed curves are the scaling fits.

tion z , to be $\alpha_D \sim 50 \text{ meV } \text{\AA}^{37}$. The remaining fit parameters are phase-coherence length, l_ϕ , and linear spin-orbit coupling α . Fitting $\delta\sigma(B)$ over the range $|B| < 150 \text{ mT}$ at $T = 2 \text{ K}$ yields parameters $l_\phi = 350 \text{ nm}$ and $\alpha = 280 \text{ meV } \text{\AA}$. The fact that $\alpha > \alpha_D$ indicates that Rashba SOC is the dominant contribution. This value of α corresponds to a spin-orbit length $l_{so} = 45 \text{ nm}$, and $l_\phi/l_{so} \sim 8$.

II. EPITAXIAL GROWTH OF SUPERCONDUCTOR ON 2DES

Figure 3a shows a high-resolution transmission electron microscope (TEM) image of epitaxial Al on In_{0.81}Ga_{0.19}As (001), with atomic planes of both crystals clearly visible. X-ray diffraction (XRD) studies only show Al (111) out-of-plane orientations. The azimuthal orientation of Al(111) with respect to the underlying semiconductor is determined by making a full sample rotation while measuring asymmetric Al {111} peaks, as illustrated in Fig. 3b. Six equally spaced Al {111} peaks with equal intensity are found. Since the point symmetry of the InGaAs surface is two-fold (indicated with a line) and the corresponding symmetry of the Al bulk is three-fold, rotating the Al implies two degenerate interfacial configurations. The same degeneracy would appear if the structure was solely determined by single plane interfacial bicrystal symmetries, where the Al (111) interface is 6-fold (indicated with 6 'x' symbols around the center point). This means that the interface consist of only one type of interfacial bonding, indicating a strong two-fold degenerate minimum with only the lowest number of grain types possible. By measuring the distances between the opposite peaks we can conclude that Al is relaxed (unstrained) within measurement uncertainty ($\pm 0.1\%$).

The growth mechanisms of the Al film can be described in the thin-film limit (of film thickness h), where the size-dependent part of the chemical potential of a S grain with in-plane radius of curvature R , is given by⁸:

$$\delta\mu_S^R \propto \frac{\gamma_S}{h} + \frac{\gamma_{Sm||S}}{h} + \frac{\gamma_{\bar{S}||S}}{R} + \frac{G\epsilon^2}{(1-\nu)} \quad (1)$$

The four terms in Eq. (1) account for the free energy excesses of the surface, the S-Sm interface, the grain boundaries and the strain energy, respectively, where γ_S , $\gamma_{Sm||S}$ and $\gamma_{\bar{S}||S}$ are the corresponding excess free energies due to the chemical bonding of the interfaces, G is the shear modulus, ϵ the strain, and ν Poisson's ratio. In the thin film limit, $h \ll R$, mechanisms determining in-plane and out-of-plane and crystal orientations can be separated. The strongest thermodynamic driving force is the surface free energy minimization ($\gamma_S > \gamma_{Sm||S}$), which determines the out-of-plane orientation. This is typically (111) for FCC materials, including Al. The in-plane orientations are secondarily determined by the last three terms and involve more considerations⁸. At the initial stage of the growth, when h is sufficiently small, the chemical bonding at the S-Sm interface (i.e. second term in Eq. (1)) dominates and dictates the in-plane orientation. In the x-ray and TEM measurements, we observe two rotational grains but the same interfacial structure across the wafer. As the Al thickness approaches the critical value, given by, $h_c = \gamma_{Sm||S}(1-\nu)/(G\epsilon^2)$, where the strain energy exceed the difference in chemical bonding energy between the strained (domain matched) and the relaxed Al, the film will start to relax. We conclude that $h_c < 5 \text{ nm}$ as 5 nm thick Al films are determined to be relaxed.

If the indium composition in In _{x} Ga_{1- x} As, x , is varied, the lattice constant of the semiconductor changes and the strain energy density $\frac{G\epsilon^2}{(1-\nu)}$ for a given domain match will change. Here $\epsilon^2 = \epsilon_1^2 + \epsilon_2^2 + 2\nu\epsilon_1\epsilon_2$, where ϵ_1 and ϵ_2 are the strain in the two in-plane directions. If the strain energy for a given domain match is too high, h_c might be smaller than the thickness of the initial nucleus, and the film will either find a different lower symmetry match or appear more disordered on a macroscopic scale with many different grain orientations. Strain energy calculation as a function of indium content, x , exhibits a

minimum energy near $x \sim 0.8$. This suggest that growth of Al on $\text{In}_{0.8}\text{Ga}_{0.2}\text{As}$ results in a smoother interface consistent with TEM images near this composition.

How grain boundaries affect the electronic properties of the S-Sm interface is poorly understood. Figure 3c shows a comparison of the critical magnetic field as a function of temperature for Al on InGaAs barriers. Critical magnetic fields at base temperature, $T = 30$ mK, are found to be $B_c(0) = 2.3$ (1.6) T for 5 (10) nm films. At elevated temperatures, the critical field data are reasonably well fit by the Bardeen-Cooper-Schrieffer (BCS) form $B_c(T) = B_c(0)[1 - (T/T_c)^2]$ ³⁹ taking T_c and B_c as fitting parameters. At the low-temperature end of the scale, the 5 nm Al is better described by Chandrasekhar-Clogston theory^{40,41} where the upper limit critical field is expected to reach $B_c = \Delta_0/\sqrt{2}\mu_B \sim 2.4$ T. Close to T_c , data for both thicknesses fit the form $B_c(T) = B_c(0)\sqrt{1 - T/T_c}$ ⁴².

III. GATEABLE SUPERCURRENT

Having shown that the Al-InAs platform satisfies several basic requirements for topological superconductivity, we next demonstrate proximity effect and gate control in an S-Sm-S geometry. This geometry provides a probe of S-Sm interface transparency. High interface transparency, corresponds to a high probability of Andreev reflection at the S-Sm interface, is reflected in the supercurrent through the S-Sm-S structure.

Figure 4a shows a scanning electron micrograph of an S-Sm-S device with barrier $d = 10$ nm and Al thickness $h = 10$ nm. Selective etching has been used to remove a thin strip of aluminum, followed by deposition of 40 nm of aluminum oxide by atomic layer deposition (ALD) and a metallic top gate. The junction is $3 \mu\text{m}$ wide and has a 200 nm separation between the superconducting electrodes. The I-V characteristic of the junction is measured at 30 mK in Fig. 4b. The voltage drop across the junction is zero (the supercurrent) up to a critical value of driving current denoted the critical current, $I_c = 1.4 \mu\text{A}$ (see Fig. 4c). As the gate is used to deplete the 2DES, the critical current remains nearly unchanged down to $V_g < -2$ V. At more negative gate voltages the critical current is reduced, roughly inversely proportional to the above-gap resistance. The above-gap resistance is approximately equal to the normal state resistance, R_n . For gate voltages in the range $-3 \text{ V} < V_g < -2 \text{ V}$ the gate voltage decreases while the product $I_c R_n$ remains roughly constant, and in fact slightly increases (Fig. 4d). For $V_g < -3 \text{ V}$ the $I_c R_n$ decreases rapidly as the critical current vanishes with the junction becoming insulating³⁵.

Transport measurements on Hall bars with the Al removed at $V_g = 0$ V yield a mean free path of $l_e \sim 230$ nm, indicating that the junction is neither clearly ballistic nor diffusive. Note that in the present geometry, the Sm extends under the S regions. The interface between Sm and S is highly transparent due to the large area of contact and *in situ* aluminum growth. The Andreev process that carries the supercurrent across the

Sm region is characterized by the induced gap Δ_{ind} in the Sm below the S rather than the bulk Al gap, Δ_0 . To characterize an S-Sm-S junction in the short limit the product of the critical current and the normal state resistance, which is related to the gap $I_c R_n = a\Delta_0/e$, is often used. Here, a is a parameter of order unity and is model dependent⁴³. We find $I_c R_n = 135 \mu\text{V}$ at $V_g = 0$ V in our device which is close to the bulk gap of the Al thin film at this thickness, $\Delta_0/e \sim 200 \mu\text{V}$. To our knowledge, previous studies of *ex situ* fabricated junctions on 2DES have reported $I_c R_n$ products typically an order of magnitude smaller than Δ_0 ⁴⁴⁻⁴⁷.

Each Andreev reflected electron contributes $2e$ to the current through the junction, leading to an excess current relative to a normal metal junction. This excess current is thus an indirect measurement of the quality of the S-Sm interface, and is found by extrapolating a linear fit at $eV > \Delta_0$ to $V = 0$ V⁴⁸. An example of the procedure is shown as a dashed gray line for $V_g = 0$ V in Figure 4b, and we find $I_{\text{exc}} = 1.99 \mu\text{A}$. We assume a diffusive junction⁴⁹, for which the excess current through a perfect S-Sm interface is related to the gap via $I_{\text{exc}} R_n = (\pi^2/4 - 1)\Delta_0/e$ ^{50,51}. Using Δ_0 (instead of the unknown Δ_{ind}) of our aluminum film, an upper bound for the induced gap in the semiconductor⁵²⁻⁵⁴, we obtain $I_{\text{exc}} R_n = 330 \mu\text{V}$. Experimentally we measure $I_{\text{exc}} R_n = 191 \mu\text{V}$ in close agreement with the theoretical predictions for an ideal interface.

IV. CONCLUSION

In this work, we demonstrated that epitaxial Al-InAs two-dimensional systems are a viable platform to study topological superconductivity. We used InGaAs top barriers to achieve high electron mobilities and facilitate the growth of ultra-thin film Al. The electronic and material properties of both 2DES and Al are characterized in 2D and in S-Sm-S junctions. We observe an exceptional quality of S-Sm junctions compared to earlier experiments that attest to the quality of the S-Sm interface. Fabrication of complex architectures offers endless possibilities for exploring new directions. Our findings are expected to spark interest in large-scale device applications in mesoscopic and topological superconductivity.

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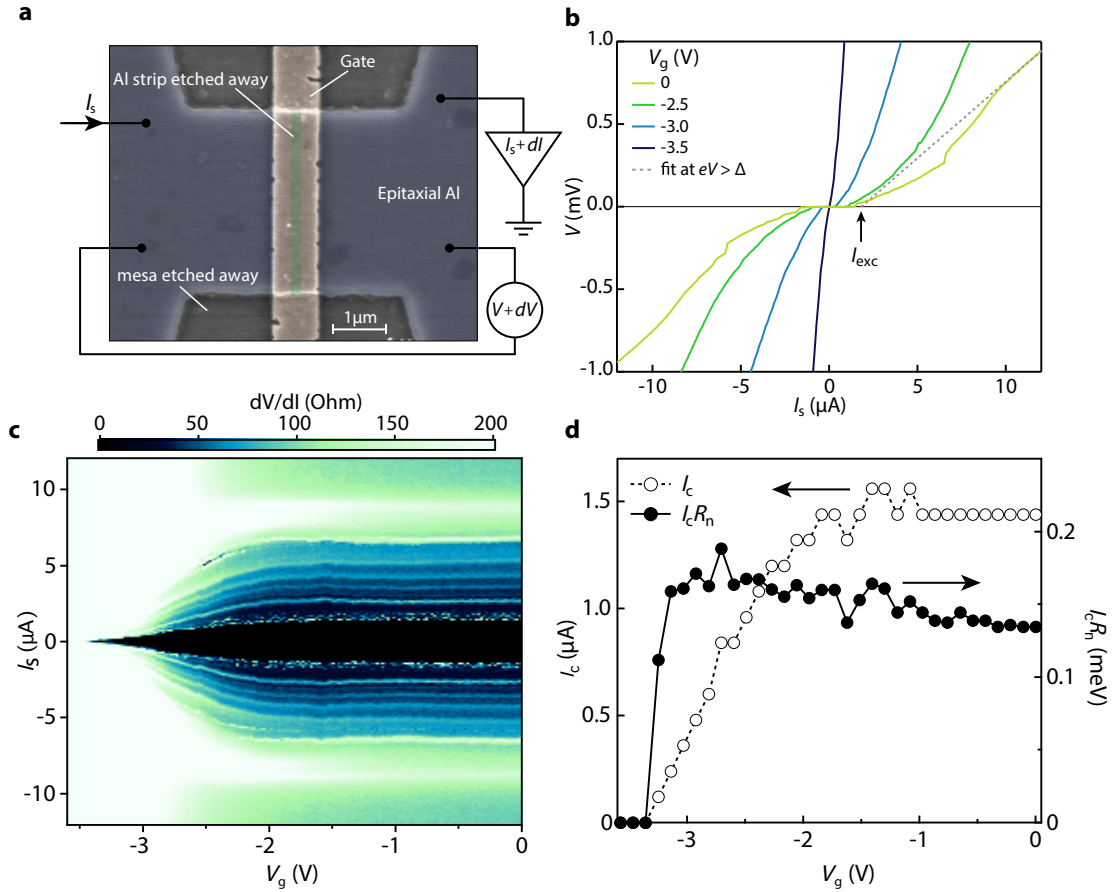


FIG. 4. (Color online) (a) A scanning electron microscope (SEM) image of a gated S-Sm-S junction fabricated on a wafer with $d = 10$ nm top barrier (b) $V - I$ characteristics of the junction as a function of top gate. An example of a linear fit to determine excess current is shown in dashed line for $V_g = 0$ V. (c) dV/dI vs dc source current, I_s measured as a function of top gate voltage. (d) The gate dependence of the critical current, I_c , and the product of the critical current and the normal state resistance, $I_c R_n$.

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