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First-principles Simulations of a Graphene Based Field-Effect Transistor

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We improvise a novel approach to carry out first-principles simulations of graphene-based vertical field effect tunneling transistors that consist of a graphene|h-BN|graphene multilayer structure. Within the density functional theory framework, we exploit the effective screening medium (ESM) method to properly treat boundary conditions for electrostatic potentials and investigate the effect of gate voltage. The distribution of free carriers and the band structure of both top and bottom graphene layers are calculated self-consistently. The dielectric properties of h-BN thin films sandwiched between graphene layers are computed layer-by-layer following the theory of microscopic permittivity. We find that the permittivities of BN layers are very close to that of crystalline h-BN. The effect of interface with graphene on the dielectric properties of h-BN is weak, according to an analysis on the interface charge redistribution.

I. INTRODUCTION

Graphene-based field-effect transistors have been synthesized,¹ but the ratio of high and low resistances (switching ratio) is limited (less than a factor of 10) because of the zero-gap band structure of pristine graphene. A possible solution is to introduce an energy gap into graphene by using, for instance, bilayer graphene,^{2,3} nanoribbons,⁴ or chemical derivatives.⁵ Recently, Brit-nell *et al.*^{6–8} reported an alternative transistor architecture, a vertical field effect tunneling transistor (FETT), exhibiting a fairly high (~ 50) switching ratio. In FETT devices, a graphene barrier graphene trilayer is utilized as the current-carrying channel, as in Fig. 1(a); the electric current flows into the channel through one graphene layer and out through the other. Inside the trilayer, electrons cross the thin barrier via tunneling. The chemical potentials of the two graphene layers and the tunneling conductance can be tuned by gate voltages. The bottom graphene layer (closer to gate electrode, Gr_B) can only partially screen the gate voltage because of the low density of states (DOS) near the Dirac point, and so the top graphene layer (away from gate electrode, Gr_{T}) is also affected by the gate voltage.

In a typical field-effect device, the current-carrying channel is separated by a thick dielectric from a metallic gate electrode. A gate voltage applied between the gate electrode and the current-carrying channel controls the free carrier density in the channel. The spatial distribution of free carriers can be obtained by solving the electrostatic Poisson equation. Macroscopic physical quantities of device components are used as parameters for Poisson's equation, including dielectric constants and electron affinities of dielectrics and work functions of gate electrodes, etc.⁹ However, the extrapolation of the macroscopic theory of electrostatics to the nanometer scale is unjustified, because interfaces often exhibit dielectric screening properties different from the bulk.¹⁰ The applicability of the macroscopic theory should be examined by studying the materials-specific dielectric screening properties of interfaces, one of motivations of this work. The density functional theory (DFT) method fully takes the atomic structure of a device into account, and microscopic electrostatics quantities such as potential and charge density can be solved self-consistently, from which microscopic dielectric properties can be deduced.¹¹ One can extract an effective dielectric constant from microscopic calculations to represent the dielectric screening of the interface, which is not necessarily a constant but a quantity that depends on the thickness of the interface and other factors.

The boundary condition is crucial in solving for the electrostatic potential. In an FET device, for example, the electrostatic potential at the surface of a metallic gate electrode should be constant, and the electric field (derivative of the potential) in vacuum far away from the device should be zero. This mixed boundary condition is different from the conventional and widely-implemented periodic boundary condition. Otani *et al.*¹² proposed a Green's-function-based effective screening medium (ESM) approach to solve the electrostatic



FIG. 1. (Color Online) (a) Schematic of a graphenebased field effect tunneling transistor. The core graphene|h-BN|graphene structure is separated from the doped Si (which serves as the gate electrode) by *h*-BN crystal and SiO_x slabs (which serve as dielectrics). The graphene layers closer to and further away from the gate electrode are labelled as Gr_B and Gr_T, respectively. (b) The model used in our simulations. The dielectrics (*h*-BN and SiO_x) are replaced by vacuum. The core graphene|h-BN|graphene structure is embedded betwen a semi-infinite vacuum ($z > z_1$) and an ideally metallic medium ($z < -z_1$, which replaces the doped Si gate).

potential under several different boundary conditions, which is promising for simulating the electric-field effect in planar devices.

In this work, graphene-based FETT devices were simulated using a DFT+ESM method. The computational approach, based on first principles, enables us to understand interface effects quantitatively, and thus enables computational design of functional systems. The rest of the paper is organized as follows. In the next Section, details of the computational method are presented. The distribution of free carriers and the band structures of graphene layers under different gate voltages are presented in Sec. III. The effective dielectric constant of the *h*-BN thin barrier in FETT is analyzed in Sec. IV. Finally, we give a summary on our results in Sec. V.

II. SIMULATION METHOD

The structure of the FETT used in experiments⁶ is schematically shown in Fig. 1(a). Doped silicon is used as the gate electrode, which is separated from the currentcarrying $\text{Gr}_{\text{T}}|h\text{-BN}|\text{Gr}_{\text{B}}$ trilayer channel by insulating silicon oxide (~ 300 nm) and h-BN thin films (~ 50 nm). The gate voltage is applied between the doped silicon and the trilayer.

In our DFT simulations, the doped silicon gate electrode was replaced by a semi-infinite ideally metallic medium for $z < -z_1$ [Fig. 1(b); the z-direction is perpendicular to the $Gr_T|h$ -BN| Gr_B trilayer], and the dielectrics between the gate electrode and the $Gr_T|h$ -BN| Gr_B trilayer were replaced by vacuum to save computational resources. Thus the $Gr_T|h$ -BN| Gr_B trilayer is sandwiched by a semi-infinite vacuum medium ($z > z_1$) and an ideally metallic medium ($z < -z_1$), see Fig. 1(b). Note that the semi-infinite vacuum and metallic media are not explicitly included in calculations, instead they are used as boundary conditions for the Hartree potential (V_H),

$$\frac{\partial V_H}{\partial z}|_{z=z_1} = 0, \qquad V_H|_{z=-z_1} = 0.$$
 (1)

We adopted the ESM method¹² to solve the Hartree potential. The purpose to employ this method is twofold: (i) Long-range Coulomb interactions with periodic images are avoided. (ii) The non-periodic boundary condition of Eq. (1) for the Hartree potential can be imposed.

A gate voltage can be simulated by adding extra electrons or holes to the $\text{Gr}_{\text{T}}|h\text{-}\text{BN}|\text{Gr}_{\text{B}}$ trilayer;¹² the areal density of free carriers is proportional to the gate voltage. In experiments the carrier density in graphene can be tuned by gate voltage up to ~ 10^{13} \text{ cm}^{-2}, which is equivalent to 5.24×10^{-3} electrons per primitive unit cell of graphene, or an electric displacement field of $D_q = 0.016 \text{ C/m}^2$ in the dielectrics.

The measured interlayer distance between *h*-BN and graphene layers¹³ is 3.32 Å, and the interlayer distance between *h*-BN layers 3.33 Å is taken as half of the lattice

constant c of crystalline h-BN.¹⁴ In-plane lattice constants for both graphene and h-BN were set to 2.46 Å due to the small lattice mismatch between them. In the x-y plane a periodic boundary condition was applied with a dense 155×155 Monkhorst-Pack¹⁵ k-mesh. The cutoff energy for wave functions and the Methfessel-Paxton¹⁶ spreading energy were taken to be 70 Ryd and 10^{-3} Ryd, respectively. The Perdew-Burke-Ernzerhof parameterization¹⁷ of the generalized gradient approximation (GGA) to the exchange-correlation functional and Trouiller-Martins norm-conserving pseudopotentials were used. DFT calculations were performed using the QUANTUM ESPRESSO package¹⁸.

Charge density in the $Gr_B|h\text{-}BN|Gr_T$ trilayer was calculated self-consistently for different gate voltages. In order to illustrate the distribution of free carriers across the trilayer, it is convenient to integrate the charge density in the x-y plane and define a charge density along z-direction $\rho(z)$. The density of free carriers is defined as charge density difference for a device under a finite gate voltage V_g with respect to that under zero gate voltage. Compared to gate voltages, the electric displacement field D_g in the dielectrics is a more convenient quantity, because it is independent of the thickness or the dielectric constant of the dielectrics. Gate voltages are expressed as D_g hereafter.



FIG. 2. (Color Online) (a1) The distribution of free carriers under $D_g = \pm 3 \times 10^{-3} \text{ C/m}^2$ and (a2) the charge density under zero gate voltage of a $\text{Gr}_{\text{T}}|\text{monolayer }h\text{-BN}|\text{Gr}_{\text{B}}$ system. (b) The free carrier distribution in a graphene|multilayer h-BN| graphene FETT under $D_g = 3 \times 10^{-3} \text{ C/m}^2$ with the positions of the top and (c) the bottom graphene layer aligned, respectively.

III. RESULTS

A FETT with a monolayer *h*-BN barrier is used as an example to illustrate the charge and free carrier densities in such devices.. The charge and free carrier density on each layer can be analyzed using the Bader decomposition,¹⁹ in which boundaries between atoms are defined by zero flux surfaces. There are three peaks in the charge density curve $\rho(z)$, corresponding to the Gr_T , *h*-BN, and Gr_B layers, respectively; cf. Fig. 2(a2). Free carrier densities $\Delta \rho(z)$, defined as the change in charge density induced by gate voltages, under $D_q =$ $\pm 0.003 \,\mathrm{C/m^2}$ are shown in Fig. 2(a1), where it can be seen that they have opposite sign but almost the same amplitude, in accordance with the electron-hole symmetry of graphene in the vicinity of the Fermi energy. For the one-dimensional charge density $\rho(z)$, atomic layers are divided by minima of $\rho(z)$, as denoted by vertical dashed lines in Fig. 2(a2) (zero flux surfaces shrink to points for the one-dimensional charge density.) A large portion of the free carriers accumulate on Gr_B at the side closer to the gate electrode; thus the free carrier density on Gr_B is larger than that on Gr_T . The asymmetric shape of $\Delta \rho(z)$ with respect to the central h-BN layer is a consequence of the asymmetric environment of the $Gr_{\rm B}|h-BN|Gr_{\rm T}$ trilayer, with the metallic gate electrode on one side and vacuum on the other side.

The *h*-BN barrier can screen the applied gate voltage by developing an internal electric polarization, which reduces the effect of the gate voltage and the free carrier density in Gr_T. We performed calculations on devices with a *h*-BN barrier thickness of up to eight monolayers. The free carrier density for these devices as a function of z for $D_g = 3 \times 10^{-3} \text{ C/m}^2$ is shown in Figs. 2(b) and 2(c), shifted to make the positions of the Gr_T (b) or Gr_B (c) layers coincide. The distribution of free carriers on Gr_B is almost independent of the thickness of the



FIG. 3. (a) Band structure of a FETT with five layers of h-BN as barrier at $D_g = 0.016 \text{ C/m}^2$, and (b) the difference in chemical potential between Gr_B and Gr_T as a function of gate voltage (the dashed line is a guide to the eye).

h-BN barrier, cf. Fig. 2(c). The amplitude of the electric polarization in the *h*-BN barrier shrinks for thicker barriers (Fig. 2(c)), and so does the free carrier density in Gr_T (Fig. 2(b)), indicating that thicker *h*-BN barriers provide stronger screening.

The chemical potential of graphene, defined as the energy difference between the Fermi energy and the charge neutrality point, can be efficiently tuned by using gate voltages because of the small DOS near the Fermi energy.⁶⁻⁸ The band structure of a Gr_T |*h*-BN|Gr_B trilayer in the vicinity of the Fermi energy has contributions only from graphene layers, because *h*-BN is a wide gap insulator with a calculated energy band gap of $4.77 \,\mathrm{eV}$. The band structure of graphene shows a tiny energy gap of $0.05 \,\mathrm{eV}$ [Fig. 3(a)] induced by interaction with *h*-BN.²⁰ A FETT with a *h*-BN barrier thicker than one monolayer shows no hybridization between Gr_B and Gr_T in its band structure. As an example, the band structure of a FETT with a five-layer-thick h-BN barrier for $D_q = 0.016 \,\mathrm{C/m^2}$ is shown in Fig. 3(a), where bands from Gr_T and Gr_B shift upward rigidly by $\sim 0.1 \,\mathrm{eV}$ and $\sim 0.2 \,\mathrm{eV}$ with respect to the Fermi energy, respectively. The bands from Gr_T are always shifted away from the Fermi level by a larger energy than those from Gr_B [Fig. 3(b)].

IV. DISCUSSION

Macroscopic electrostatic models have been used to calculate the electrostatic potential and free carrier density in graphene based FETT.^{6,21–23} In these models, the *h*-BN barrier between graphene layers is assumed to have a dielectric constant equal to that of the *h*-BN crystal. However, interfaces can exhibit significantly different dielectric properties compared to the bulk,¹¹ and the dielectric properties of a few-layer-thick *h*-BN barrier sandwiched between two graphene layers need to be revisited. We seek to find the (effective) dielectric constant $\epsilon_{\rm BN}$ of a *h*-BN barrier in a FETT and compare to its bulk value, in order to investigate if the dielectric constant is still a valid physical concept for thin *h*-BN films and to investigate whether $\epsilon_{\rm BN}$ of a *h*-BN barrier is modified by the interfaces with graphene layers.

The dielectric constants of *h*-BN thin films ($\epsilon_{\rm BN}$) sandwiched between graphene layers can be deduced from the electric field inside the *h*-BN thin film ($E_{\rm inside}$) and the electric polarization (*P*) induced by gate voltages,

$$\epsilon_{\rm BN} = \frac{E_{\rm inside} + P/\epsilon_0}{E_{\rm inside}},\tag{2}$$

where E_{inside} is equal to the slope of the self-consistent Kohn-Sham effective potential in the z-direction. E_{inside} is also related to the difference between chemical potentials of graphene layers $\Delta \mu$ by $eE_{\text{inside}}d = \Delta \mu$, where d is the distance between the two graphene layers.

The polarization can be expressed as the summation of centers of Wannier functions according to the modern theory of polarization.^{24,25} In practice we followed the method proposed in Refs. 26 and 27 which extends the Wannier function theory of polarization^{24,25,28,29} to metal-insulator heterostructures.

The hybrid Wannier functions^{30,31}, which are exponentially localized in the z direction but Bloch-like in the x-y plane, were constructed using the parallel-transport method.^{30,31} The first Brillouin zone was sampled by discrete k-points of the type $\mathbf{k} = \mathbf{k}_{\perp} + j \mathbf{b}$, where the vectors \mathbf{k}_{\perp} form a $N_{\perp} \times N_{\perp}$ uniform mesh in the x-y plane, and $\mathbf{b} = (0, 0, 2\pi N_{\parallel}/L)$ is along the z-direction with L the height of the unit cell and N_{\parallel} the number of k-points along the z-direction. The total number of k-points is $N_{\perp}^2 N_{\parallel}$. We used $N_{\perp} = 7$ and $N_{\parallel} = 3$, which are sufficient to converge the polarization.

The matrices $M_{mn}(\mathbf{k}) = \langle u_{m,\mathbf{k}} | u_{n,\mathbf{k}+\mathbf{b}} \rangle$ were constructed where $|u_{m,\mathbf{k}}\rangle$ is periodic under lattice translations and n is the band index; $|u_{m,\mathbf{k}}\rangle$ is normalized such that $\langle u_{m,\mathbf{k}} | u_{m,\mathbf{k}} \rangle = 1$. Singular value decomposition of each M matrix was done utilizing the LAPACK library: $M = U\Sigma V^{\dagger}$ where U and V are complex unitary matrices and Σ is a diagonal matrix with diagonal elements very close to 1 for small \mathbf{b} . A new matrix $\tilde{M} = UV^{\dagger}$ was constructed corresponding to each M matrix, and a global matrix $\Lambda(\mathbf{k}_{\perp}) = \prod_{j=0}^{N_{\parallel}-1} \tilde{M}(\mathbf{k}_{\perp}+j\mathbf{b})$ was then constructed for each \mathbf{k}_{\perp} point. The centers of hybrid Wannier functions were calculated as $z_m = (-L/2\pi) \operatorname{Im}[\ln \lambda_m]$, where the λ_m are the eigenvalues of Λ .

The number of bands considered to construct the hybrid Wannier functions, i.e., the dimension of the M matrices, is equal to four for each graphene or h-BN atomic layer, the number of occupied bands in most of the first



FIG. 4. (a) The polarization of *h*-BN layers in a FETT with a five-layer *h*-BN thin film induced by an gate voltage of $D_g = 6.114 \times 10^{-3} \text{ C/m}^2$. (b) The calculated average relative dielectric permittivity of *h*-BN thin films embedded by graphene layers as a function of the number of *h*-BN atomic layers, and the dashed line denotes the relative dielectric permittivity of bulk *h*-BN.

Brillouin zone except for the small portion near the Kpoint (see Fig. 3). This choice does not affect the calculations of the polarization inside h-BN thin films because the bands near the Fermi energy are contributed by graphene layers.

Four of the resulting hybrid Wannier functions can be assigned to each graphene or *h*-BN layer according to the positions of their centers. Two of them are very close to the atomic plane (within 10^{-3} Å) and the other two are located about 0.4 Å above and beneath the atomic plane respectively. The center of charge for each atomic layer is equal to the average value of the corresponding four Wannier functions. The dipole moment corresponding to each *h*-BN layer was calculated using the shift of the center of charge under an electric field, and the polarization was calculated with the thickness of each *h*-BN layer set to be 3.33 Å.

The calculated polarization of the *h*-BN layer adjacent to the graphene layers is almost the same as h-BN layers deeply inside [see Fig. 4(a)], indicating that interface with graphene layers has little effect on the dielectric properties of h-BN thin films. As a result, the average dielectric constant for *h*-BN thin films embedded by graphene layers is independent of the thickness; see Fig. 4(b). The local dielectric properties are governed by the local chemical bonding, according to Ref. 11. The relation between local dielectric properties and chemical states can be understood in terms of the decomposition of the polarization using Wannier functions. The gate-voltage induced polarization in h-BN is mainly contributed by the Wannier functions with p_z character according to our calculations. The interface between h-BN thin films and graphene involve no chemical bonds breaking or formation, since the interaction among h-BN atomic layers and that between h-BN and graphene layers are of van der Waals type. As a result the h-BN thin film have dielectric properties very similar to the bulk *h*-BN.

The effect of an interface with graphene on the dielectric properties of *h*-BN can be analyzed using the interface charge redistribution, denoted as $\Delta \rho_{\text{intf}}$ and defined as the difference in charge redistribution at the interface with respect to that in the bulk (denoted as $\Delta \rho_{\text{bulk}}$). In practice we used the charge redistribution at the center of the *h*-BN as the bulk, as shown in Fig. 5. Thus one can obtain $\Delta \rho_{\text{intf}}$ by subtracting $\Delta \rho_{\text{bulk}}$ from the total charge redistribution. Because *h*-BN is a wide-band-gap insulator, $\Delta \rho_{\text{intf}}$ should decay exponentially away from the interface. The strength of the interface effect is determined by the amplitude of $\Delta \rho_{\text{intf}}$ near the interface.

An example the charge redistribution in a FETT device with eight-layer h-BN for $D_g = 0.016 \text{ C/m}^2$ is shown in Fig. 5. The difference $\Delta \rho_{\text{intf}}$ is large near the graphene layers, decays quickly into h-BN, and becomes invisible after crossing the first h-BN atomic layers (BN₁ and BN₈ in Fig. 5). The amplitude of $\Delta \rho_{\text{intf}}$ is also presented on a logarithmic scale in the lower panel of Fig. 5. The decay of $\Delta \rho_{\text{intf}}$ into h-BN is approximately exponential. Most importantly, the amplitude of $\Delta \rho_{\text{intf}}$ is about 50 times



FIG. 5. (Color online) Interface charge redistribution $\Delta \rho_{\text{intf}}$ in a FETT device with eight-layer *h*-BN for $D_g = 0.016 \text{ C/m}^2$, where $\Delta \rho_{\text{intf}}$ is the total charge redistribution $\Delta \rho$ minus the bulk charge redistribution $\Delta \rho_{\text{bulk}}$ at the center of *h*-BN; $\Delta \rho_{\text{bulk}}$ is denoted by the patterned rectangle in the top panel. The absolute value of $\Delta \rho_{\text{intf}}$ is plotted on a logarithmic scale in the bottom panel. The positions of the atomic planes of Gr_B, Gr_T and the two interface *h*-BN layers BN₁ and BN₈ are denoted by vertical dashed lines.

smaller than that of $\Delta \rho_{\text{bulk}}$ between BN₁ and BN₈ in Fig. 5: $\Delta \rho_{\text{intf}}$ is very small inside the atomic plane of the first *h*-BN layer. As a result, any interface effect on the dielectric properties of *h*-BN is very weak, which explains why the calculated dielectric constant of *h*-BN in FETT devices is close to the bulk value.

We also compared the *h*-BN thin films with silicon thin films, because the silicon thin films are known to exhibit lower dielectric permittivity than for the corresponding bulk.^{11,28} The purpose is to emphasize that the dielectric properties of thin films are not always expected to be the same with the bulk. The in-plane lattice constant of Si(111) slabs was chosen to be the experimental lattice constant of *fcc*-Si. Dangling bonds on both surfaces are saturated by hydrogen, as shown in the upper panel of Fig. 6, and the Si-H bond length is 1.50 Å after structure optimization.

We plotted in Fig. 6 the shift of each hybrid Wannier functions induced by an electric field of 0.0385 V/Åalong the z-direction, where the electric field was applied using the ESM method using a metal|slab|metal configuration. The hybrid Wannier functions can be divided into two categories according to their polarizability. The hybrid Wannier functions located at canted Si-Si bonds with respect to the z-direction (denoted as • in Fig. 6) exhibit lower polarizability, and they show negligible deviations at the surface. On the other hand, the hybrid Wannier functions located at parallel Si-Si or Si-H bonds with respect to the z-direction (denoted as \circ in Fig. 6) exhibit higher polarizability. We also observed that the hybrid Wannier functions of Si-H bonds at the surface



FIG. 6. (Color online) (Upper panel) The atomic structure of a hydrogen saturated Si(111) thin film with a thickness of about 2 nm. Si and H atoms are represented by large blue and small pink spheres, respectively. Solid lines denote the boundary of the unit cell. (Lower panel) The shift of the hybrid Wannier functions induced by an electric field of 0.0385 V/Å along the z-direction. Red circles (\circ) and blue discs (\bullet) represent hybrid Wannier functions with higher and lower polarizability, respectively.

show a polarizability 12% lower than those corresponding to parallel Si-Si bonds. The 12% lower polarizability of the Si(111) surfaces shown in Fig. 6 is not as severe as reported by previous studies.^{28,32} In those studies, the macroscopic polarization and dielectric constant were obtained after a smoothing procedure. The purpose of the smoothing procedure is to eliminate the dielectric nonlocality, but this procedure reduces the dielectric permittivity of surfaces artificially because the dielectric constant of vacuum is smeared into the surface.

V. SUMMARY

The distribution of free carriers and the band structure of graphene layers in graphene based FETT have been simulated using the DFT+ESM method. The dielectric properties of h-BN thin films sandwiched between graphene layers in FETT were investigated using the theory of microscopic permittivity and found to have a dielectric permittivity close to that of crystalline h-BN. The small amplitude of interface charge redistribution inside the atomic plane of the first h-BN layer proves that the effect of the interface with graphene on the dielectric properties of h-BN is weak.

In this study we have demonstrated the DFT+ESM method as a promising approach to simulate field-effect devices with a planar structure. Once the charge density and effective potential of a field-effect device are self-

consistently obtained, the scattering of transport electrons and electric conductivity can be calculated using scattering theory.

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