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Hardware-Efficient Qubit Control with Single Flux Quantum Pulse Sequences

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The hardware overhead associated with microwave control is a major obstacle to scale-up of superconducting quantum computing. An alternative approach involves irradiation of the qubits with trains of Single Flux Quantum (SFQ) pulses, pulses of voltage whose time integral is precisely equal to the superconducting flux quantum. Here we describe the derivation and numerical validation of compact SFQ pulse sequences in which classical bits are clocked to the qubit at a frequency that is roughly a factor 5 higher than the qubit oscillation frequency, allowing for variable pulse-to-pulse timing. The control sequences are constructed by repeated streaming of short subsequence registers that are designed to suppress leakage out of the computational manifold. With a single global clock, high-fidelity ($> 99.99\%$) control of qubits resonating at over 20 distinct frequencies is possible. SFQ pulses can be stored locally and delivered to the qubits via a proximal classical Josephson digital circuit, offering the possibility of a streamlined, low-footprint classical coprocessor for monitoring errors and feeding back to the qubit array.

I. INTRODUCTION

A fault-tolerant quantum computer will possess a computational power far exceeding that of any classical computer [1], and superconducting integrated circuits are a promising physical platform for the realization of scalable qubits [2]. While the error thresholds of the two-dimensional surface code are within reach [3, 4], quantum error detection involves a massive hardware overhead: estimates suggest that a general-purpose fault-tolerant quantum computer will require millions of physical qubits, far beyond current capabilities. Conventionally, qubit control pulses are generated by single-sideband modulation of a microwave carrier tone; accurate control of both the in-phase and quadrature pulse amplitudes allows arbitrary rotations on the Bloch sphere [5–8]. In order to minimize crosstalk between neighboring qubit channels, it is generally necessary to arrange the qubit array so that devices are biased at a handful of distinct operating frequencies; this approach makes it possible to address a large-scale multiqubit circuit with a small number of carrier tones, resulting in a significant hardware savings [9]. In addition, there have been proposals to recycle pulse waveforms across the qubit array [10]. However, the control waveform that is delivered to the qubit is the convolution of the applied waveform with the transfer function of the wiring in the qubit cryostat, which in general is not well controlled. As wiring transfer functions can vary substantially across the array [11], it is not clear that recycling of control waveforms will allow high-fidelity control. Moreover, the separate high-bandwidth control lines for each qubit channel entail a massive heat load on the millikelvin stage [12]. Finally, the significant latency associated with the round trip from the quantum array to the room-temperature classical coprocessor will limit the performance of any scheme to use high-fidelity projective measurement and feedback to stabilize the qubits [13, 14].

An alternative approach is to control the quantum array using a classical coprocessor that is integrated tightly

with the qubits at the millikelvin stage. Recently we proposed an approach to coherent control involving irradiation of the qubit with trains of quantized flux pulses derived from the Single Flux Quantum (SFQ) digital logic family. Here, classical bits of information are stored as the presence or absence of a phase slip across a Josephson junction in a given clock cycle [15]; the phase slip results in a voltage pulse whose time integral is precisely quantized to $\Phi_0 = h/2e$, the superconducting flux quantum. For typical parameters, SFQ pulse amplitudes are of order 1 mV and pulse durations are around 2 ps, roughly two orders of magnitude shorter than the typical qubit oscillation period. As a result, the SFQ pulse imparts a delta function-like kick to the qubit that induces a coherent rotation in the qubit subspace [16]. In the first experimental implementation of this idea, gate fidelity was limited by spurious quasiparticle generation by the dissipative SFQ pulse driver, which was integrated on the same chip as the qubit circuit [17]. In next-generation devices, we expect that segregation of classical control elements and quantum elements on the two separate chips of a multichip module (MCM) will lead to a significant suppression of quasiparticle poisoning [18]. Ultimately, the fidelity of naive, resonant SFQ pulse trains will be limited by leakage out of the computational subspace, with simulated fidelity around 99.9% for typical values of qubit anharmonicity and gate times around 20 ns. This fidelity is likely insufficient for fault-tolerant operations in a large-scale surface code array.

In order to achieve SFQ-based gates with fidelity well beyond threshold, it is possible to clock SFQ bits to the qubit at a higher rate, allowing for SFQ pulse trains with variable pulse-to-pulse separation. In previous work, genetic algorithms were used to derive optimized pulse sequences that lead to very low leakage and gate fidelities better than 99.99% [18, 19]. However, the register lengths involved were over 250 bits, and the genetic approach provides no intuition as to why a particular sequence yields good performance. While this proof-of-principle demonstration suggests that SFQ control sequences might offer

a low-footprint alternative to conventional microwave sequences, the following questions remain:

- What is the minimum number of classical bits needed to control a qubit to a given level of fidelity?
- Is it possible to achieve high-fidelity control of qubits at different frequencies using a single global clock, as would be ideal for the surface code?

In this work, we describe a method to derive hardware-efficient SFQ control sequences for scalable qubit control: SCALable Leakage Optimized Pulse Sequences (SCALLOPS). The sequences are built up from short subsequences consisting of 35-55 classical bits that are repeatedly streamed to the qubit. Leakage is minimized at the subsequence level; because the subsequences are short, it is possible to perform efficient search over the subsequence space in order to optimize gate fidelity. For SFQ clock frequency roughly a factor 5 greater than the characteristic qubit frequency, we achieve high-fidelity qubit rotations for a large number of discrete qubit frequencies, as required for low-crosstalk control of a large-scale qubit array designed to implement the surface code.

This manuscript is organized as follows. In Section II we present a simple analytic framework for the study of SFQ-based pulse sequences. In Section III we introduce the key features of the SCALLOPS approach: (1) a palindromic sequence construction consisting of symmetric pulse pairs that suppresses errors within the qubit subspace; (2) accurate control of qubits resonating at a number of discrete frequencies by repeated streaming of a short pulse subsequence; and (3) a normal graph algorithm to minimize leakage from the computational subspace at the subsequence level. Finally, in Section IV we summarize our results and conclude our discussion of SCALLOPS.

Throughout this paper, we consider a fixed SFQ clock frequency of 25 GHz, so that SFQ pulses are delivered to the qubit at intervals that are integer multiples of the 40 ps clock period. In addition, we consider transmon qubits with fixed anharmonicity $(\omega_{10} - \omega_{21})/2\pi$ of 250 MHz, where $\omega_{10} \equiv \omega_q$ is the qubit transition frequency and ω_{21} is the transition frequency between the qubit $|1\rangle$ state and the noncomputational $|2\rangle$ state. Finally, for the sake of concreteness we target high fidelity for a single gate, the $Y_{\pi/2}$ rotation; the SCALLOPS approach is readily generalized to arbitrary single-qubit sequences.

II. MODEL FOR SFQ CONTROL OF A TRANSMON QUBIT

A. General Model

We consider a conventional transmon qubit [20] coupled via a small capacitance C_c to an SFQ driver modeled as a time-dependent voltage source $V_{\text{SFQ}}(t)$, as drawn in

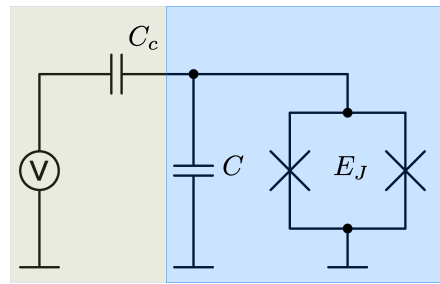


FIG. 1. SFQ drive circuit (yellow) capacitively coupled to a transmon qubit (blue).

Fig. 1. The Hamiltonian of the undriven transmon is written as

$$H_{\text{fr}} = \frac{\hat{Q}^2}{2C'} - E_J \cos \hat{\varphi}, \quad (1)$$

where \hat{Q} and $\hat{\varphi}$ are the charge and phase operators of the transmon, E_J is the transmon Josephson energy, and $C' = C_c + C$ is the sum of the coupling capacitance C_c and the transmon self capacitance C . H_{fr} can be diagonalized in closed form, with the resulting energy eigenfunctions $\langle \varphi | \gamma \rangle$ and energies E_γ represented by the Mathieu functions and coefficients.

Interaction between the transmon and the SFQ pulse driver adds the following term to the Hamiltonian:

$$H_{\text{SFQ}}(t) = \frac{C_c}{C'} V_{\text{SFQ}}(t) \hat{Q}, \quad (2)$$

where the SFQ pulse $V_{\text{SFQ}}(t)$ satisfies the condition $\int_{-\infty}^{\infty} V_{\text{SFQ}}(t) dt = \Phi_0$. Since the pulse width (around 2 ps) is much less than the Larmor period (around 200 ps) of the transmon, we model the SFQ pulse as a Dirac delta function: $V_{\text{SFQ}}(t) = \Phi_0 \delta(t)$. The charge operator can be constructed in the basis of $|\gamma\rangle$ once H_{fr} is diagonalized. The free evolution of the transmon then becomes

$$U_{\text{fr}}(t) = \exp \left(-\frac{i}{\hbar} \sum |\gamma\rangle \langle \gamma| E_\gamma t \right). \quad (3)$$

The time evolution operator for a transmon subjected to an SFQ pulse is [16]

$$U_{\text{SFQ}} = \exp \left(-i \Phi_0 (C_c / C') \hat{Q} \right). \quad (4)$$

B. Three-level Model

It is advantageous to restrict the size of the transmon Hilbert space in order to accelerate the search for high-fidelity pulse sequences. Since leakage outside the computational subspace is dominated by population of the first noncomputational state $|2\rangle$, we truncate the transmon to a three-level qutrit; subsequent sequence validation will be performed on a more complete model of the transmon

consisting of 7 states. Within the three-level subspace, the operators H_{fr} and $H_{\text{SFQ}}(t)$ take the form

$$H_{\text{fr}}^{(3)} = \frac{\hbar\omega_q}{2}\hat{\Sigma}_z, \quad H_{\text{SFQ}}^{(3)}(t) = \frac{\hbar\omega_y(t)}{2}\hat{\Sigma}_y; \quad (5)$$

$$\hat{\Sigma}_z = \begin{bmatrix} 0 & 0 & 0 \\ 0 & 2 & 0 \\ 0 & 0 & 4-2\eta \end{bmatrix}, \quad \hat{\Sigma}_y = i \begin{bmatrix} 0 & -1 & 0 \\ 1 & 0 & -\lambda \\ 0 & \lambda & 0 \end{bmatrix}. \quad (6)$$

Here, we have introduced the notation $\omega_y(t) = -(2V(t)/\hbar)(C_c/C')\langle 1|\hat{Q}|0\rangle$; $\eta = 1 - \omega_{21}/\omega_q$ represents the fractional anharmonicity of the transmon; and $\lambda = \langle 2|\hat{Q}|1\rangle / \langle 1|\hat{Q}|0\rangle$.

Next, we derive the three-level matrix form for the free evolution of the transmon and for the evolution of the transmon subjected to a single SFQ pulse. The free evolution is given by the diagonal matrix $U_{\text{fr}}^{(3)}(t) = \exp(-i\omega_q t \hat{\Sigma}_z/2)$. In the qubit subspace, the effect of $U_{\text{fr}}^{(3)}$ is clearly a precession at the rate ω_q . For the time evolution under a single SFQ pulse, we can write

$$U_{\text{SFQ}}^{(3)} = \exp\left(-\frac{i\hat{\Sigma}_y}{2} \int_{-\infty}^{\infty} \omega_y(t) dt\right) = \exp\left(\frac{-i\delta\theta\hat{\Sigma}_y}{2}\right), \quad (7)$$

where

$$\delta\theta = \int_{-\infty}^{\infty} \omega_y(t) dt = (2\Phi_0/\hbar)(C_c/C')\langle 1|\hat{Q}|0\rangle \quad (8)$$

is the tip angle associated with a single SFQ pulse. Using the Cayley-Hamilton theorem on $\hat{\Sigma}_y$, we obtain the property $\hat{\Sigma}_y^3 = (\lambda^2 + 1)\hat{\Sigma}_y$, which yields $\hat{\Sigma}_y^n = (\lambda^2 + 1)^{\frac{n-2}{2}}\hat{\Sigma}_y^2$ for even n and $\hat{\Sigma}_y^n = (\lambda^2 + 1)^{\frac{n-1}{2}}\hat{\Sigma}_y$ for odd n . We then expand and regroup Eq. (7):

$$U_{\text{SFQ}}^{(3)} = \hat{1} + \sum_{\text{even}, n \geq 2}^{\infty} \frac{(\lambda^2 + 1)^{\frac{n-2}{2}} (-i\delta\theta/2)^n}{n!} \hat{\Sigma}_y^2 + \sum_{\text{odd}, n \geq 1}^{\infty} \frac{(\lambda^2 + 1)^{\frac{n-1}{2}} (-i\delta\theta/2)^n}{n!} \hat{\Sigma}_y. \quad (9)$$

The two sums in Eq. (9) yield

$$U_{\text{SFQ}}^{(3)} = \frac{1}{\kappa^2} \times \begin{bmatrix} \lambda^2 + \cos(\kappa\delta\theta/2) & -\kappa \sin(\kappa\delta\theta/2) & 2\lambda \sin^2(\kappa\delta\theta/4) \\ \kappa \sin(\kappa\delta\theta/2) & \kappa^2 \cos(\kappa\delta\theta/2) & -\kappa\lambda \sin(\kappa\delta\theta/2) \\ 2\lambda \sin^2(\kappa\delta\theta/4) & \kappa\lambda \sin(\kappa\delta\theta/2) & 1 + \lambda^2 \cos(\kappa\delta\theta/2) \end{bmatrix}, \quad (10)$$

where $\kappa = \sqrt{\lambda^2 + 1}$.

To see the effect of a single SFQ pulse, we compare this time evolution to a y -rotation by angle $\delta\theta$ in the qubit subspace:

$$Y_{\delta\theta} = \begin{bmatrix} \cos(\delta\theta/2) & -\sin(\delta\theta/2) \\ \sin(\delta\theta/2) & \cos(\delta\theta/2) \end{bmatrix}. \quad (11)$$

We observe that: (1) within the three-level model, the SFQ pulse provides a rotation in the qubit subspace that is slightly smaller than $\delta\theta$; and (2) leakage from state $|1\rangle$ to state $|2\rangle$ is first order in $\delta\theta$, while leakage from state $|0\rangle$ to state $|2\rangle$ is second order in $\delta\theta$.

C. Pulse Sequences and Gate Fidelity

The above analysis shows that it is impossible to perform coherent qubit rotations with a single SFQ pulse without incurring significant excitation of noncomputational states. However, composite sequences consisting of multiple SFQ pulses spaced by appropriate time intervals can achieve low leakage and high gate fidelity. More specifically, we consider a high-speed SFQ clock that delivers pulses to the transmon according to a vector of binary variables \mathbf{S} , where $S_i = 0$ if no SFQ pulse is applied on the i^{th} clock edge and $S_i = 1$ if an SFQ pulse is applied. Using these expressions, the total time evolution operator of the gate U_G , time ordered in terms of clock edges, can be written as

$$U_G = \mathcal{T} \left\{ \prod_i^{N_c} (\delta_{S_i 1} U_{\text{fr}}(T_c) U_{\text{SFQ}} + \delta_{S_i 0} U_{\text{fr}}(T_c)) \right\}. \quad (12)$$

Here, N_c is the number of clock cycles in the sequence and T_c is the clock period.

We evaluate the fidelity of the gate U_G as in [21];

$$F_{\text{avg}} = \frac{1}{6} \sum_{|\alpha\rangle \in \mathcal{V}} \left| \langle \alpha | U_G^\dagger Y_{\pi/2} |\alpha\rangle \right|^2, \quad (13)$$

where the summation runs over the six states \mathcal{V} aligned along the cardinal directions of the Bloch sphere

$$\begin{aligned} |x_{\pm}\rangle &= \frac{|0\rangle \pm |1\rangle}{\sqrt{2}}, \\ |y_{\pm}\rangle &= \frac{|0\rangle \pm i|1\rangle}{\sqrt{2}}, \\ |z_{+}\rangle &= |0\rangle, \quad |z_{-}\rangle = |1\rangle; \end{aligned} \quad (14)$$

and where the gate $Y_{\pi/2}$ gate is represented by the following matrix in the qubit subspace:

$$Y_{\pi/2} = \frac{1}{\sqrt{2}} (|0\rangle\langle 0| + |1\rangle\langle 1| + |1\rangle\langle 0| - |0\rangle\langle 1|). \quad (15)$$

The crux of the problem then becomes proper selection of \mathbf{S} so that U_G becomes a high-fidelity $Y_{\pi/2}$ gate.

III. SCALLOP SEQUENCES

A. Symmetric SFQ Pulse Pairs

In the simplest scheme for SFQ-based coherent control, one applies a regular train of SFQ pulses that is

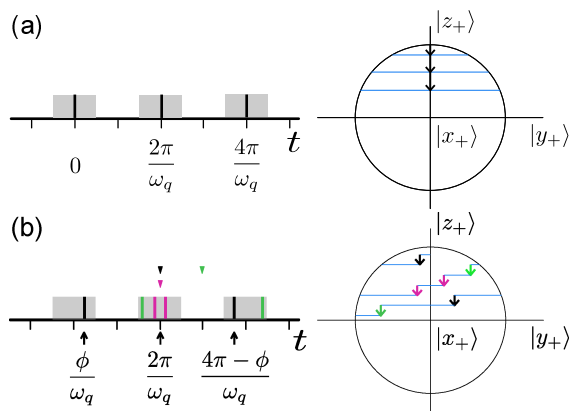


FIG. 2. SFQ pulse sequences. The shaded regions denote time windows in which SFQ pulses can induce a positive y -rotation. The right-hand panels depict the qubit trajectory on the front hemisphere ($\langle z \rangle > 0$) of the Bloch sphere. (a) Resonant pulse sequence. Here, pulses are applied at regular intervals of $2\pi/\omega_q$. The SFQ pulses induce small y -rotations spaced by free precession for a full period. (b) Symmetric pulse pairs. Three symmetric pairs are shown, denoted by the different colors. For the black pair, one pulse is delivered at ϕ/ω_q and the other at $(4\pi - \phi)/\omega_q$. For each pulse pair, the pulses are symmetric around $m\pi/\omega_q$ (arrows). It is possible to overlay multiple symmetric pairs to induce larger qubit rotations.

synchronized to the qubit oscillation period $T_q = 2\pi/\omega_q$ (Fig. 2a); this is the situation considered in [16]. In this method, because $U_{\text{fr}}(T_c) = \hat{1}$ in the qubit subspace, only the desired y -rotations can be induced by $U_{\text{fr}}(T_c)$ and U_{SFQ} ; however, leakage out of the qubit subspace can be significant. To attain higher fidelity, it is necessary to clock SFQ pulses to the qubit at a higher rate and to employ more sophisticated sequences. When the qubit is no longer resonant with the SFQ clock, $U_{\text{fr}}(T_c) \neq \hat{1}$, and the time evolution of an arbitrary sequence of $U_{\text{fr}}(T_c)$ and U_{SFQ} is generally not confined to y -rotations. To ensure high overlap with the target y -rotation, we construct a composite sequence built up from *symmetric pairs* of SFQ pulses delivered to the qubit at times ϕ/ω_q and $(2m\pi - \phi)/\omega_q$ for some integer m , as shown in Fig. 2b. The pulses of the symmetric pair occur symmetrically with respect to time $mT_q/2$. We represent the symmetric pair by the tuple notation (m, ϕ) . As an example, we can write the resonant sequence in terms of symmetric pairs: the first and last pulses form the pair $(N_q, 0)$, where N_q is the number of qubit cycles in the sequence; the second and penultimate pulses form the pair $(N_q, 2\pi)$, etc. In general, the sequence can be described as the set of symmetric pairs $(N_q, 2\pi k)$ for each k between 0 and $N_q/2$.

To see that application of symmetric pulse pairs has the net effect of a y -rotation within the qubit subspace, we inspect the time evolution operator $U_{(m, \phi)}$ associated

with symmetric pair (m, ϕ) :

$$U_{(m, \phi)} = U_{\text{fr}}(\phi/\omega_q) U_{\text{SFQ}} U_{\text{fr}}((2m\pi - 2\phi)/\omega_q) \times U_{\text{SFQ}} U_{\text{fr}}(\phi/\omega_q). \quad (16)$$

We substitute U_{SFQ} from Eq. (10), expand to the first order in $\delta\theta$, and obtain

$$U_{(m, \phi)} = \begin{bmatrix} 1 & -\cos(\phi)\delta\theta & 0 \\ \cos(\phi)\delta\theta & 1 & -\lambda\mu\delta\theta \\ 0 & \lambda\mu\delta\theta & \dots \end{bmatrix} + \mathcal{O}(\delta\theta^2), \quad (17)$$

where

$$\mu = \exp\left(\frac{im\pi(2\omega_q + \omega_{21})}{\omega_q}\right) \cos\left(\frac{\omega_{21}}{\omega_q}(m\pi - \phi)\right). \quad (18)$$

We observe that, to first order in $\delta\theta$, $U_{(m, \phi)}$ is indeed a y -rotation in the qubit subspace. Moreover, the dependence of leakage on the timing of the symmetric pair through ϕ provides a degree of freedom that will enable us to tailor subsequences in order to minimize leakage errors, as we discuss in Sec. III C. We remark that although it is tempting to set $\mu = 0$ by appropriate selection of ϕ and thereby eliminate the 1-2 transition, the 0-1 transition will become very weak as a side effect. In fact, there is an analogous composite microwave pulse method that exploits a restricted form of this idea corresponding to $m = 1$ [22]; however, the gate performance is no better than that of naive Gaussian pulses.

As we shall see below, the construct of symmetric pairs becomes particularly advantageous when it is extended to the case of multiple symmetric pairs (m_i, ϕ_i) applied at times ϕ_i/ω_q and $(2m_i\pi - \phi_i)/\omega_q$ for $i \in \mathbb{N}$, as shown in Fig. 2b. We note that the pulse pairs do interfere with each other because they generally do not commute; however, the resulting error is acceptably small for practical choices of $\delta\theta$.

B. Control of Qubits at Multiple Frequencies

In general, the qubit oscillation period will not be commensurate with the SFQ clock, so that the optimal delivery times of the symmetric pairs will not exactly coincide with SFQ clock edges. As a result, it is necessary to round a symmetric pair to a particular pair of clock edges n_i and n_j . We first note that n_i and n_j preserve the symmetry precisely if the times at which the pulses are applied are symmetric with respect to $mT_q/2$ for some integer m :

$$\frac{1}{2} \left(\frac{n_i}{N_c} \cdot \frac{2\pi N_q}{\omega_q} + \frac{n_j}{N_c} \cdot \frac{2\pi N_q}{\omega_q} \right) = mT_q/2, \quad (19)$$

where N_c is the number of clock cycles. This condition is equivalent to the expression

$$A_{\text{sym}} = \left| \left(\frac{n_i}{N_c} N_q \right) \bmod 1 + \left(\frac{n_j}{N_c} N_q \right) \bmod 1 - 1 \right| = 0, \quad (20)$$

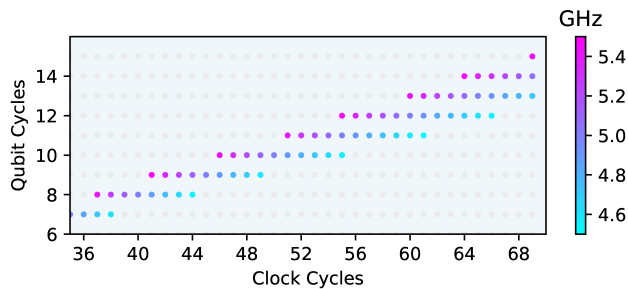


FIG. 3. Map of qubit frequencies permitting high-fidelity control. Each grid point represents a qubit oscillation frequency that satisfies Eq. (21). The grid points highlighted with the color scale span qubit frequencies from 4.5 to 5.5 GHz, assuming an SFQ clock frequency of 25 GHz.

where A_{sym} is now a measure of the violation of symmetry due to mismatch between the SFQ clock and the qubit oscillation period. We find empirically that coherent pulse errors are acceptably small for pulse pairs delivered at times such that $A_{\text{sym}} < 0.05$. In the following, pulse pairs that are termed symmetric are understood to satisfy this condition.

The delivery of SFQ pulses to the qubit as symmetric pairs constrains the time evolution to the desired y -rotation; however, it is not obvious how to control multiple qubits resonating at different frequencies, as demanded by the surface code. For a qubit frequency that is not a subharmonic of the SFQ clock frequency, the concern is that mismatch between the qubit oscillation period and the SFQ clock will lead to phase error, as the precession of the qubit during the gate is not an integer number of qubit cycles.

To avoid these phase errors, the key is to tune the qubit frequency such that the total gate time T_g corresponds to both an integer number N_c of clock cycles T_c and an integer number N_q of qubit cycles T_q , so that $T_g = N_c T_c = N_q T_q$. This relation translates into the following frequency matching condition:

$$\frac{N_q}{\omega_q} = \frac{N_c}{\omega_c}, \quad (21)$$

where $\omega_c = 2\pi f_c$ is the angular frequency of the clock. We can use Fig. 3 to find frequencies that satisfy Eq. (21). In this diagram, each grid point represents a qubit frequency (shown in color scale) determined from the above matching condition. Again, we consider a 25 GHz SFQ clock; for a small range of frequencies around each of the “magic” qubit operating points given by Eq. (21), accurate qubit control is possible.

From frequency-matching relation (21), it is clear that longer gate times will permit high-fidelity control of a larger number of distinct qubit frequencies. However, the number of register bits needed to describe the pulse sequence can be drastically reduced by the repeated streaming of high-fidelity *subsequences*. This strategy leads to compact registers that are efficient to implement

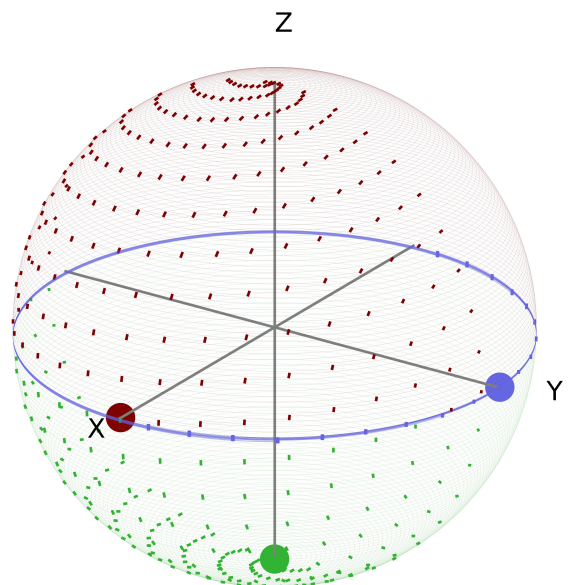


FIG. 4. Basic SCALLOP sequence. We show the trajectory on the Bloch sphere for qubits initialized in states $|x_+\rangle$ (green), $|y_+\rangle$ (purple), and $|z_+\rangle$ (red). Under the influence of the pulse sequence, the state $|x_+\rangle$ is rotated to $|z_-\rangle$; the state $|y_+\rangle$ undergoes small oscillations on the equator but returns to itself; and the state $|z_+\rangle$ moves to $|x_+\rangle$, as expected for a $Y_{\pi/2}$ gate.

in hardware, and it provides a desirable periodic suppression of leakage as a side effect, as we discuss below in Sec. III D.

In more detail, the length of the subsequence involves a trade-off between the number of register bits and the performance of the subsequence. If the length is too short, the search space will be too restricted to contain high-fidelity subsequences, and the number of qubit frequencies we can control will be reduced (see Fig. 3). In our simulations, we find a good balance for subsequences consisting of 35 – 55 bits. The number of subsequence repetitions, and thus the overall length of the gate, is set by the size of the coherent rotation $\delta\theta$ imparted to the qubit per SFQ pulse. Since the minimum gate time scales with tip angle as $T_g \propto T_c/(2\delta\theta)$, it is tempting to reduce the gate time by increasing $\delta\theta$; for large tip angle, however, errors that are second order in $\delta\theta$ will become significant. In contrast, small tip angle leads to long gate time. Empirically, we find that $\delta\theta \approx 0.03$ is optimal, corresponding to a reasonable coupling capacitance from the SFQ driver circuit to the qubit island of order 100 aF for typical transmon parameters. For the simulations described here, we target $Y_{\pi/2}$ gate time around 12 ns.

The choice of clock frequency involves a compromise to avoid leakage while maintaining hardware efficiency. For clock frequencies below around 10 GHz, very large tip angles per SFQ pulse are needed in order to maintain short sequence lengths. These pulses induce significant

leakage to states beyond $|2\rangle$, leading to errors that can't be mitigated by the SCALLOP approach. On the other hand, leakage minimization places a lower limit around $\pi/(\omega_{10} - \omega_{21})$ on the duration of the subsequences. For very high clock frequency, the subsequences will therefore require a large number of register bits, making them less hardware efficient. In our simulations, we find that 25 GHz is a good compromise.

With this frequency matching condition and approach to hardware optimization, we can immediately construct some *basic subsequences* as follows. Given the number of clock cycles N'_c and qubit cycles N'_q in a subsequence, for each clock cycle $i \in [0, N'_c]$ we apply an SFQ pulse on a given clock edge provided the pulse induces a rotation in the positive y -direction. We then repeat the subsequence an appropriate number of times to achieve the target rotation. Explicitly, we deliver an SFQ pulse to the qubit on the k^{th} clock edge of the subsequence provided the following condition is fulfilled:

$$\left(N'_q \cdot \frac{k}{N'_c}\right) \bmod 1 \leq 1/4 \text{ or } \geq 3/4. \quad (22)$$

This class of subsequences is expected to yield reasonably high fidelity because it has a palindrome structure, which implies that pulses are delivered to the qubit as symmetric pairs. For example, the first and last pulses form the pair $(N_q, 0)$; the second and penultimate pulses form the pair $(N_q, \omega_q T_c)$, etc. In general, the sequence contains a pair $(N'_q, k\omega_q T_c)$ for each k between 0 and $N'_c/2$ that satisfies (22).

As an example, we simulate a sequence built from 10 repetitions of a basic subsequence using $N'_c = 39$ and $N'_q = 8$; a plot of the qubit trajectory on the Bloch sphere is shown in Fig. 4 for a 5.12781 GHz qubit initialized along the $+x$ (green), $+y$ (purple), and $+z$ (red) directions. Here, the tip angle $\delta\theta = 0.0126$ is chosen to achieve the $Y_{\pi/2}$ rotation in 390 clock steps. Assuming a qubit anharmonicity of 250 MHz and a 25 GHz SFQ clock frequency, this sequence achieves fidelity of 99.9% in under 16 ns. Although this scheme for constructing basic subsequences demonstrates the possibility of controlling multiple qubit frequencies using a single global clock, it is by no means optimal, as the achieved fidelity is rather modest. The dominant source of infidelity is leakage from the computational subspace. In the following subsection, we describe an approach to suppress this leakage.

C. Leakage Suppression

At the core of SCALLOPS is the optimization algorithm that eliminates leakage from the computational subspace. Starting with a *basic subsequence* of the type described in Sec. III B, we need to flip bits in order to suppress leakage while preserving the target rotation in the qubit subspace. The major difficulty in subsequence

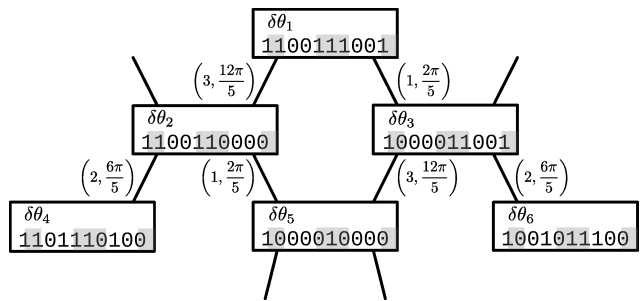


FIG. 5. Subsequence Graph. Each vertex represents a 10-clock cycle (2-qubit cycle) subsequence with a distinct tip angle $\delta\theta_i$ in the qubit subspace (we consider a 25 GHz clock and a 5 GHz qubit). The shaded regions denote windows in which positive y -rotations can be induced by the application of SFQ pulses. Vertices that are connected differ by a single symmetric pair (m, ϕ) , which labels the connection. For example, the subsequence at the top of the graph differs from its left neighbor by the pair $(3, 12\pi/5)$, corresponding to pulses applied on the sixth and ninth clock edges following initiation of the sequence (clock edge zero).

optimization is that bit flips that reduce leakage will generally disrupt the rotation in the qubit subspace. This problem is analogous to solving a Rubik's cube: when the cube mismatched at the top layer, a naive set of operations to complete the top layer will generally disrupt the other layers that are already matched. This difficulty can be circumvented by using a sequence of operations whose net effect is felt only at the top layer. We can solve the qubit control problem analogously: the corresponding sequence of operations is to flip a symmetric pair of bits in the subsequence and to scale the tip angle $\delta\theta$ to preserve rotation in the qubit subspace. While this latter step might seem dubious, given that $\delta\theta$ is fixed by the geometric coupling of the SFQ driver to the qubit, we will show that for a given qubit frequency satisfying the matching condition Eq. (21) there exists a high density of high-fidelity subsequences in the space of tip angles $\delta\theta$. Our strategy will be to allow $\delta\theta$ to vary as we search for a cluster of high-fidelity, low-leakage subsequences. Then we will select those subsequences that achieve highest fidelity for the specific value of $\delta\theta$ dictated by the available hardware.

More formally, we can describe this method in terms of a subsequence graph $G = (V, E)$, where the vertices V represent individual SFQ subsequences with their optimal tip angles $\delta\theta$ and the connections E link subsequences that are separated by a single symmetric pair of bit flips. Explicitly,

- Each vertex V is described by a subsequence bit pattern \mathbf{S} and its optimal tip angle $\delta\theta_{\text{opt}} = \text{argmax}_{\delta\theta} F_{\text{avg}}$.
- Each connection E links subsequences (\mathbf{S}, \mathbf{L}) that differ by a single symmetric pair (m, ϕ) .

We define V and E in this way with the goal of separating

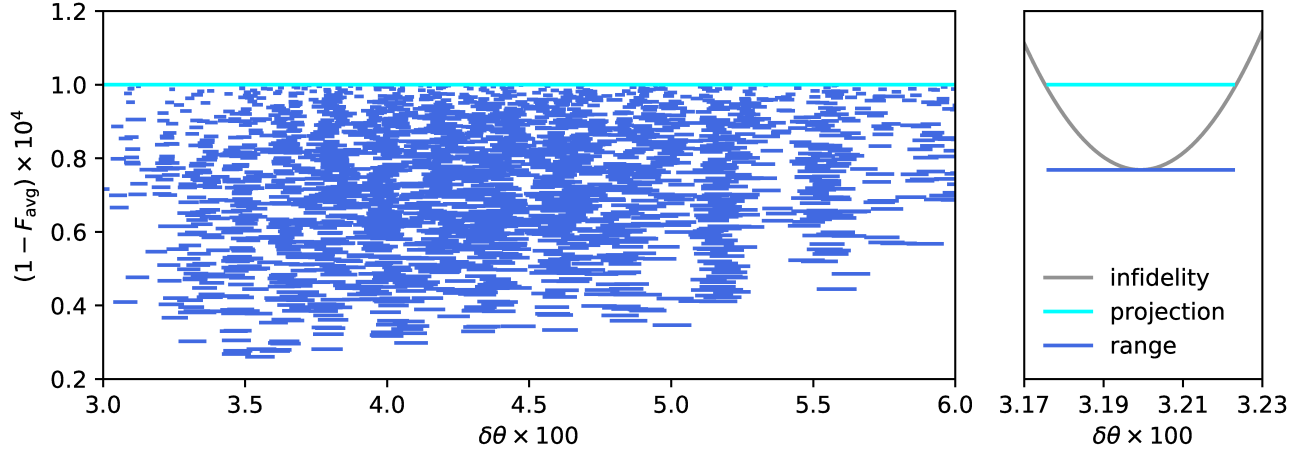


FIG. 6. Gate infidelity versus tip angle $\delta\theta$. Each horizontal bar represents a unique subsequence. The bars are centered horizontally at the optimal tip angle $\delta\theta_{\text{opt}}$, and the vertical position of the bars represents the minimum subsequence infidelity. The horizontal extent of each bar denotes the range of $\delta\theta$ over which the infidelity of the subsequence remains below 10^{-4} (see inset). The cyan trace is the projection of all subsequences onto the line $1 - F_{\text{avg}} = 10^{-4}$; these segments merge into a nearly continuous line that spans the range of tip angles from 0.03 to 0.06 rad.

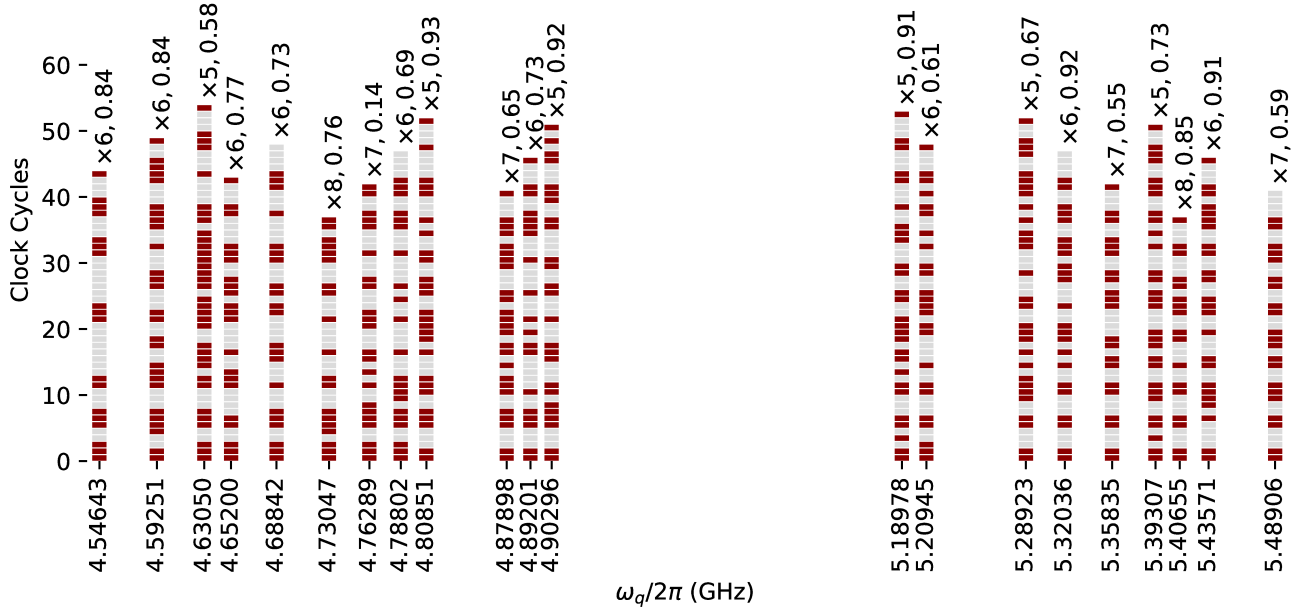


FIG. 7. A collection of SCALLOP subsequences. The subsequences are labeled below with the frequency of the target qubit and above with the achieved gate fidelity (in units of 10^{-4}) and number of repetitions required to achieve the $Y_{\pi/2}$ gate. Here, time flows upward; red (grey) bars correspond to clock cycles during which SFQ pulses are applied (omitted). The SCALLOP sequences span 21 qubit frequencies and share a fixed tip angle $\delta\theta = 0.032$. The frequency spacing of the subsequences is slightly adjusted for improved readability.

control in the qubit subspace from leakage elimination: navigation through the subsequence graph G preserves rotation in the qubit subspace, but movement from vertex to vertex can change leakage out of the computational subspace substantially, as one can see from Eq. (18). A trivial example of the subsequence graph is shown in Fig.

5.

With this definition of the subsequence graph G , we describe a simple procedure to find high-fidelity subsequences. We first construct a *basic subsequence* \mathbf{S} as defined in Sec. III B. This subsequence serves as the entrance point to the subsequence graph. We then explore

all vertices adjacent to \mathbf{S} and greedily move to the vertex with the highest fidelity. We repeat this greedy move until we reach a local fidelity maximum. This typically takes only 5-10 steps. In our simulations, 5-8 repetitions of such high-fidelity subsequences will yield gates with fidelity greater than 99.99% in a total sequence time under 12 ns.

The subsequences generated by the algorithm described above are not yet sufficient for experimental implementation because we have allowed the tip angle per SFQ pulse to vary during our search. In practice, the tip angle is determined by the coupling capacitance of the SFQ driver to the transmon qubit and cannot be exquisitely controlled during fabrication, or varied *in situ* following fabrication.

The solution to the problem is to explore a larger region of the subsequence graph and to identify a large ensemble of high-fidelity candidate subsequences corresponding to a range of optimal tip angle $\delta\theta_{\text{opt}}$. For each of these subsequences, high-fidelity rotations (say, infidelity under 10^{-4}) are achieved over a range of $\delta\theta$, so that it is straightforward to identify from this ensemble specific subsequences that yield high fidelity for a fixed $\delta\theta$. More specifically, we ignore all vertices with fidelity lower than 99.99% and perform a standard breadth-first search to traverse the remaining vertices of the graph, leading to a set of characterized subsequences which we call the subsequence *neighborhood*.

In Fig. 6 we plot the simulated infidelities achieved versus the tip angle $\delta\theta$ for a subsequence neighborhood associated with a 4.65200 GHz qubit. For each individual subsequence in the neighborhood, high fidelity is reached for only a small range of tip angles around the optimal value. However, given a fixed value of $\delta\theta$, numerous subsequences are available that achieve gate fidelity well beyond the target of 99.99%. This is true for SFQ tip angle spanning a broad range from 0.03 to 0.06, which is more than enough to accommodate any inaccuracy in the design of the coupling capacitance between the SFQ driver and the transmon.

D. Sequence Verification

We have performed the above-described neighborhood search for 21 different frequencies satisfying the matching condition given by Eq. (21). The result is shown in Fig. 7. While a 3-level model of the transmon was used to derive the sequences, the presented fidelities were calculated for a model incorporating 7 energy levels.

In Fig. 8, we examine leakage into the noncomputational states $|2\rangle$, $|3\rangle$, $|4\rangle$, and $|5\rangle$ for the SCALLOP sequence corresponding to the 4.89201 GHz qubit. We observe that the dominant leakage into state $|2\rangle$ is roughly bounded at 10^{-2} for initial qubit states spanning the cardinal points on the Bloch sphere. Moreover, as the qubit state approaches $|0\rangle$, we see that leakage into $|2\rangle$ is particularly low, as demonstrated in the curves corresponding

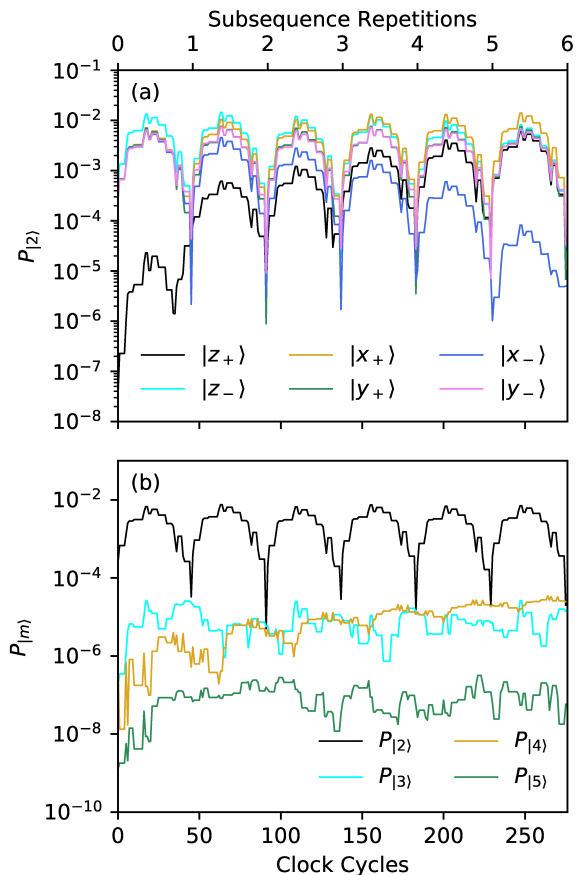


FIG. 8. Leakage into noncomputational states for the sequence of Fig. 6 corresponding to the 4.89201 GHz qubit. The sequence involves 6 repetitions of a subsequence consisting of 46 bits. (a) Population of $|2\rangle$ for initial qubit states aligned along the cardinal directions of the Bloch sphere. (b) Population of states $|2\rangle$, $|3\rangle$, $|4\rangle$, and $|5\rangle$ averaged over the same initial qubit states.

to initial states $|z_+\rangle$ and $|x_-\rangle$. While the population of state $|2\rangle$ can approach 10^{-2} toward the middle of the subsequence, the population always drops below 10^{-4} at the completion of each subsequence repetition, as the subsequences are explicitly constructed to minimize leakage from the qubit subspace. The population of states $|3\rangle$ and $|4\rangle$ is well below 10^{-4} throughout, while states $|5\rangle$ and higher have negligible populations.

Finally, in Fig. 9 we simulate the effect of qubit parameter variation on SCALLOP gate fidelity. Error from frequency drift can be modeled as an ideal gate followed by a small precession: $U_{\text{fr}}(\delta\omega T_g/\omega_q)Y_{\pi/2}$. From Eq. (13), the infidelity of this gate is then approximately $(\delta\omega T_g)^2/6$. For gate fidelity to degrade by 10^{-4} , the qubit frequency drift $\delta\omega/2\pi$ must reach about 300 kHz, given a gate time of 12 ns. This naive estimate is in qualitative agreement with the full simulation results in Fig. 9a; note that based on the above argument, we expect microwave-based qubit gates to display similar sensitivity to qubit

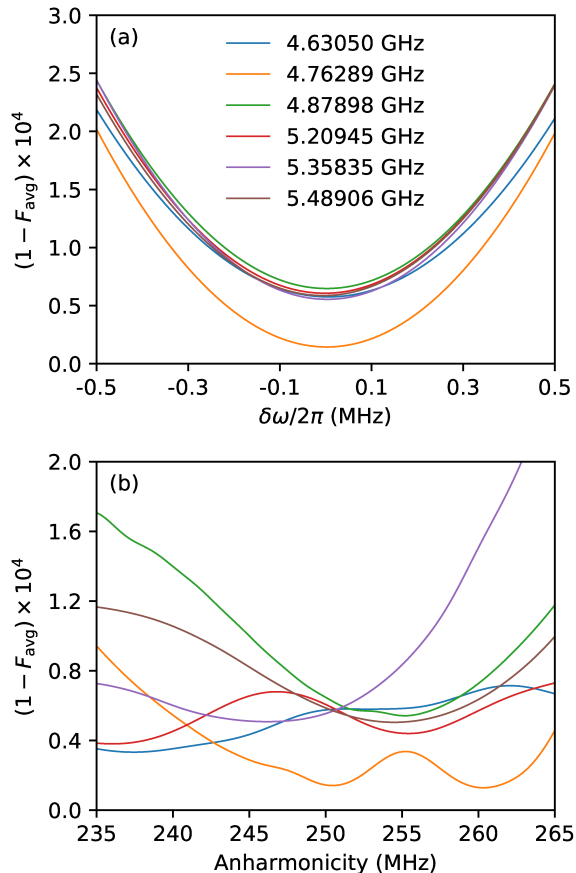


FIG. 9. Sensitivity of gate fidelity to variation in (a) qubit frequency and (b) anharmonicity. The simulation is run for six different qubit frequencies.

frequency drift. In Fig. 9b we see that SCALLOP gate

fidelity is relatively insensitive to variation in qubit anharmonicity. In a practical system, the anharmonicity of each qubit would be calibrated upon system bringup. As anharmonicity is set by the transmon charging energy, it is not expected to fluctuate in time.

IV. CONCLUSION

We have performed numerical simulations to demonstrate coherent qubit control across multiple frequencies using irradiation with classical bits derived from the SFQ logic family. Using a single global clock at 25 GHz to stream pulses from compact registers consisting of 35-55 bits, we achieve gate fidelity better than 99.99% across 21 qubit frequencies spanning the range from 4.5 to 5.5 GHz. The control subsequences are readily amenable to storage in compact SFQ-based shift registers, as outlined in [18]. We have described an intuitive, efficient method for the derivation of high-fidelity SFQ-based pulse sequences that is readily adapted to arbitrary single-qubit gates. The SCALLOPS method is robust in the sense that large imprecision in the tip angle per SFQ pulse is readily accommodated by appropriate variation in the subsequence bitstream. The control approach is immune to wiring parasitics and offers the possibility for tight integration of a large-scale quantum array with a proximal classical coprocessor for the purposes of reducing system footprint, wiring heatload, and control latency.

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